

MIC94070/71/72/73

High Side Power Switches

General Description

The MIC94070-73 are high-side load switches designed for operation between 1.7V to 5.5V. The devices contain a low on-resistance P-channel MOSFET that supports 1.2A of continuous current. The MIC94071 and MIC94073 feature an active load discharge circuit which insures capacitive loads retain no charge when the main switch is in an OFF state.

MIC94070-71 feature rapid turn on while MIC94072-73 provide a slew rate controlled Soft-Start turn-on of 800µs (typical) to prevent in-rush current from glitching supply rails.

An active pull-down on the enable input keeps MIC94070-73 in a default OFF state until the EN pin is pulled to a high level. Built-in level shift circuitry allows low voltage logic signals to switch higher supply voltages, or vice versa; high level logic signals can control low level voltages.

MIC94070-73's operating voltage range makes them suitable for 1-cell Lithium ion and 2- to 3-cell NiMH/NiCad/Alkaline powered systems, as well as all 5V applications. Their low operating current of 2μ A and low shutdown current of $<1\mu$ A maximize battery life.

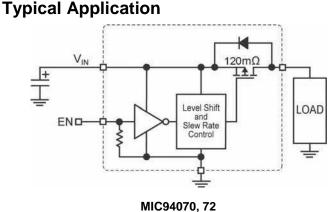
Data sheets and support documentation can be found on Micrel's web site at: www.micrel.com.

Features

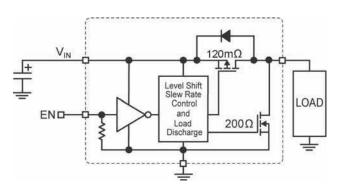
- 1.7V to 5.5V input voltage range
- 1.2A continuous operating current
- 3A pulse current
- 120mΩ R_{DSON} (typical)
- Built-in level shift for control logic; can be operated by 1.5V logic.
- Low 2µA quiescent current
- Soft-Start: MIC94072/73
- Micro-power shutdown <1µA
- Load discharge circuit: MIC94071, MIC94073
- Space saving 1.2mm × 1.6mm Thin MLF[®] package

Applications

- Load switch in portable applications:
 - Cellular phones
 - PDAs
 - MP3 players
 - Digital Cameras
 - Portable instrumentation
- Battery switch-over circuits
- Level translator



Load Switch Application



MIC94071, 73 Load Switch with Capacitive Load Discharge

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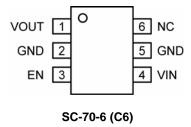
Ordering Information

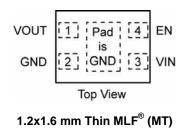
Part Number		Part Marking ⁽¹⁾		Fast	Soft-	Load	Package
Standard	Pb-Free	Standard	Pb-Free	Turn On	Start	Discharge	Fackage
—	MIC94070YC6	—	70P	•			SC-70-6
	MIC94071YC6	—	71P	•		•	SC-70-6
	MIC94072YC6	—	72P		٠		SC-70-6
	MIC94073YC6	—	73P		٠	•	SC-70-6
	MIC94070YMT	—	P70	•			1.2mm x 1.6mm Thin MLF [®]
_	MIC94071YMT	—	P71	•		•	1.2mm x 1.6mm Thin MLF®
—	MIC94072YMT	—	P72		٠		1.2mm x 1.6mm Thin MLF [®]
—	MIC94073YMT	—	P73		•	•	1.2mm x 1.6mm Thin $MLF^{\ensuremath{\mathbb{R}}}$

Notes

1. Under-bar symbol on SC-70 Pb-free packages may not be to scale.

Pin Configuration





Pin Description

Pin Nu	umber	Pin Name	Pin Function		
SC-70	MLF	Fin Name			
1	1	V _{OUT}	Drain of P-channel MOSFET.		
2,5	2	GND	Ground and the backside pad (MLF only) should both be connected to electrical ground.		
4	3	V _{IN}	Source of P-channel MOSFET.		
3	4	EN	Enable (Input): Active-high CMOS compatible control input for switch A. Do not leave floating.		
6		NIC	No Internal Connection. A signal or voltage applied to this pin will have no effect on device operation.		

Absolute Maximum Ratings⁽¹⁾

Input Voltage (V _{IN})	+6V
Enable Voltage (V _{FN})	
Continuous Drain Current (I _D) ⁽³⁾	
$T_A = 25^{\circ}C (MLF^{(R)})$	±1.2A
$T_A = 25^{\circ}C (SC-70)$	±1.2A
Pulsed Drain Current (I _{DP}) ⁽⁴⁾	±3.0A
Continuous Diode Current (I _S) ⁽⁴⁾	–50mA
Storage Temperature (T _s)	5°C to +150°C
ESD Rating – HBM ⁽⁶⁾	4KV

Operating Ratings⁽²⁾

Input Voltage (V _{IN})	+1.7 to +5.5V
Junction Temperature (T _J)	–40°C to +125°C
Package Thermal Resistance	
SC-70-6 (θ _{JA})	
1.2mm × 1.6mm MLF [®] (θ _{JA})	172°C/W
1.2mm × 1.6mm MLF [®] $(\theta_{JC})^{(3)}$	134°C/W

Electrical Characteristics

$T_A = 25^{\circ}C$, bol	d values indicate	e –40°C <u><</u> T _A ∙	< +85°C, unless noted	d.

Symbol	Parameter	Condition	Min	Тур	Мах	Units
$V_{\text{EN}_{\text{TH}}}$	Enable Threshold Voltage	$V_{IN} = 1.8V$ to 4.5V, $I_D = -250\mu A$	0.5		1.2	V
		$V_{IN} = 1.7V$ to 4.5V, $I_D = -250\mu A$	0.4		1.2	V
l _Q	Supply Current	$V_{IN} = V_{EN} = 5.5V, I_D = OPEN$		50nA	5	μA
		Measured on the V_{IN} pin (7)				
I _{EN}	Enable Input Current	$V_{IN} = V_{EN} = 5.5V, I_D = OPEN$		2	4	μA
I _{SHUT-Q}	Shutdown Current	V_{IN} = +5.5V, V_{EN} = 0V, I_D = OPEN		25nA	1	μA
		Measured on the V_{IN} pin (7)				
I _{SHUT-SWITCH}	OFF State Leakage Current	V_{IN} = +5.5V, V_{EN} = 0V, I_D = SHORT		50nA	1	μA
		Measured on V_{OUT} (7)				
R _{DS(ON)}	P-Channel Drain to Source ON Resistance	V_{IN} = +5.0V, I_D = -100mA, V_{EN} = 1.5V		120	170	mΩ
		V_{IN} = +4.5V, I_D = -100mA, V_{EN} = 1.5V		130	185	mΩ
	SC-70 Package	V_{IN} = +3.6V, I_D = -100mA, V_{EN} = 1.5V		145	210	mΩ
		V_{IN} = +2.5V, I_D = -100mA, V_{EN} = 1.5V		165	225	mΩ
		$V_{IN} = +1.8V, I_D = -100mA, V_{EN} = 1.5V$		200	260	mΩ
		$V_{IN} = +1.7V, I_D = -100mA, V_{EN} = 1.5V$		210	285	mΩ
R _{DS(ON)}	P-Channel Drain to Source	V_{IN} = +5.0V, I_D = -100mA, V_{EN} = 1.5V		100	160	mΩ
	ON Resistance	V_{IN} = +4.5V, I_{D} = -100mA, V_{EN} = 1.5V		110	165	mΩ
		V_{IN} = +3.6V, I_D = -100mA, V_{EN} = 1.5V		125	180	mΩ
	MLF Package	V_{IN} = +2.5V, I_D = -100mA, V_{EN} = 1.5V		145	200	mΩ
		V_{IN} = +1.8V, I_D = -100mA, V_{EN} = 1.5V		180	240	mΩ
		$V_{IN} = +1.7V, I_D = -100mA, V_{EN} = 1.5V$		190	265	mΩ
R _{SHUTDOWN}	Turn-Off Resistance	$V_{IN} = +3.6V, I_{TEST} = 1mA, V_{EN} = 0V$		200	400	Ω
		MIC94071, 73				

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Electrical Characteristics (Dynamic)

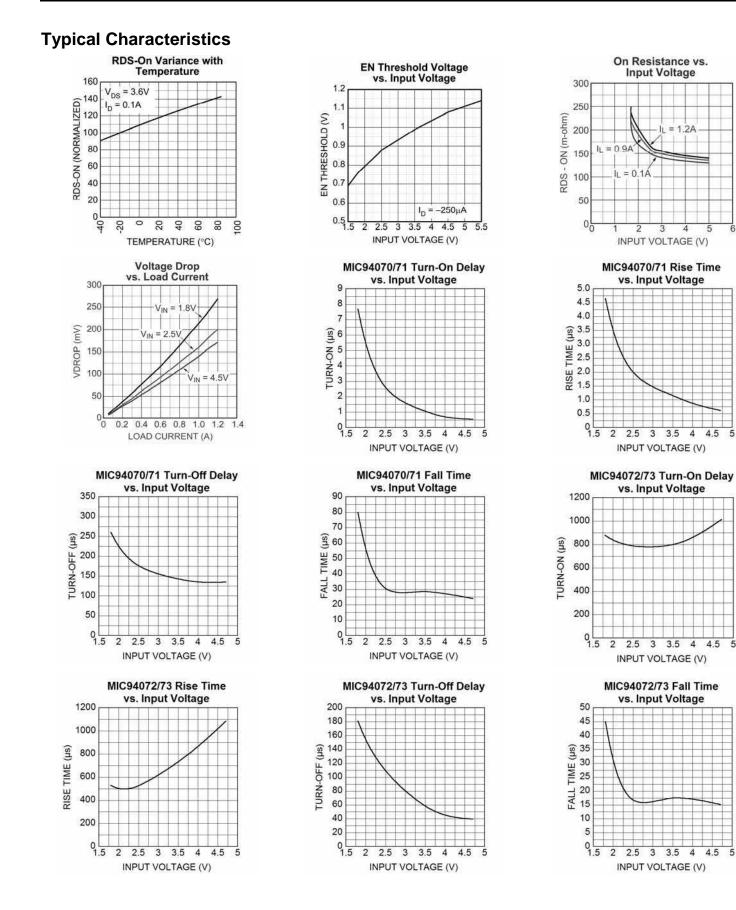
$V_{IN} = 5V' T_{A} = 25^{\circ}C$	bold values indicate	–40°C< T₄ < +85°0	C unless noted
$v_{\rm IN} = 0v$, $r_{\rm A} = 200$,		$+0.0 \times 1_A \times 100$, unicos noteu.

Symbol	Parameter	Condition	Min	Тур	Мах	Units
t _{ON_DLY}	Turn-On Delay Time	$V_{IN} = +3.6V, ID = -100mA, V_{EN} = 1.5V$		0.85	1.5	μs
		MIC94070, 71				
		$V_{IN} = +3.6V, ID = -100mA, V_{EN} = 1.5V$		700	1200	μs
		MIC94072, 73				
t _{ON_RISE}	Turn-On Rise Time	$V_{IN} = +3.6V, ID = -100mA, V_{EN} = 1.5V$	0.5	1	5	μs
		MIC94070, 71				
		V_{IN} = +3.6V, ID = -100mA, V_{EN} = 1.5V	500	800	1500	μs
		MIC94072, 73				

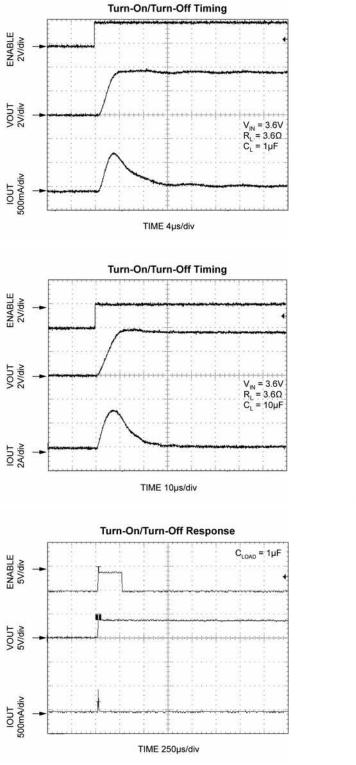
t _{OFF_DLY}	Turn-Off Delay Time	V_{IN} = +3.6V, ID = -100mA, V_{EN} = 1.5V	100	200	ns
		MIC94070, 71			
		V_{IN} = +3.6V, ID = -100mA, V_{EN} = 1.5V	60	200	ns
		MIC94072, 73			
t _{OFF_FALL}	Turn-Off Fall Time	V_{IN} = +3.6V, ID = -100mA, V_{EN} = 1.5V	60	100	ns
		MIC94070, 71			
		V_{IN} = +3.6V, ID = -100mA, V_{EN} = 1.5V	60	100	ns
		MIC94072, 73			

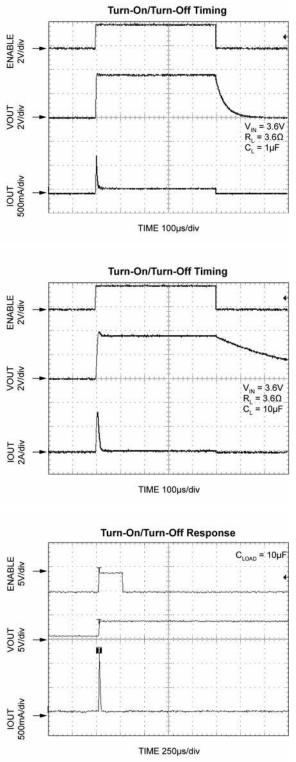
Notes:

- 1. Exceeding the absolute maximum rating may damage the device.
- 2. The device is not guaranteed to function outside its operating rating.
- 3. With backside thermal contact to PCB.
- 4. Pulse width $<300\mu$ s with <2% duty cycle.
- 5. Continuous body diode current conduction (reverse conduction, i.e. V_{OUT} to V_{IN}) is not recommended.
- 6. Devices are ESD sensitive. Handling precautions recommended. HBM (Human body model), 1.5k in series with 100pF.
- 7. Measured on the MIC94070YMT, for other part numbers, please contact Micrel.

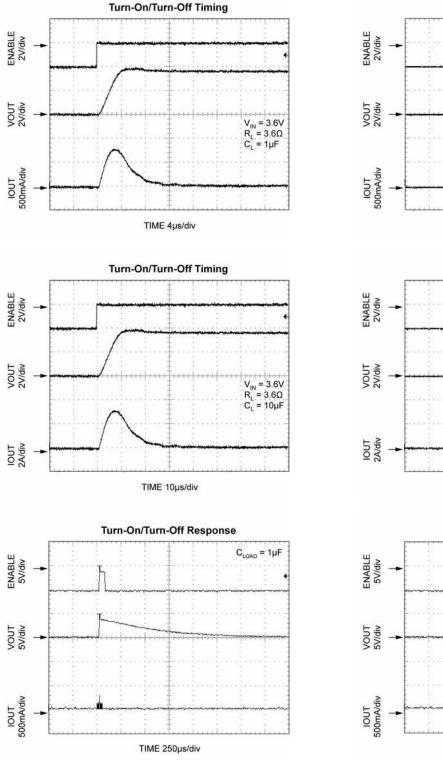


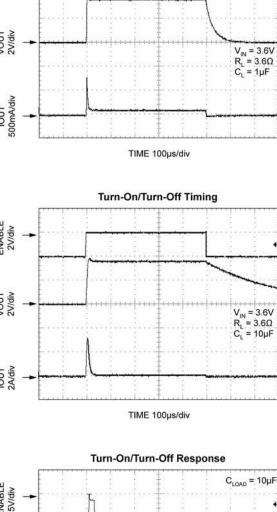
Functional Characteristics MIC94070





MIC94071

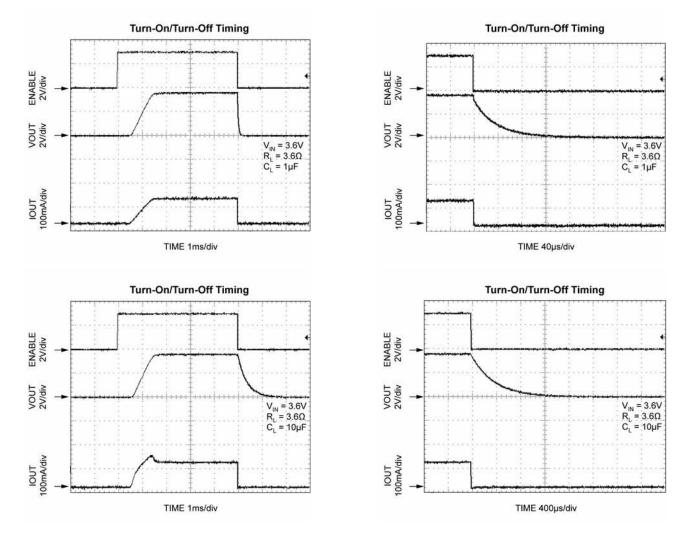




TIME 250µs/div

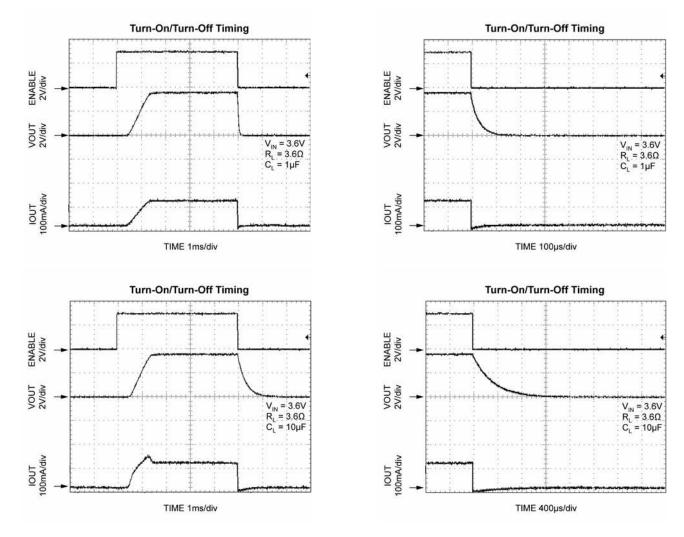
Turn-On/Turn-Off Timing

MIC94072



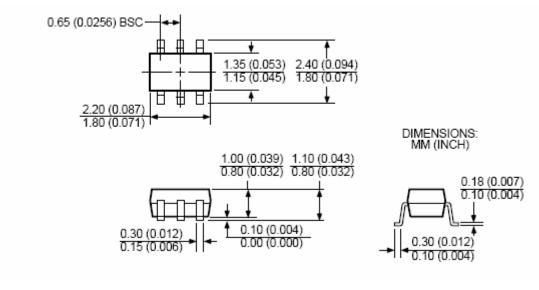
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MIC94073



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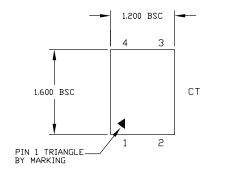
Package Information



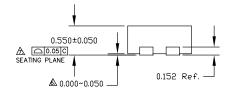
6-Pin SC-70 (C6)

▲ 0.250±0.050

0.350±0.050







BOTTOM VIEW

3 4

2

1

- NDTE: 1. ALL DIMENSIONS ARE IN MILLIMETERS. 2. MAX. PACKAGE WARPAGE IS 0.05 mm. 3. MAXIMUM ALLOWABE BURRS IS 0.076 mm IN ALL DIRECTIONS. 4. PIN #1 ID ON TOP WILL BE LASER/INK MARKED. MILLING DATE: TO METALIZED TERMINAL AND IS MEASURED BETVECH 0.20 AND 0.25 mm FRUM TERMINAL TIP. APPLIED ONLY FOR TERMINALS. 4. NOPLIES FOR EXPROSED OLD MUS TOPMUNG

0.860±0.050 E×p.DAP

- 0.500 Bsc

PIN #1 ID R0.100

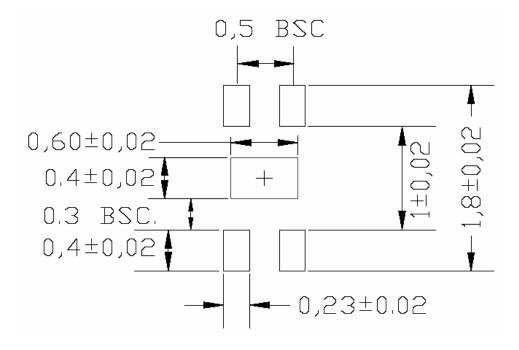
V 0.500±0.050 E×p.DAP

- Â APPLIED FOR EXPOSED PAD AND TERMINALS.

SIDE VIEW



Recommended Land Pattern for MLF 1.2x1.6 4 Lead



Optional for maximum thermal performance. Heatsink should be connected to GND plane of PCB for maximum thermal performance.

Disclaimer: This is only a recommendation based on information available to Micrel from its suppliers. Actual land pattern may have to be significantly different due to various materials and processes used in PCB assembly. Micrel makes no representation or warranty of performance based on the recommended land pattern."

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