

isc N-Channel MOSFET Transistor

TK12A50W,ITK12A50W

• FEATURES

- Low drain-source on-resistance: $R_{DS(ON)} = 0.3\Omega$
- Easy to control Gate switching
- Enhancement mode: $V_{th} = 2.7$ to 3.7 V ($V_{DS} = 10$ V, $I_D=0.6$ mA)
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• DESCRIPTION

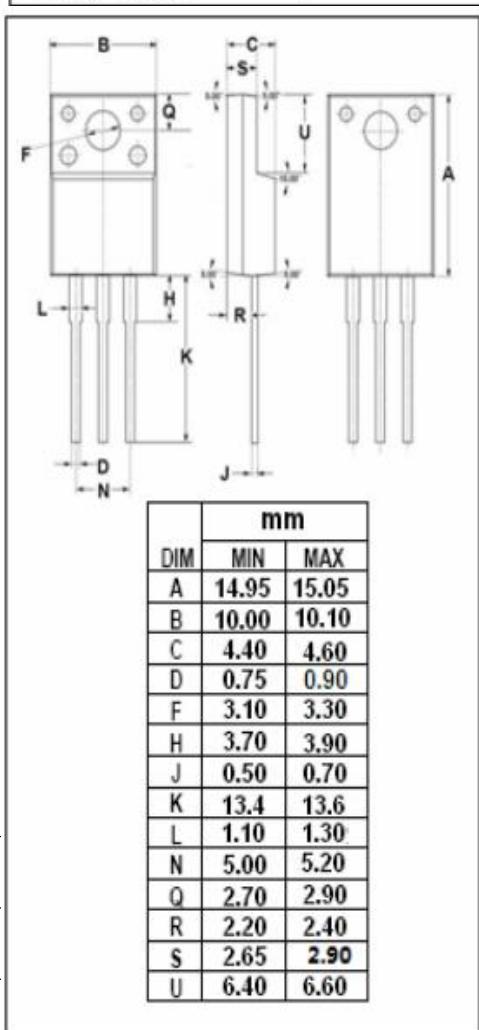
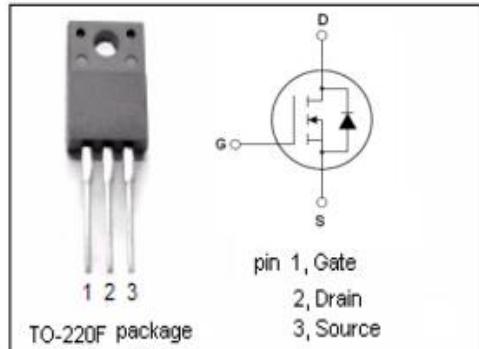
- Switching Voltage Regulators

• ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DSS}	Drain-Source Voltage	500	V
V_{GS}	Gate-Source Voltage	± 30	V
I_D	Drain Current-Continuous	11.5	A
I_{DM}	Drain Current-Single Pulsed	46	A
P_D	Total Dissipation @ $T_c=25^\circ C$	35	W
T_j	Max. Operating Junction Temperature	150	°C
T_{stg}	Storage Temperature	-55~150	°C

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	3.57	°C/W
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	62.5	°C/W



isc N-Channel MOSFET Transistor**TK12A50W,ITK12A50W****ELECTRICAL CHARACTERISTICS** $T_c=25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}; \text{I}_D= 10\text{mA}$	500			V
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}= 10\text{V}; \text{I}_D= 0.6\text{mA}$	2.7		3.7	V
$\text{R}_{\text{DS(on)}}$	Drain-Source On-Resistance	$\text{V}_{\text{GS}}= 10\text{V}; \text{I}_D= 5.8\text{A}$			300	$\text{m}\Omega$
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}= \pm 30\text{V}; \text{V}_{\text{DS}}= 0\text{V}$			± 1	μA
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}= 500\text{V}; \text{V}_{\text{GS}}= 0\text{V}$			10	μA
V_{SDF}	Diode forward voltage	$\text{I}_{\text{DR}} = 11.5 \text{ A}, \text{V}_{\text{GS}} = 0 \text{ V}$			1.7	V