

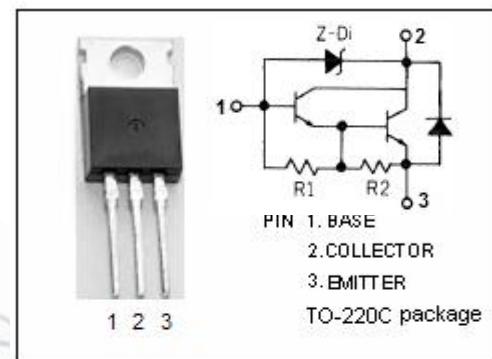


## isc Silicon NPN Darlington Power Transistor

2SD1647

**DESCRIPTION**

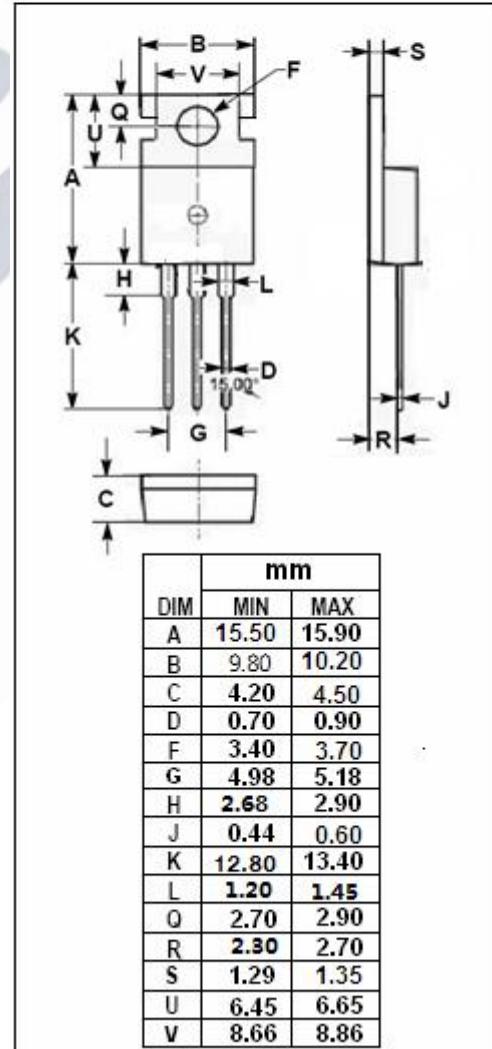
- Low Collector-Emitter Saturation Voltage-  
:  $V_{CE(sat)} = 1.5V(\text{Max}) @ I_C = 1A$
- High DC Current Gain  
:  $h_{FE} = 1000(\text{Min}) @ I_C = 1.0A$
- Low Saturation Voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**APPLICATIONS**

- Designed for general purpose amplifier and low frequency power Amp applications.

**ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	50-70	V
$V_{CEO}$	Collector-Emitter Voltage	50-70	V
$V_{EBO}$	Emitter-Base Voltage	6	V
$I_C$	Collector Current-Continuous	2	A
$I_{CP}$	Collector Current-Peak	3	A
$P_c$	Collector Power Dissipation @ $T_c=25^\circ\text{C}$	25	W
$T_J$	Junction Temperature	150	°C
$T_{stg}$	Storage Temperature Range	-55~150	°C



INCHANGE Semiconductor

**isc Silicon NPN Darlington Power Transistor****2SD1647****ELECTRICAL CHARACTERISTICS****T<sub>c</sub>=25°C unless otherwise specified**

<b>SYMBOL</b>	<b>PARAMETER</b>	<b>CONDITIONS</b>	<b>MIN</b>	<b>TYP.</b>	<b>MAX</b>	<b>UNIT</b>
V <sub>CEO(sus)</sub>	Collector-Emitter Sustaining Voltage	I <sub>C</sub> = 5mA, I <sub>B</sub> = 0	50		70	V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = 100 μ A, I <sub>E</sub> = 0	50		70	V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 1A, I <sub>B</sub> = 1mA			1.5	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = 50V, I <sub>E</sub> = 0			10	uA
I <sub>CEO</sub>	Collector Cutoff Current	V <sub>CE</sub> = 50V, I <sub>B</sub> = 0			1.0	mA
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = 5V; I <sub>C</sub> = 0			3.0	mA
h <sub>FE-1</sub>	DC Current Gain	I <sub>C</sub> = 1A; V <sub>CE</sub> = 2V	1000		10000	
C <sub>OB</sub>	Output Capacitance	I <sub>E</sub> = 0 ; V <sub>CB</sub> = 10V, f <sub>test</sub> = 1MHz		25		pF