♠TDK

Multilayer Ceramic Chip Capacitors

CGA3E2X7R2A103M080AE

RoHS | REACH SVHC-Free | Halogen Free | Lead Free

TDK item description	CGA3E2X7R2A103MT***S
Applications	🚗 Automotive Grade
Feature	Mid Mid Voltage (100 to 630V)
	Soft Soft Termination
	AEC-Q200 AEC-Q200
Series	CGA3(1608) [EIA 0603]
Status	💾 Production

Dimensions in mm

Size	
Length(L)	1.60mm +0.20,-0.10mm
Width(W)	0.80mm +0.15,-0.10mm
Thickness(T)	0.80mm +0.15,-0.10mm
Terminal Width(B)	0.20mm Min.
Terminal Spacing(G)	0.30mm Min.
Recommended Land Pattern (PA)	0.70mm to 1.00mm(Flow Soldering)
	0.60mm to 0.80mm(Reflow Soldering)
Recommended Land Pattern (PB)	0.80mm to 1.00mm(Flow Soldering)
	0.60mm to 0.80mm(Reflow Soldering)
Recommended Land Pattern (PC)	0.60mm to 0.80mm(Flow Soldering)
	0.60mm to 0.80mm(Reflow Soldering)

Electrical Characteristics	
Capacitance	10nF ±20%
Rated Voltage	100VDC
Temperature Characteristic	X7R(±15%)
Dissipation Factor (Max.)	3%
Insulation Resistance (Min.)	10000MΩ

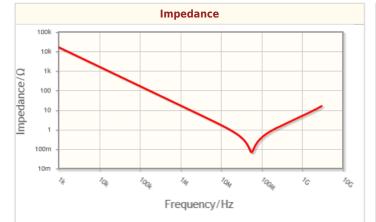| Other	
Soldering Method	Wave (Flow)
	Reflow
AEC-Q200	Yes
Packing	Punched (Paper)Taping [180mm Reel]
Package Quantity	4000pcs

! Images are for reference only and show exemplary products.
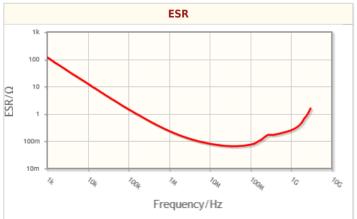! This PDF document was created based on the data listed on the TDK Corporation website.
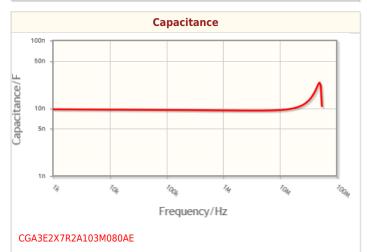! All specifications are subject to change without notice.

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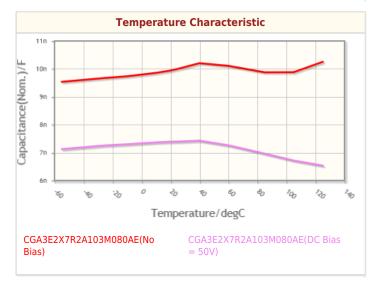## Multilayer Ceramic Chip Capacitors

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Characteristic Graphs (This is reference data, and does not guarantee the products characteristics.)

Impedance

CGA3E2X7R2A103M080AE

ESR

CGA3E2X7R2A103M080AE

Capacitance

CGA3E2X7R2A103M080AE

DC Bias Characteristic

CGA3E2X7R2A103M080AE

Temperature Characteristic

CGA3E2X7R2A103M080AE(No Bias) CGA3E2X7R2A103M080AE(DC Bias = 50V)

Ripple Temperature Rising
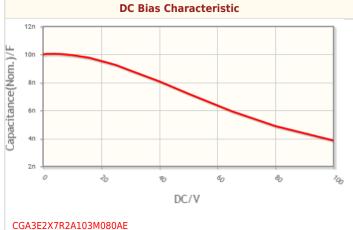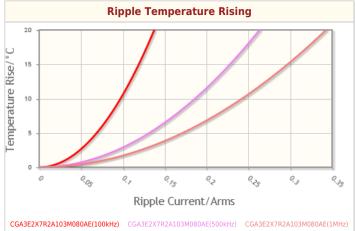
CGA3E2X7R2A103M080AE(100kHz) CGA3E2X7R2A103M080AE(500kHz) CGA3E2X7R2A103M080AE(1MHz)

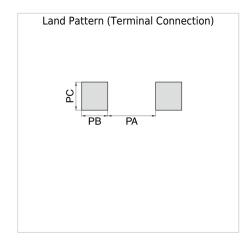! Images are for reference only and show exemplary products.
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Associated Images

Land Pattern (Terminal Connection)

PC

PB PA

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