TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SH86FS

2-Input EXCLUSIVE OR Gate

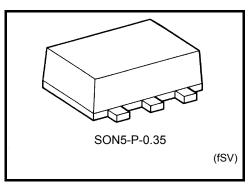
Features

• High speed : t_{pd} = 4.8 ns (typ.) at V_{CC} = 5V, C_L =15pF

Low power dissipation : I_{CC} = 2μA (max) at Ta = 25°C
 High noise immunity : V_{NIH} = V_{NIL} = 28% V_{CC} (min)

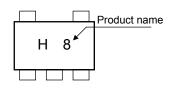
5.5-V tolerant inputs.

• Wide operating voltage range: V_{CC} = 2 to 5.5 V

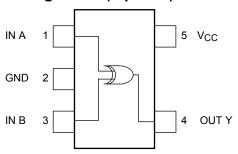


Weight: 0.001 g (typ.)

Marking



Pin Assignment (top view)



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	– 0.5 to 7	V
DC input voltage	V _{IN}	– 0.5 to 7	V
DC output voltage	Vout	- 0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	- 20	mA
Output diode current	lok	± 20 (Note 1)	mA
DC output current	lout	± 25	mA
DC V _{CC} /ground current	Icc	± 50	mA
Power dissipation	PD	50	mW
Storage temperature	T _{stg}	– 65 to 150	°C

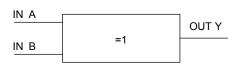
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$

Start of commercial production 2003-08

IEC Logic Symbol



Truth Table

Α	В	Υ
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	2 to 5.5	V	
Input voltage	V _{IN}	0 to 5.5	V	
Output voltage	V _{OUT}	0 to V _{CC}	V	
Operating temperature	T _{opr}	– 40 to 85	°C	
Input rise and fall time	dt/dv	0 to 100 (V _{CC} = 3.3 ± 0.3 V)	ns/V	
	avav	0 to 20 (V _{CC} = 5.0 ± 0.5 V)	115/V	

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Electrical Characteristics

DC Characteristics

Characteristics Symbol Test 0		Condition		Ta = 25°C			Ta = -40 to 85°C		Unit	
		1650	Test Condition		Min	Тур.	Max	Min	Max	Offic
High-level input voltage V _{IH}			_		1.50			1.50	_	
					V _{CC} × 0.7	_	_	V _{CC} × 0.7	_	
				2.0	_	_	0.5	_	0.5	V
Low-level input voltage	V _{IL}		_	3.0 to 5.5	_	_	V _{CC} × 0.3	_	_ V _{CC} × 0.3	
		V _{IN} = V _{IH} or V _{IL}	I _{OH} = -50 μA	2.0	1.9	2.0	_	1.9	_	V
				3.0	2.9	3.0	_	2.9	_	
High-level output voltage	V _{OH}			4.5	4.4	4.5	_	4.4	_	
			$I_{OH} = -4 \text{ mA}$	3.0	2.58			2.48	_	
			$I_{OH} = -8 \text{ mA}$	4.5	3.94			3.80	_	
Low-level output voltage			I _{OL} = 50 μA	2.0	1	0.0	0.1	_	0.1	
				3.0	1	0.0	0.1	_	0.1	
	V _{OL}	$V_{IN} = V_{IH}$		4.5		0.0	0.1	_	0.1	
		0	$I_{OL} = 4 \text{ mA}$	3.0			0.36	_	0.44	
			$I_{OL} = 8 \text{ mA}$	4.5			0.36	_	0.44	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5			±0.1	_	±1.0	μΑ
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		5.5	_	_	2.0	_	20.0	μА

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AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics Symb	Symbol	Т	est Condition		Ta = 25°C			Ta = -40 to 85°C		- Unit
	Syllibol	Symbol		C _L (pF)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	t _{pLH}		3.3 ± 0.3	15	_	7.0	11.0	1.0	13.0	ns
				50	_	9.5	14.5	1.0	16.5	
		_	5.0 ± 0.5	15	_	4.8	6.8	1.0	8.0	
				50	_	6.3	8.8	1.0	10.0	
Input capacitance	C _{IN}		_		_	4	10	_	10	pF
Power dissipation capacitance	C _{PD}			(Note 2)	_	18		_		pF

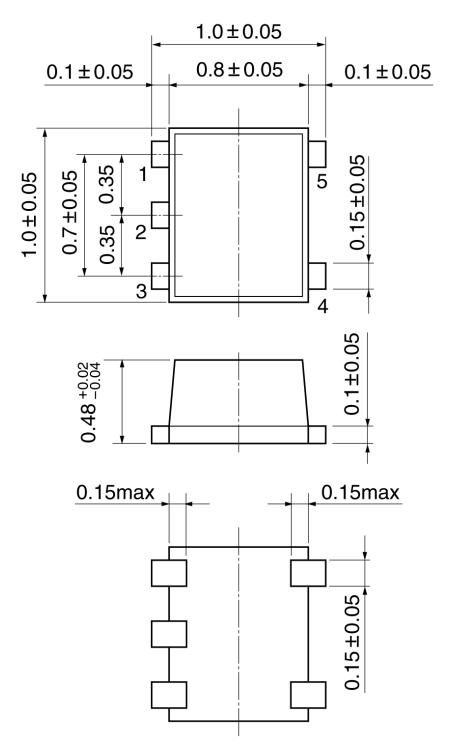
Note 2 : C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC \text{ (opr)}} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

Package Dimensions

SON5-P-0.35 Unit: mm



Weight: 0.001 g (typ.)

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