TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7PZ14FU

#### **Dual Schmitt Inverter**

#### **Features**

High output current : ±24 mA (min) at V<sub>CC</sub> = 3 V

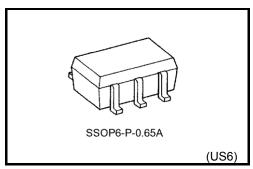
• Super high speed operation : t<sub>pd</sub> = 3.7 ns (typ.)

at  $V_{CC} = 5 \text{ V}, 50 \text{ pF}$ 

Operation voltage range : V<sub>CC</sub> = 1.65 to 5.5 V

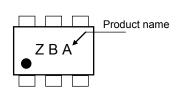
5.5-V tolerant inputs

• 5.5-V power down protection outputs

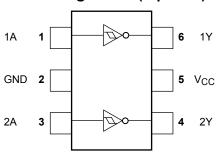


Weight: 0.0068g (typ.)

#### Marking



#### Pin Assignment (top view)



#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	−0.5 to 6	V
DC input voltage	V <sub>IN</sub>	-0.5 to 6	V
DC output valtage	\/	-0.5 to 6 (Note 1)	V
DC output voltage	V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> + 0.5 (Note 2)	
Input diode current	l <sub>IK</sub>	-20	mA
Output diode current	lok	-20 (Note 3)	mA
DC output current	lout	±50	mA
DC V <sub>CC</sub> /ground current	Icc	±100	mA
Power dissipation	PD	200	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{CC} = 0 V$ 

Note 2: High or Low State. Do not exceed I<sub>OUT</sub> of absolute maximum ratings.

Note 3: V<sub>OUT</sub> < GND

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# **IEC Logic Symbol**



#### **Truth Table**

Α	Y
L	Н
Н	L

## **Operating Ranges**

Characteristics	Symbol	Rating	Unit	
Cupply voltage	V	1.65 to 5.5	V	
Supply voltage	V <sub>CC</sub>	1.5 to 5.5 (Note 4)	V	
Input voltage	V <sub>IN</sub>	0 to 5.5	V	
Output voltage	V	0 to 5.5 (Note 5)	V	
	V <sub>OUT</sub>	0 to V <sub>CC</sub> (Note 6)	V	
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	

Note 4: Data retention only

Note 5:  $V_{CC} = 0 V$ 

Note 6: High or Low state

#### **Electrical Characteristics**

#### **DC Electrical Characteristics**

Characteristics		Symbol Test Condition			Ta = 25°C			Ta = -40	Unit	
Charac	tensucs	Symbol	rest Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
				1.65	0.6	1.0	1.4	0.6	1.4	
				1.8	0.7	1.1	1.5	0.7	1.5	
	Lligh lovel	\/-	_	2.3	1.0	1.4	1.8	1.0	1.8	
	High level	V <sub>P</sub>		3.0	1.3	1.75	2.2	1.3	2.2	
				4.5	1.9	2.45	3.1	1.9	3.1	
Threshold				5.5	2.2	2.9	3.6	2.2	3.6	\/
voltage			_	1.65	0.2	0.5	8.0	0.2	0.8	V
				1.8	0.25	0.55	0.9	0.25	0.9	
	L our lovel			2.3	0.40	0.75	1.15	0.40	1.15	
	Low level	V <sub>N</sub>		3.0	0.6	1.0	1.5	0.6	1.5	
				4.5	1.0	1.43	2.0	1.0	2.0	
				5.5	1.2	1.70	2.4	1.2	2.4	
					0.1	0.48	0.9	0.1	1.0	
Hysteresis voltage				1.8	0.15	0.54	1.0	0.15	1.0	
			2.3	0.25	0.65	1.1	0.25	1.1	1	
	ıtage	V <sub>H</sub>	_	3.0	0.4	0.77	1.2	0.4	1.2	V
				4.5	0.6	1.01	1.5	0.6	1.5	
					5.5	0.7	1.18	1.7	0.7	1.7

Characteristics Symbol		Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit	
		Symbol	rest	Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
				I <sub>OH</sub> = -100 μA	1.65	1.55	1.65		1.55	_	
					1.8	1.7	1.8	_	1.7	_	
					2.3	2.2	2.3	_	2.2	_	
					3.0	2.9	3.0	_	2.9	_	
	High level	V <sub>OH</sub>	$V_{IN} = V_N$		4.5	4.4	4.5		4.4	_	
	i ligit level	VOH	NIN – N	I <sub>OH</sub> = -4 mA	1.65	1.29	1.52		1.29	_	
				$I_{OH} = -8 \text{ mA}$	2.3	1.9	2.15		1.9	_	
				I <sub>OH</sub> = -16 mA	3.0	2.4	2.8		2.4	_	
				I <sub>OH</sub> = -24 mA	3.0	2.3	2.68		2.3	_	
Output	Output		I <sub>OH</sub> = -32 mA	4.5	3.8	4.2	_	3.8	_	V	
voltage			oL VIN = VP	I <sub>OL</sub> = 100 μA	1.65	_	0	0.1	_	0.1	V
	Low level V				1.8	_	0	0.1	_	0.1	
					2.3		0	0.1	_	0.1	
					3.0		0	0.1	_	0.1	
		Vai			4.5		0	0.1	_	0.1	
	LOW level	Low level V <sub>OL</sub> \		I <sub>OL</sub> = 4 mA	1.65		0.08	0.24		0.24	
				I <sub>OL</sub> = 8 mA	2.3		0.1	0.3		0.3	
				I <sub>OL</sub> = 16 mA	3.0		0.15	0.4		0.4	
				I <sub>OL</sub> = 24 mA	3.0		0.22	0.55	_	0.55	
				I <sub>OL</sub> = 32 mA	4.5	1	0.22	0.55		0.55	
Input leakage	current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5		_	±1	_	±10	μΑ
Power OFF le	Power OFF leakage current I <sub>OFF</sub> V <sub>IN</sub> or V <sub>OUT</sub> = 1		UT = 5.5 V	0.0	_	_	1	_	10	μА	
Quiescent su	pply current	Icc	$V_{IN} = 5.5$	V or GND	1.65 to 5.5		_	1	_	10	μА

### AC Electrical Characteristics (Unless otherwise specified Input: $t_r = t_f = 3$ ns)

Characteristics	0	T 10 111		Ta = 25°C			Ta = -40 to 85°C		1.1
	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time		$\begin{array}{l} C_L = 15 \ pF, \\ R_L = 1 \ M\Omega \end{array}$	1.8± 0.15	2.0	9.1	15.0	2.0	15.6	ns
			$2.5 \pm 0.2$	1.0	5.0	9.0	1.0	9.5	
	t <sub>pLH</sub> t <sub>pHL</sub>		$3.3 \pm 0.3$	1.0	3.7	6.3	1.0	6.5	
			$5.0 \pm 0.5$	0.5	3.1	5.2	0.5	5.5	
		$C_L = 50 \text{ pF},$ $R_L = 500 \Omega$	$3.3 \pm 0.3$	1.5	4.4	7.2	1.5	7.5	
			5.0 ± 0.5	0.5	3.7	5.9	0.8	6.2	
Input capacitance	C <sub>IN</sub>	_	0 to 5.5	_	4	_	_	_	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 7)	3.3	_	24	_	_	_	pF
			5.5	_	30	_	_	_	pF

Note 7: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

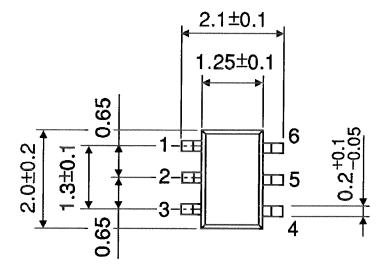
$$I_{CC \text{ (opr.)}} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$$

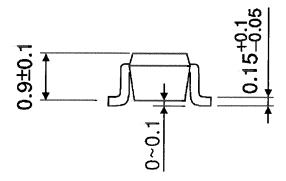
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# **Package Dimensions**

SSOP6-P-0.65A

Unit: mm





Weight: 6.8 mg (typ.)

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