



DMN3008SFG

30V N-CHANNEL ENHANCEMENT MODE MOSFET POWERDI®

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _C = +25°C		
30V	$4.4 m\Omega$ @ $V_{GS} = 10V$	62A		
	$5.5 \text{m}\Omega$ @ $V_{GS} = 4.5 \text{V}$	56A		

Description

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- Backlighting
- Power Management Functions
- DC-DC Converters

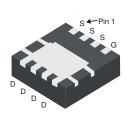
Features and Benefits

- Low R_{DS(ON)} Ensures on-state losses are minimized
- Small, form factor thermally efficient package enables higher density end products
- Occupies only 33% of the board area occupied by SO-8 enabling smaller end products
- 100% Unclamped Inductive Switch (UIS) test in production
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

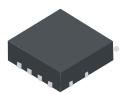
Mechanical Data

- Case: POWERDI[®]3333-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.072 grams (Approximate)

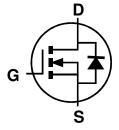
POWERDI®3333-8



Bottom View



Top View



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3008SFG-7	POWERDI®3333-8	2,000/Tape & Reel
DMN3008SFG-13	POWERDI®3333-8	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

POWERDI®3333-8



N08= Product Type Marking Code YYWW = Date Code Marking YY = Last Digit of Year (ex: 15 = 2015) WW = Week Code (01 ~ 53)



Maximum Ratings (@T_A = +25 °C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V _{DSS}	30	V		
Gate-Source Voltage	V _{GSS}	±20	V		
	Steady State	T _A = +25 °C T _A = +70 °C	I _D	17.6 14.1	А
Continuous Drain Current (Note 6) V _{GS} = 10V	t<10s	T _A = +25 °C T _A = +70 °C	I _D	23.0 18.4	Α
	Steady State	$T_C = +25$ °C $T_C = +70$ °C	I_{D}	62 50	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I _{DM}	80	Α		
Maximum Continuous Body Diode Forward Current	Is	2	Α		
Avalanche Current, L = 0.1mH	I _{AS}	45	Α		
Avalanche Energy, L = 0.1mH	Eas	101	mJ		

Thermal Characteristics (@TA = +25 °C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Total Bower Dissipation (Note 5)	T _A = +25℃	D-	0.9	W	
Total Power Dissipation (Note 5)	T _A = +70 ℃	P _D	0.6	T vv	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	ReJA	134	%C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t < 10s	HOJA	79	%C/W	
Total Power Dissipation (Note 6)	T _A = +25 ℃	P_D	2.1	w	
Total Fower Dissipation (Note o)	T _A = +70 ℃	FD	1.3]	
Thermal Peciatones, Junction to Ambient (Note 6)	Steady State	ם	58	°C/W	
Thermal Resistance, Junction to Ambient (Note 6)	t < 10s	$R_{\theta JA}$	34	%C/W	
Thermal Resistance, Junction to Case (Note 6)		$R_{ heta JC}$	4.8	°C/W	
Operating and Storage Temperature Range		T_J,T_STG	-55 to +150	°C	

Electrical Characteristics (@T_A = +25 °C, unless otherwise specified.)

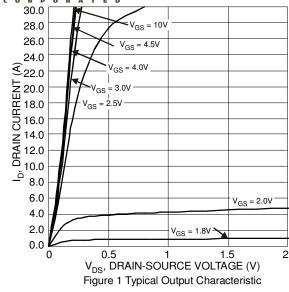
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	30	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	10	μA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}		_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)					·	,	
Gate Threshold Voltage	V _{GS(th)}	1	_	2.3	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Statia Drain Sauras On Decistores		_	3.9	4.4	mΩ	$V_{GS} = 10V, I_D = 13.5A$	
Static Drain-Source On-Resistance	R _{DS} (ON)	_	4.6	5.5	11177	V _{GS} = 4.5V, I _D = 13.5A	
Diode Forward Voltage	V_{SD}		0.75	1.2	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 8)				•	•		
Input Capacitance	C _{iss}	ı	3,690	_	рF	V 40V V 0V	
Output Capacitance	Coss	_	530	_	pF	$V_{DS} = 10V, V_{GS} = 0V,$ -f = 1MHz	
Reverse Transfer Capacitance	C _{rss}		459	_	pF	TI = TIVITIZ	
Gate resistance	R_q	_	0.9	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	41	_	nC		
Total Gate Charge (V _{GS} = 10V)	Qq	_	86	_	nC	1.,	
Gate-Source Charge	Qgs	_	9.2	_	nC	$V_{DS} = 24V, I_D = 27A$	
Gate-Drain Charge	Q_{qd}		18.6	_	nC	1	
Turn-On Delay Time	t _{D(on)}		5.7	_	ns		
Turn-On Rise Time	t _r		14.0	_	ns	$V_{DD} = 15V, V_{GS} = 10V,$ $R_{L} = 1.11\Omega, R_{G} = 4.7\Omega,$ $I_{D} = 13.5A$	
Turn-Off Delay Time	$t_{D(off)}$		63.7	_	ns		
Turn-Off Fall Time	t _f	-	28.4	_	ns		
Reverse Recovery Time	t _{rr}	-	19.3	_	ns	1 40 54 41/44 4004/	
Reverse Recovery Charge	Q _{rr}	_	10.7	_	nC	nC I _F =13.5A, di/dt=100A/µs	

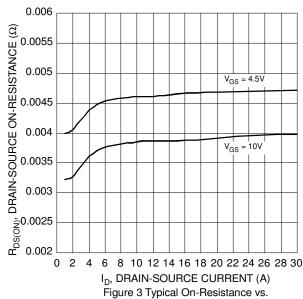
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. Notes:

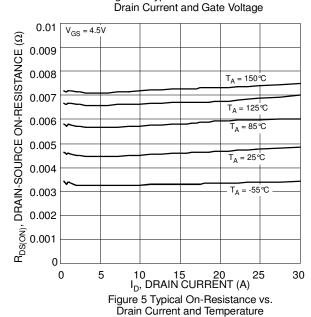
^{6.} Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. Short duration pulse test used to minimize self-heating effect.

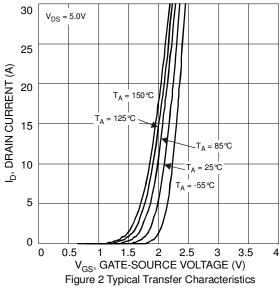
^{8.} Guaranteed by design. Not subject to product testing.

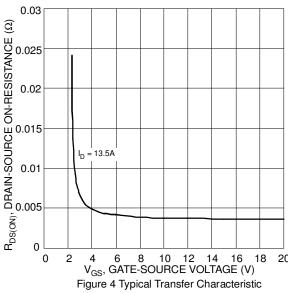
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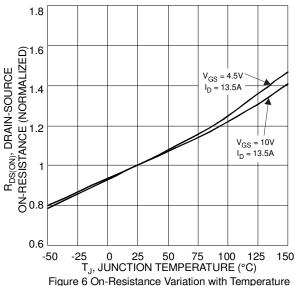




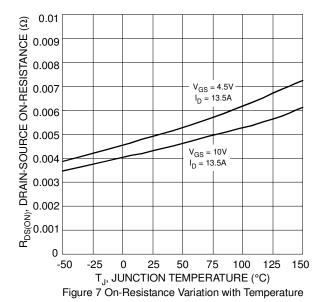


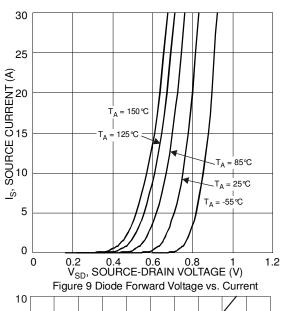


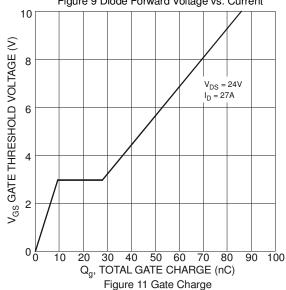


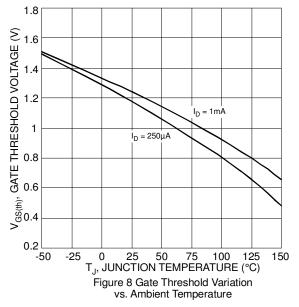


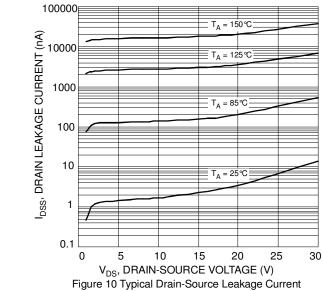






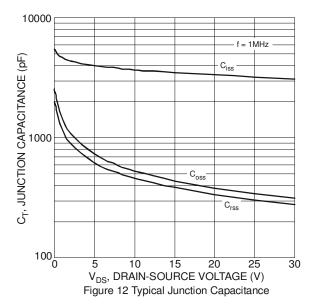


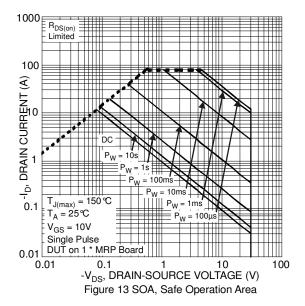


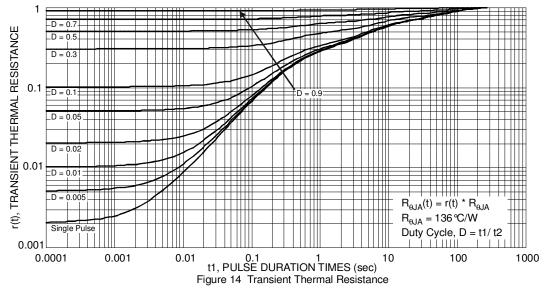


vs. Voltage







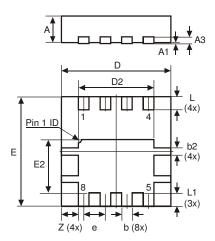




Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

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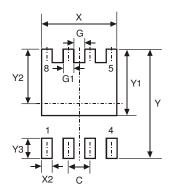


POWERDI [®] 3333-8					
Dim	Min	Max	Тур		
D	3.25	3.35	3.30		
Е	3.25	3.35	3.30		
D2	2.22	2.32	2.27		
E2	1.56	1.66	1.61		
Α	0.75	0.85	0.80		
A1	0	0.05	0.02		
A3	1	-	0.203		
b	0.27	0.37	0.32		
b2	_	_	0.20		
L	0.35	0.45	0.40		
L1	_	_	0.39		
е	ı	ı	0.65		
Z	_	_	0.515		
All I	All Dimensions in mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

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Dimensions	value (in mm)			
С	0.650			
G	0.230			
G1	0.420			
Υ	3.700			
Y1	2.250			
Y2	1.850			
Y3	0.700			
X	2.370			
X2	0.420			



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