

LOW DROPOUT VOLTAGE REGULATOR

■ GENERAL DESCRIPTION

NJM2874/75/76 is a low dropout voltage regulator designed for cellular phone application.

Advanced Bipolar technology achieves low noise, high ripple rejection and low quiescent current.

■ PACKAGE OUTLINE



NJM2874F/75F/76F

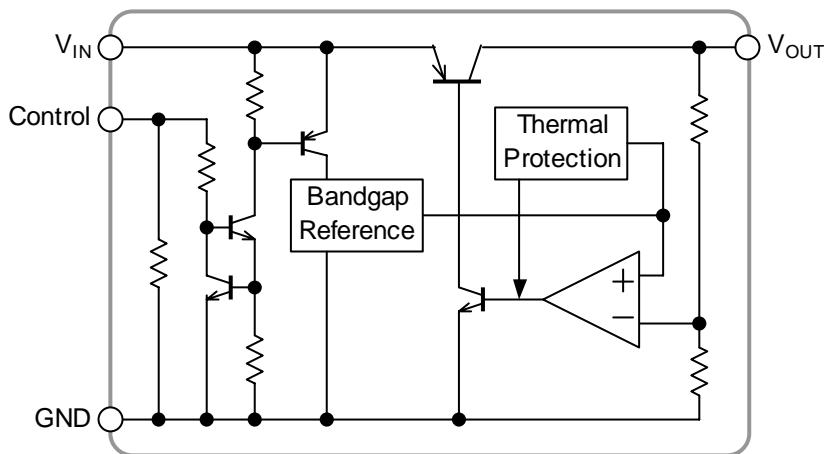
■ FEATURES

- High Ripple Rejection 75dB typ. ($f=1\text{kHz}$ $V_o=3\text{V}$ Version)
- Output Noise Voltage $V_{no}=45\mu\text{VRms}$ typ.
- Output capacitor with $1.0\mu\text{F}$ ceramic capacitor ($V_o \geq 2.7\text{V}$)
- Output Current $I_o(\text{max.})=150\text{mA}$
- High Precision Output $V_o \pm 1\%$
- Low Dropout Voltage 0.10V typ. ($I_o=60\text{mA}$)
- ON/OFF Control (Active High)
- Internal Short Circuit Current Limit
- Internal Thermal Overload Protection
- Bipolar Technology
- Package Outline SOT-23-5

■ PIN CONFIGURATION

		PIN FUNCTION				
		1.CONTROL	1. V_{IN}	1. V_{OUT}	2.GND	2.GND
		2.GND	2.GND	3.V _{IN}	3.CONTROL	3.V _{IN}
		3.NC	4.V _{OUT}	4.NC	4.CONTROL	5.NC
		4.V _{OUT}	5.V _{IN}	5.V _{OUT}	5.NC	
		NJM2874F	NJM2875F	NJM2876F		

■ EQUIVALENT CIRCUIT



■ OUTPUT VOLTAGE RANK LIST

Device Name	V_{OUT}
NJM287xF21	2.1V
NJM287xF28	2.8V
NJM287xF03	3.0V
NJM287xF33	3.3V
NJM287xF05	5.0V

■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS		UNIT
Input Voltage	V _{IN}	+14		V
Control Voltage	V _{CONT}	+14(*1)		V
Power Dissipation	P _D	SOT-23-5	350(*2)	mW
			200(*3)	
Operating Temperature	T _{opr}	-40 ~ +85		°C
Storage Temperature	T _{stg}	-40 ~ +125		°C

(*1): When input voltage is less than +14V, the absolute maximum control voltage is equal to the input voltage.

(*2) : Mounted on glass epoxy board. (114.3x76.2x1.6mm: 2Layer, FR-4)

(*3) : Device itself

■ ELECTRICAL CHARACTERISTICS

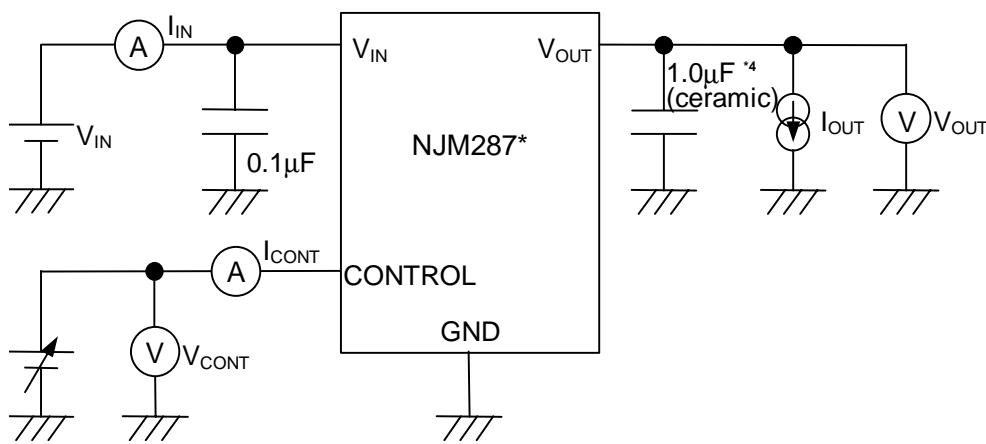
(V_{IN}=V_O+1V, C_{IN}=0.1μF, Co=1.0μF: V_O≥2.7V (Co=2.2μF: V_O≤2.6V), Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _O	I _O =30mA	-1.0%	—	+1.0%	V
Quiescent Current	I _Q	I _O =0mA, expect I _{cont}	—	120	180	μA
Quiescent Current at Control OFF	I _{Q(OFF)}	V _{CONT} =0V	—	—	100	nA
Output Current	I _O	V _O -0.3V	150	200	-	mA
Line Regulation	ΔV _O /ΔV _{IN}	V _{IN} =V _O +1V ~ V _O +6V, I _O =30mA	—	—	0.10	%/V
Load Regulation	ΔV _O /ΔI _O	I _O =0 ~ 100mA	—	—	0.03	%/mA
Dropout Voltage	ΔV _{I-O}	I _O =60mA	—	0.10	0.18	V
Ripple Rejection	RR	ein=200mVrms, f=1kHz, I _O =10mA, V _O =3V Version	—	75	—	dB
Average Temperature Coefficient of Output Voltage	ΔV _O /ΔT _a	T _a =0 ~ 85°C, I _O =10mA	—	±50	—	ppm/°C
Output Noise Voltage	V _{NO}	f=10Hz ~ 80kHz, I _O =10mA, V _O =3V Version	—	45	—	μVrms
Control Voltage for ON-state	V _{CONT(ON)}		1.6	—	—	V
Control Voltage for OFF-state	V _{CONT(OFF)}		—	—	0.6	V

The above specification is a common specification for all output voltages.

Therefore, it may be different from the individual specification for a specific output voltage.

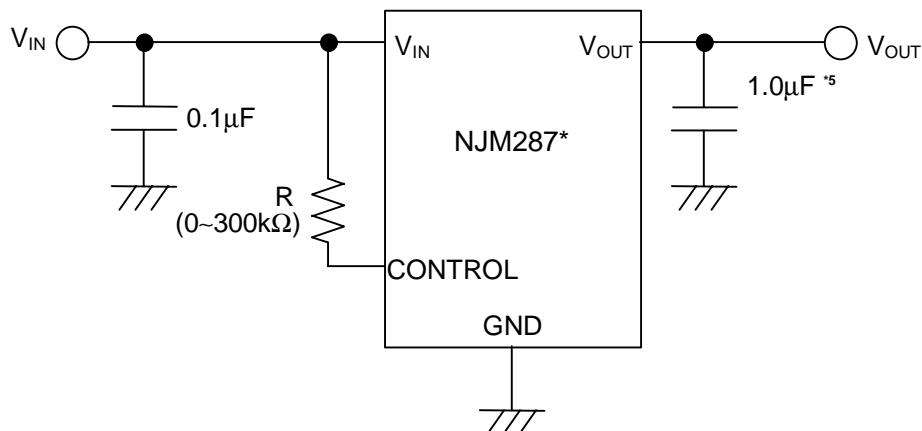
■ TEST CIRCUIT



*4 V_O≤2.6V version: Co=2.2μF(ceramic)

■ TYPICAL APPLICATION

- ① In case that ON/OFF Control is not required:

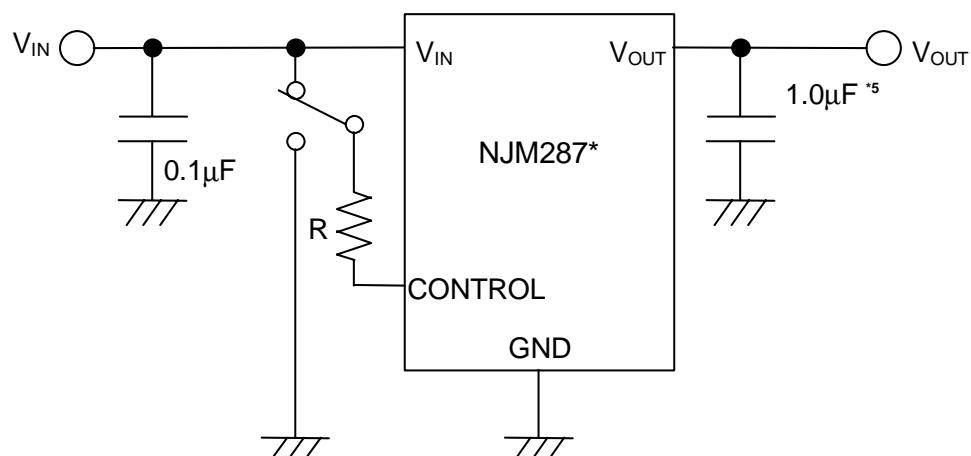


*5 $V_o \leq 2.6V$ version: $C_o = 2.2\mu F$

Connect control terminal to V_{IN} terminal

The quiescent current can be reduced by using a resistance "R". Instead, it increases the minimum operating voltage. For further information, please refer to Figure "Output Voltage vs. Control Voltage".

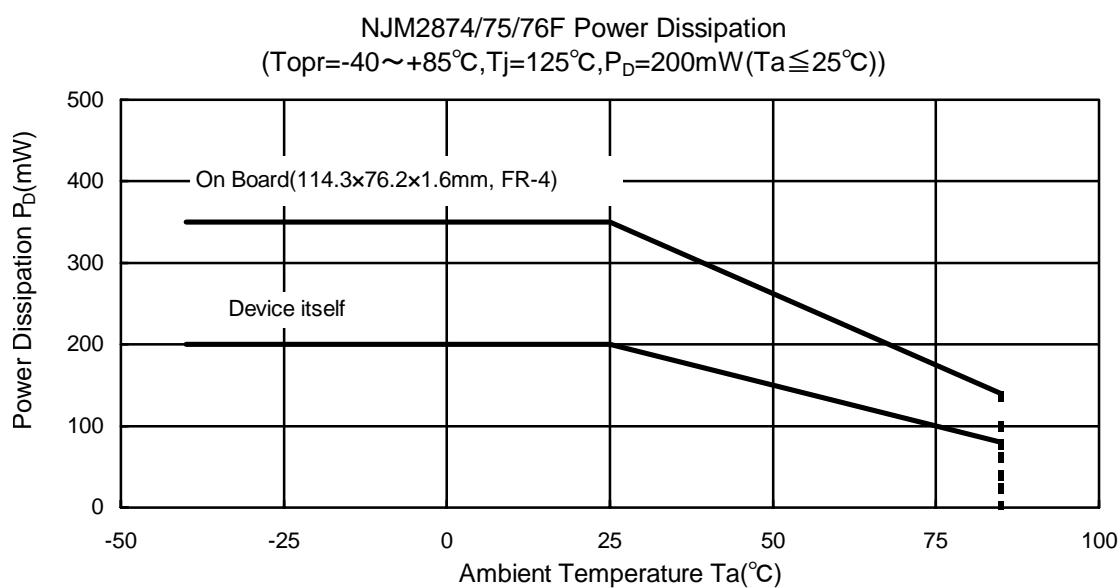
- ② In use of ON/OFF CONTROL:



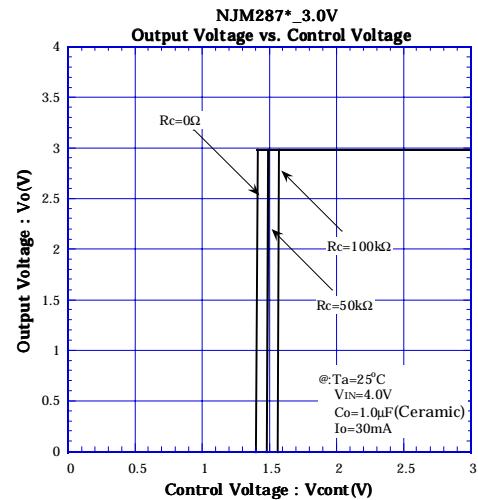
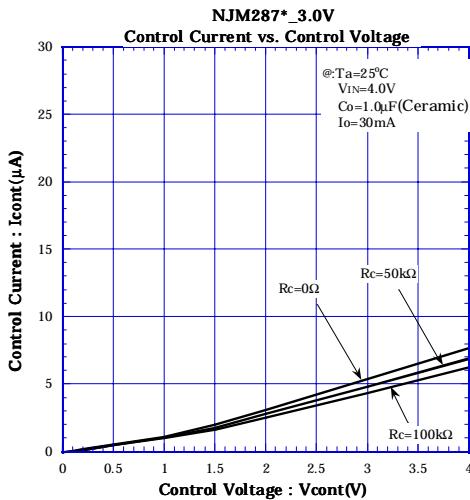
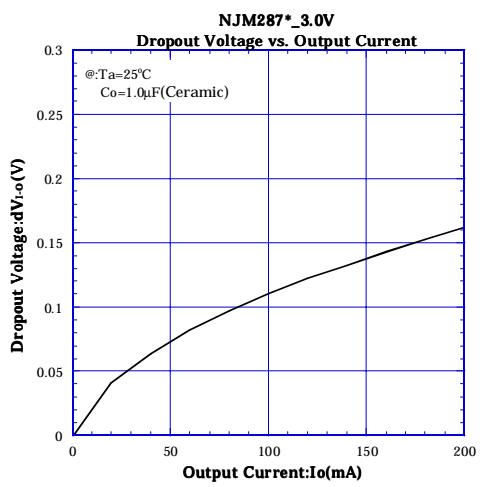
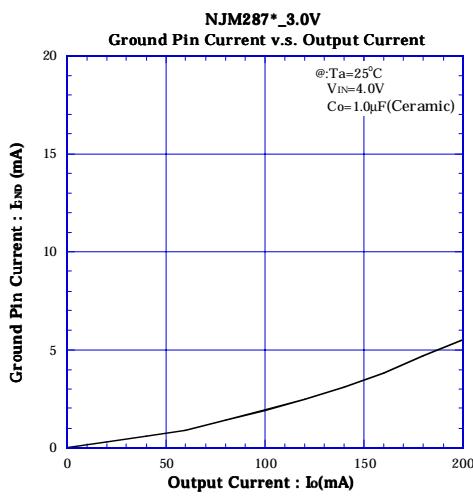
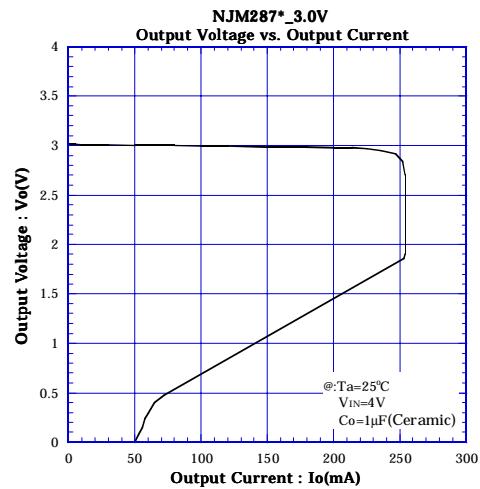
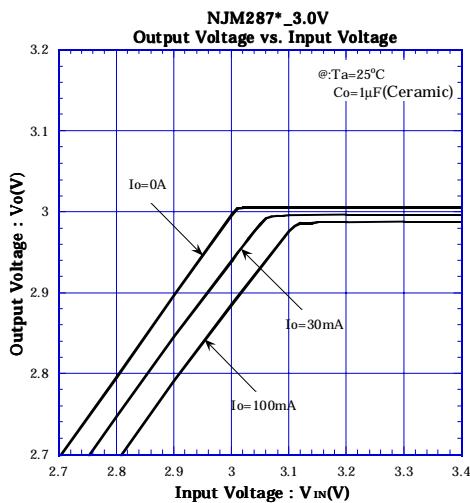
*5 $V_o \leq 2.6V$ version: $C_o = 2.2\mu F$

State of control terminal:

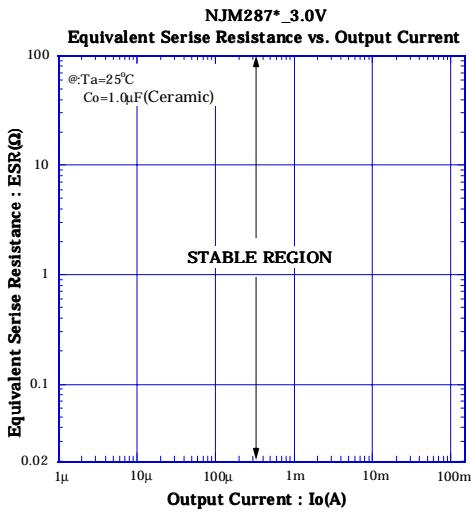
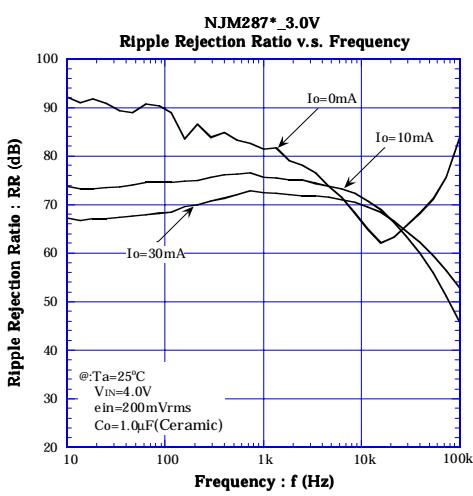
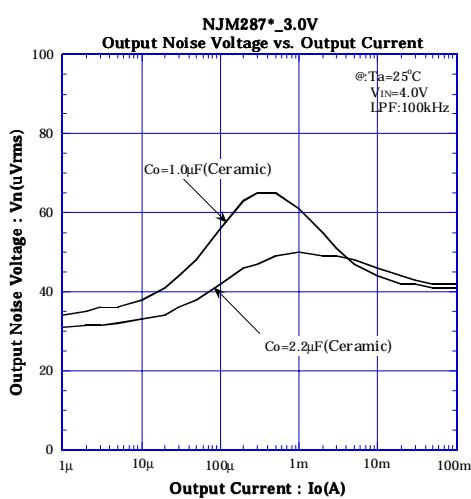
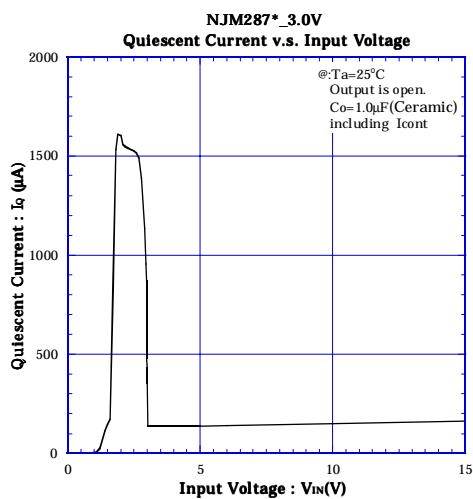
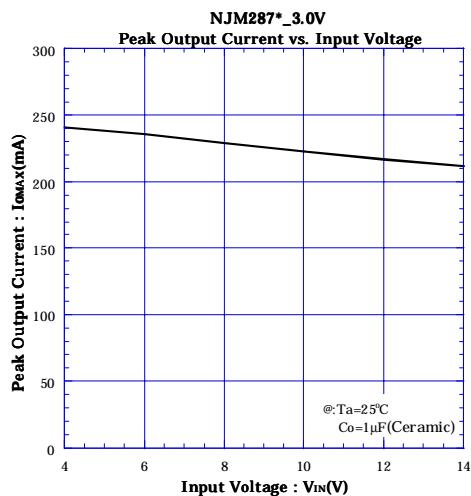
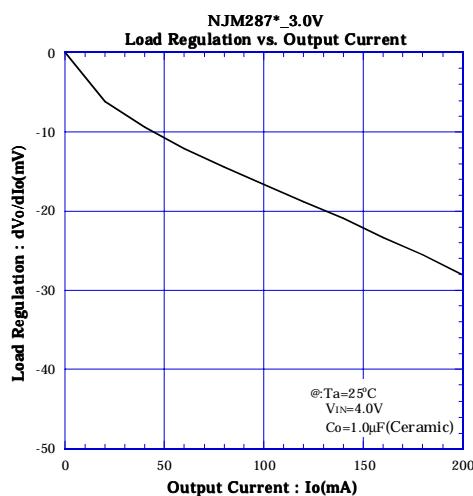
- "H" → output is enabled.
- "L" or "open" → output is disabled.

■ POWER DISSIPATION vs. AMBIENT TEMPERATURE

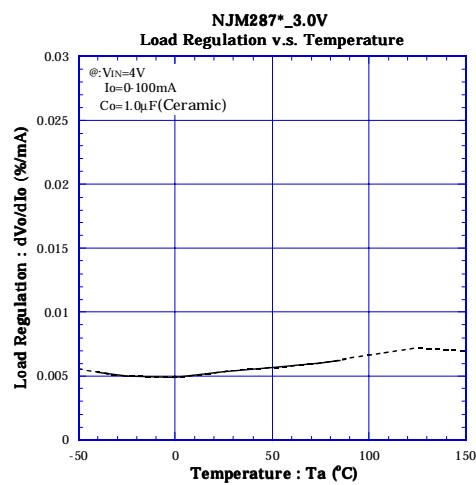
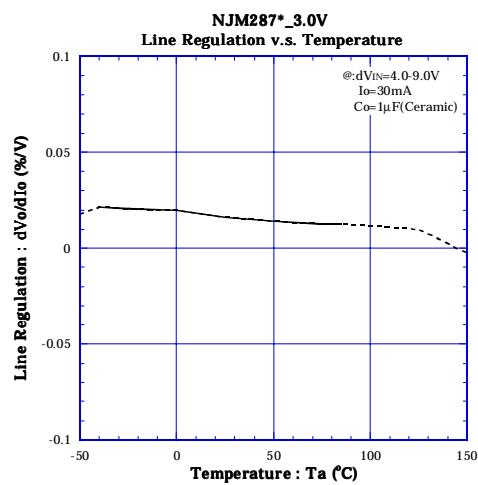
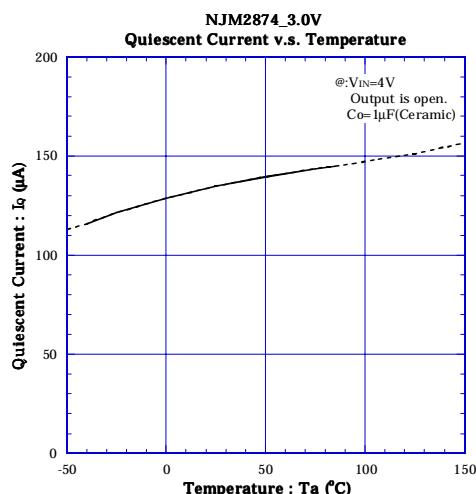
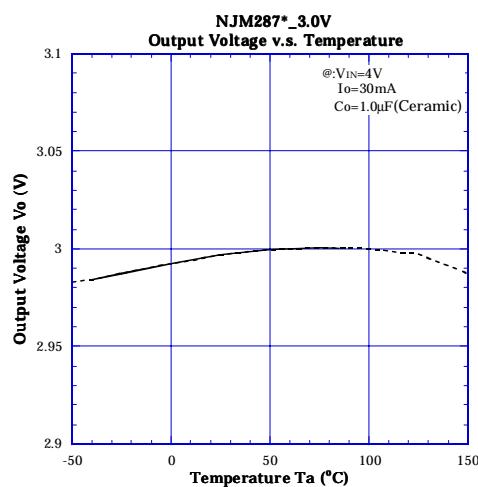
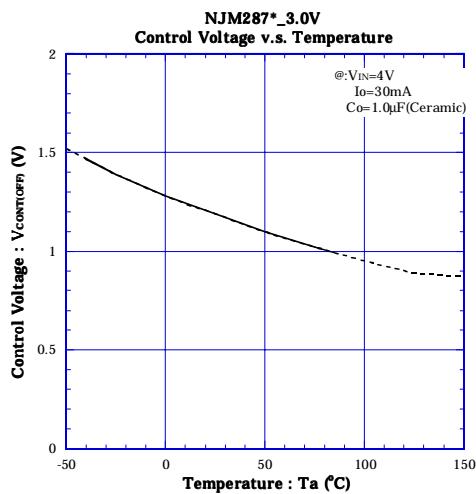
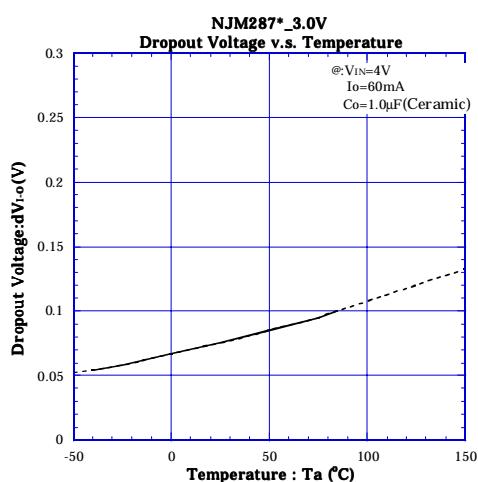
■ ELECTRICAL CHARACTERISTICS



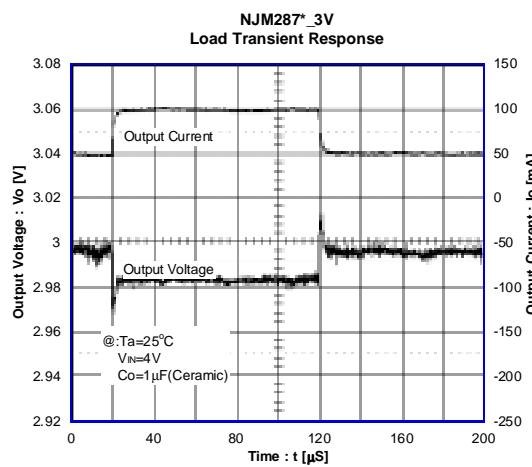
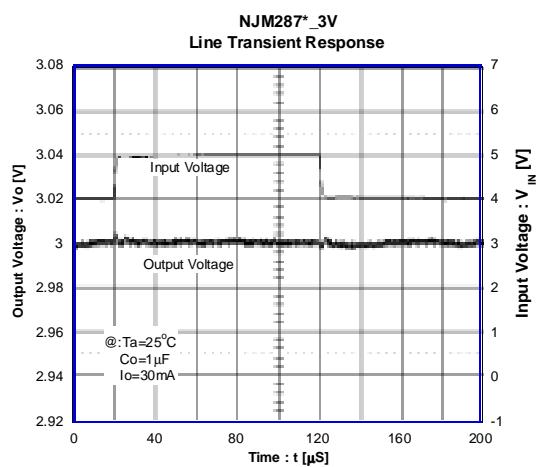
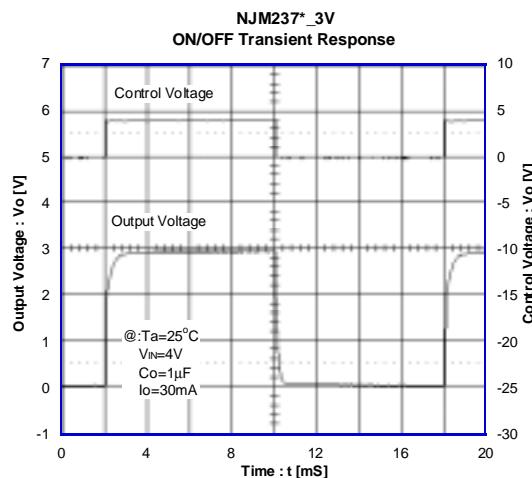
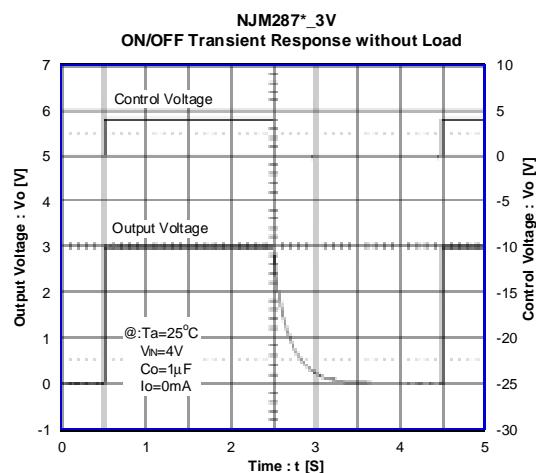
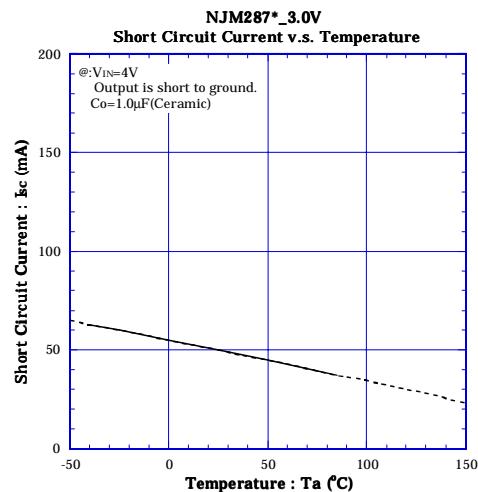
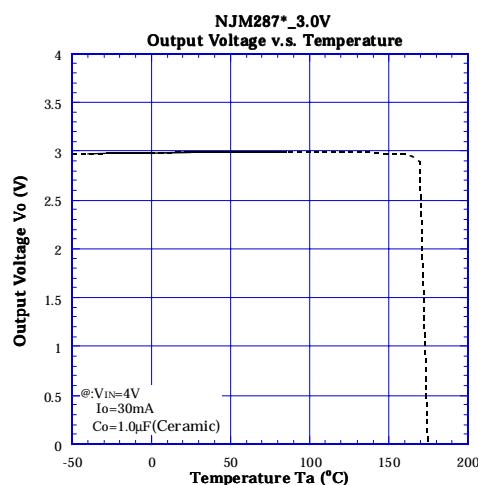
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