

## FRED

### Ultrafast Soft Recovery Diode Module, 200 A

#### FEATURES

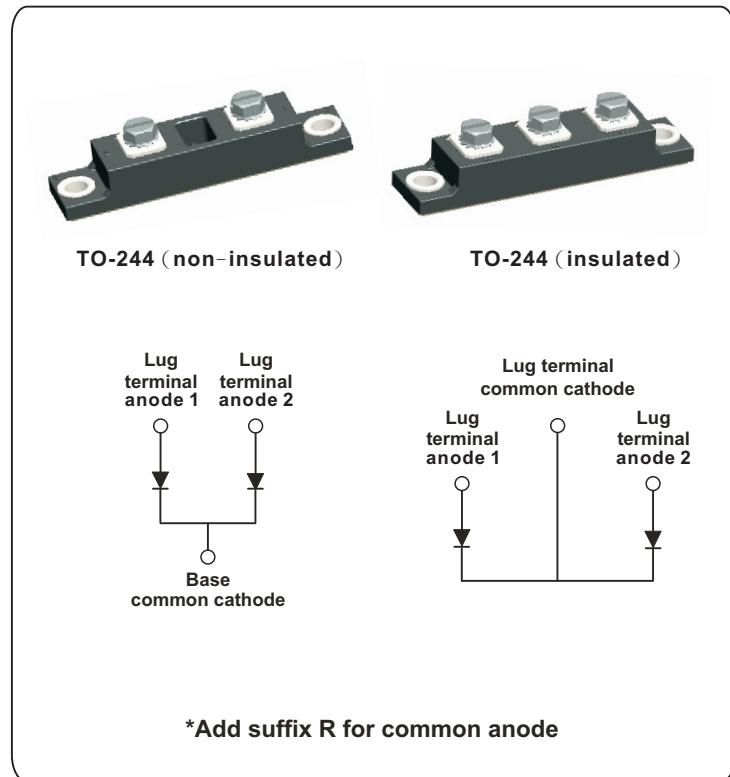
- Very low  $Q_{rr}$  and  $t_{rr}$
- Lead (Pb)-free
- Designed and qualified for industrial level
- Reduced RFI and EMI
- Reduced snubbing

#### DESCRIPTION

FRED diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. An extensive characterization of the recovery behavior for different values of current, temperature and  $dI/dt$  simplifies the calculations of losses in the operating conditions. The softness of the recovery eliminates the need for a snubber in most applications.

#### TYPICAL APPLICATIONS

- Power converters
- Motor drives
- Welder
- Switching power supplies



#### PRODUCT SUMMARY

$I_{F(AV)}$	200A
$V_R$	600V
$I_{F(DC)} \text{ at } T_C$	120A at 100 °C

#### ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS		MAX.	UNIT
Cathode to anode voltage	$V_R$			600	V
Average forward current	$I_{F(AV)}$	$T_C = 25^\circ\text{C}$ , per device		235	A
		$T_C = 115^\circ\text{C}$ per device		200	
		$T_C = 115^\circ\text{C}$ per leg		100	
DC forward current	$I_{F(DC)}$	$T_C = 100^\circ\text{C}$		120	
Single pulse forward current	$I_{FSM}$	Limited by junction temperature, per leg		1400	
Non-repetitive avalanche energy	$E_{AS}$	$L = 100 \mu\text{H}$ , duty cycle limited by maximum $T_J$		2.2	mJ
Maximum power dissipation	$P_D$	$T_C = 25^\circ\text{C}$		463	W
		$T_C = 100^\circ\text{C}$		185	
Operating junction and storage temperature range	$T_J, T_{Stg}$			- 55 to 150	°C

ELECTRICAL SPECIFICATIONS ( $T_J = 25^\circ\text{C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Cathode to anode breakdown voltage	$V_{BR}$	$I_R = 100 \mu\text{A}$	600	-	-	
Maximum forward voltage	$V_{FM}$	$I_F = 100 \text{ A}$	-	1.20	1.35	V
		$I_F = 200 \text{ A}$	-	1.35	1.50	
		$I_F = 100 \text{ A}, T_J = 125^\circ\text{C}$	-	1.0	1.15	
Maximum reverse leakage current per leg	$I_{RM}$	$T_J = 125^\circ\text{C}, V_R = 600\text{V}$	-	1.8	6.0	mA
		$T_J = 25^\circ\text{C}, V_R = 600\text{V}$	-	5.0	50	$\mu\text{A}$
Junction capacitance	$C_T$	$V_R = 200\text{V}$	-	200	300	$\text{pF}$
Series inductance	$L_S$	From top of terminal hole to mounting plane	-	6.0	-	nH
Maximum RMS insulation voltage (for insulated type)	$V_{INS}$	50Hz	-	-	3000(1min) 3600(1s)	V

DYNAMIC RECOVERY CHARACTERISTICS PER LEG ( $T_J = 25^\circ\text{C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Reverse recovery time	$t_{rr}$	$I_F = 0.5\text{A}, I_R = 1.0\text{A}, I_{RR} = 0.25\text{A}$	-	95	110	
		$I_F = 1.0\text{A}, dI_F/dt=200\text{A}/\mu\text{s}, V_R = 30\text{V}$	-	70	-	ns
		$T_J = 25^\circ\text{C}$	-	90	140	
		$T_J = 125^\circ\text{C}$	-	160	240	
Peak recovery current	$I_{RRM}$	$T_J = 25^\circ\text{C}$	-	10	18	A
		$T_J = 125^\circ\text{C}$	-	15	30	
Reverse recovery charge	$Q_{rr}$	$T_J = 25^\circ\text{C}$	-	450	1300	nC
		$T_J = 125^\circ\text{C}$	-	1200	3600	
Peak rate of recovery current	$dI_{(rec)M}/dt$	$T_J = 25^\circ\text{C}$	-	310	-	$\text{A}/\mu\text{s}$
		$T_J = 125^\circ\text{C}$	-	240	-	

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Maximum junction and storage temperature range	$T_J, T_{stg}$	-55	-	150	$^\circ\text{C}$	
Thermal resistance, junction to case per leg	TO-244 (non-insulated)	$R_{thJC}$	-	-	0.27	$^\circ\text{C/W}$
	TO-244 (insulated)		-	-	0.40	
Thermal resistance, junction to case per module	TO-244 (non-insulated)	$R_{thJC}$	-	-	0.135	$^\circ\text{C/W}$
	TO-244 (insulated)		-	-	0.20	
Typical thermal resistance, case to heatsink	$R_{thCS}$	-	0.10	-		
Weight	TO-244 (non-insulated)		-	80 (2.82)	-	g (oz.)
	TO-244 (insulated)		-	95 (3.36)	-	
Mounting torque <sup>(1)</sup>		30 (3.4)	-	40 (4.6)		$\text{lbf} \cdot \text{in}$ ( $\text{N} \cdot \text{m}$ )
Mounting torque center hole		12 (1.4)	-	18 (2.1)		
Terminal torque		30 (3.4)	-	40 (4.6)		
Vertical pull		-	-	80		$\text{lbf} \cdot \text{in}$
2" lever pull		-	-	35		

**Note**

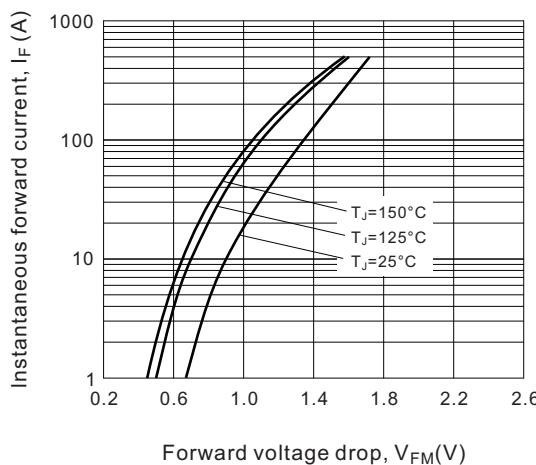
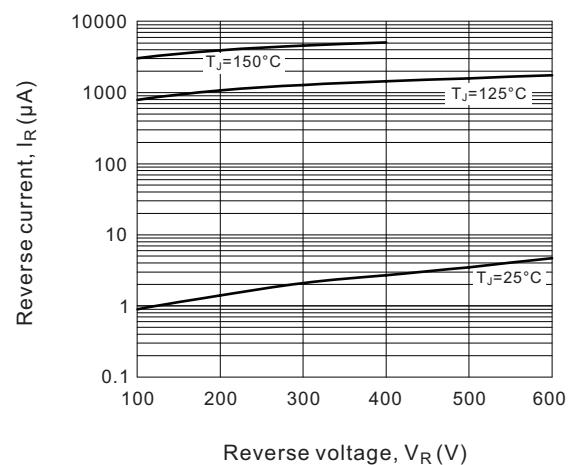
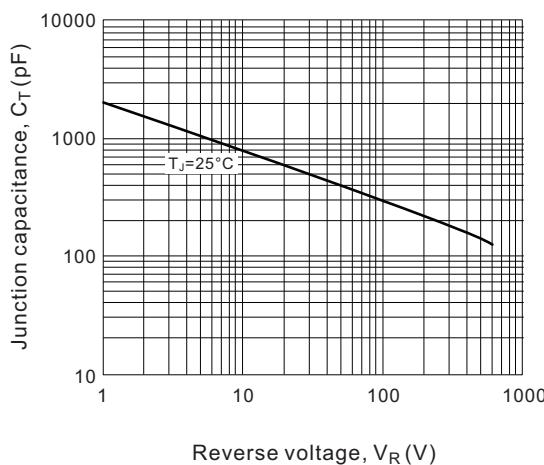
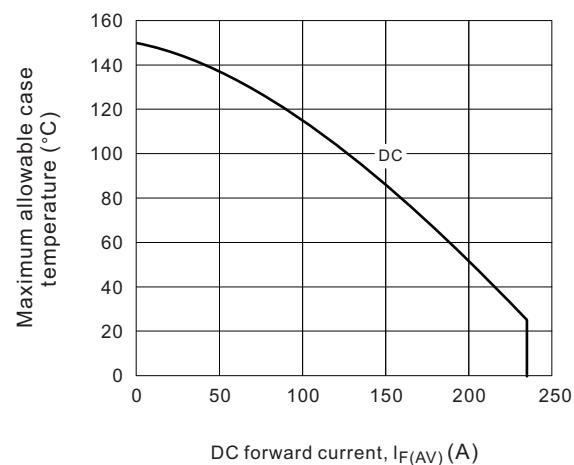
(1)Mounting surface must be smooth, flat, free of burrs or other protrusions. Apply a thin even film or thermal grease to mounting surface.

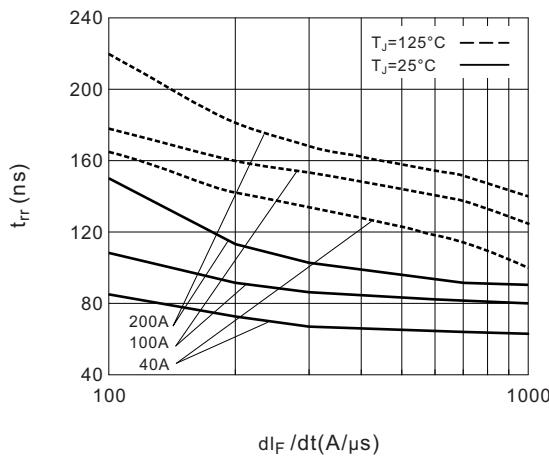
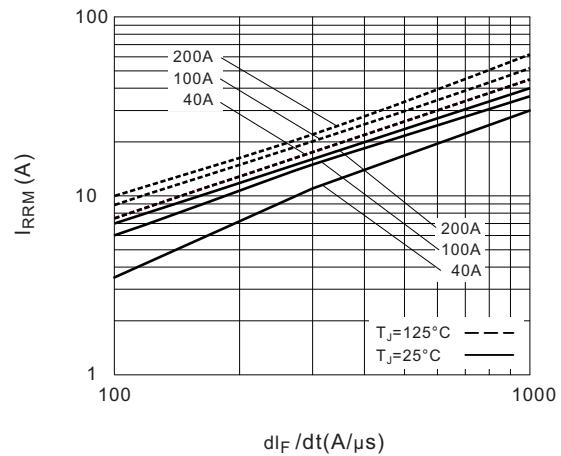
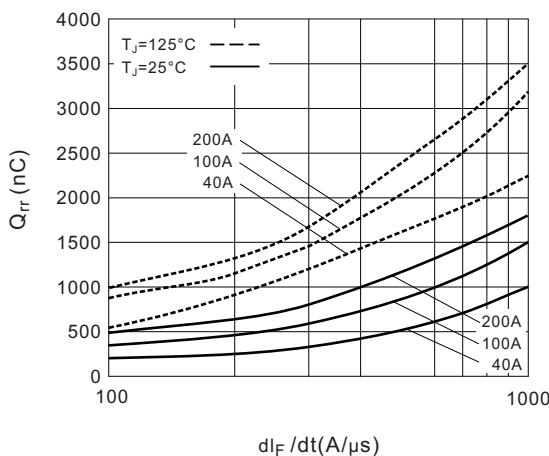
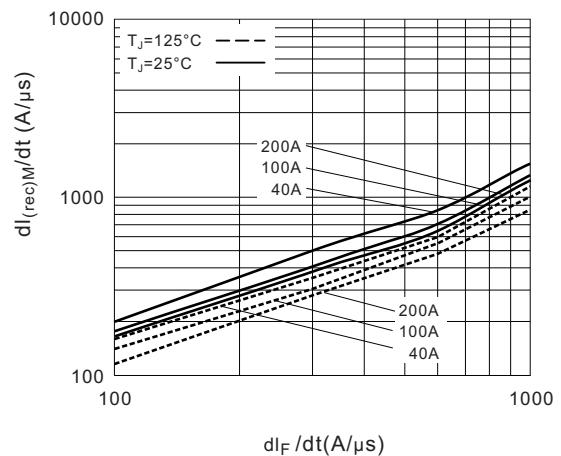
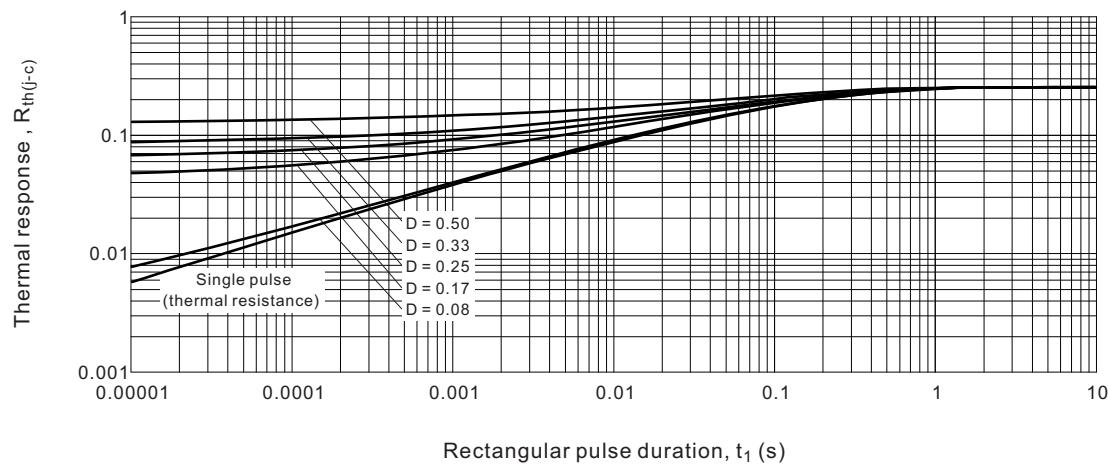
Gradually tighten each mounting bolt in 5 to 10 lbf. in steps until desired or maximum torque limits are reached

**Ordering Information Table**

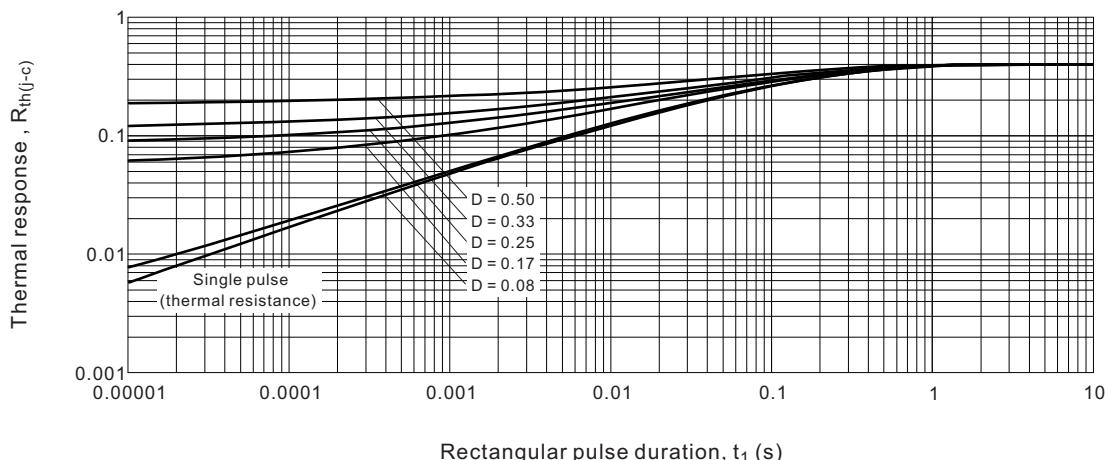
Device code	NK	F	D	200	-	60	R	I
	1	2	3	4		5	6	7

- [1] - NELL's power module
- [2] - F for Ultrafast soft recovery diode
- [3] - D for Dual Diodes, TO-244 Package
- [4] - Maximum average forward current, A
- [5] - Voltage rating (60 = 600V)
- [6] - None for common cathode configuration  
"R" for common anode configuration
- [7] - None for non-insulated type  
"I" for insulated type

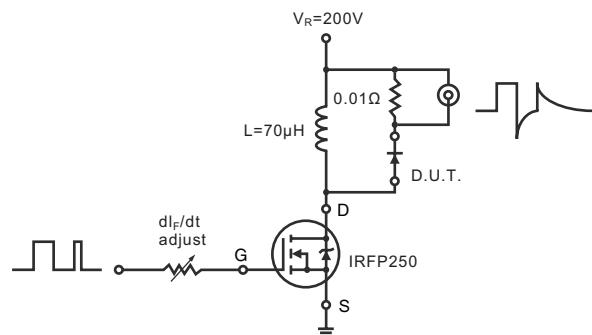
**Fig.1 Maximum forward voltage drop vs.  
Instantaneous forward current (per leg)**

**Fig.2 Typical reverse current vs.  
reverse voltage (per leg)**

**Fig.3 Typical junction capacitance vs.  
reverse voltage (per leg)**

**Fig.4 Maximum allowable case temperature vs.  
DC forward current (per leg)**


**Fig.5 Typical reverse recovery time vs.  $dI_F/dt$  (per leg)**

**Fig.6 Typical recovery current vs.  $dI_F/dt$  (per leg)**

**Fig.7 Typical stored charge vs.  $dI_F/dt$  (per leg)**

**Fig.8 Typical  $dI_{(rec)M}/dt$  vs.  $dI_F/dt$  (per leg)**

**Fig.9-1 Maximum thermal impedance  $R_{th(j-c)}$  characteristics (per leg, for TO-244 non-insulated)**


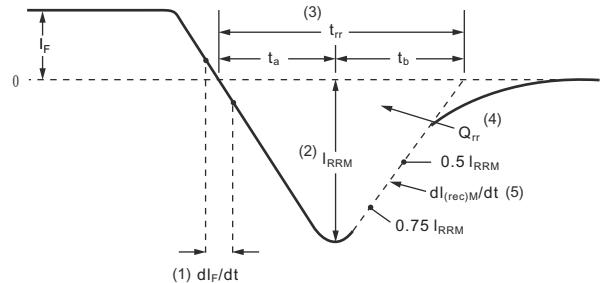
**Fig.9-2 Maximum thermal impedance  $R_{th(j-c)}$  characteristics  
(per leg, for TO-244 insulated)**



**Fig.10 Reverse recovery parameter test circuit**



**Fig.11 Reverse recovery waveform and definitions**



- (1)  $dl_F/dt$  - rate of change of current through zero crossing
  - (2)  $I_{RRM}$  - peak reverse recovery current
  - (3)  $t_{rr}$  - reverse recovery time measured from zero crossing point of negative going  $I_F$  to point where a line passing through  $0.75 I_{RRM}$  and  $0.5 I_{RRM}$  extrapolated to zero current.
  - (4)  $Q_{rr}$  - area under curve defined by  $t_{rr}$  and  $I_{RRM}$
  - (5)  $dl_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$
- $$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

**Fig.12 Avalanche test circuit and waveforms**

