

EMG2 / UMG2N / FMG2A

●Features

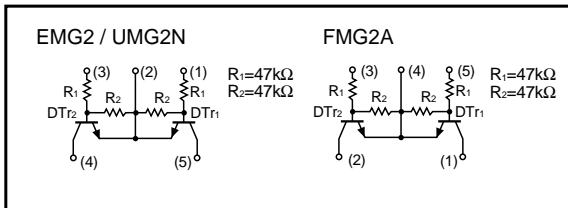
- 1) Two DTC144E chips in a EMT or UMT or SMT package.
- 2) Mounting cost and area can be cut in half.

●Structure

Dual NPN digital transistor
(each with a single built in resistors)

The following characteristics apply to both the DTr1 and DTr2.

●Equivalent circuit



●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit
Supply voltage	V _{CC}	50	V
Input voltage	V _{IN}	40	V
		-10	
Output current	I _o	30	mA
	I _{c (Max.)}	100	
Power dissipation	EMG2, UMG2N	150 (TOTAL)	mW
	FMG2A	300 (TOTAL)	
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

*1 120mW per element must not be exceeded.
*2 200mW per element must not be exceeded.

●External dimensions (Unit : mm)

