# **MORNSUN®**

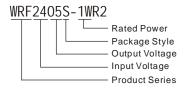
# WRE S - 1WR2 & WRF S-1WR2 Series 1W, WIDE INPUT, ISOLATED & REGULATED **DUAL/SINGLE OUTPUT DC-DC CONVERTER**





Patent Protected RoHS

## PART NUMBER SYSTEM



## **FEATURES**

- Ultra-Miniature SIP Package
- 2:1 wide input voltage range
- Operating temperature range: -40°C ~ +85°C
- 3KVDC isolation
- Short circuit protection(automatic recovery)
- External On/Off control
- High Power Density

## **APPLICATION**

The WRE\_S-1WR2 & WRF\_S-1WR2 Series are specially designed for applications where a wide range input voltage power supplies are isolated from the input power supply in a distributed power supply system on a circuit board. For these DC-DC converters, You can reduce the design point of failure and save the development of micro power supply's manpower, material and time costs, also better ensure product quality stability, protect safety and reliability of the end of products.

These products apply to where:

- Input voltage range ≤2:1;
- 2) Input and output isolation ≤3KVDC;
   3) Regulated and low ripple noise is required. Such as: industrial control, telecommunication etc.

SELECTION G	Input Volta	ge(VDC)		Output Cu	rrent (mA)	Input Current	(mA)(typ.)	Reflected		Efficienc
Model	Nominal (Range)	Max. <sup>①</sup>	Output Voltage (VDC)	Max.	Min.	@Max. Load	@No Load	Reflected Ripple Current (mA,typ.)	Max. Capacitive Load <sup>2</sup> (µF)	(%, typ. @Max Load
WRE0505S-1WR2			±5	±100	±5	274			1000	73
WRE0512S-1WR2			±12	±42	±2	263			470	76
WRE0515S-1WR2	5	11	±15	±33	±2	267	25	30	330	75
WRF0505S-1WR2	(4.5-9)	11	5	200	10	278	25	30	2200	72
WRF0512S-1WR2			12	83	4	263			1000	76
WRF0515S-1WR2			15	67	3	267			680	75
WRE1205S-1WR2			±5	±100	±5	107			1000	78
WRE1212S-1WR2			±12	±42	±2	103			470	81
WRE1215S-1WR2		,	±15	±33	±2	104			330	80
WRF1203S-1WR2	12	20	3.3.	303	15	112	12	40	2700	75
WRF1205S-1WR2	(9-18)	20	5	200	10	108		40	2200	77
WRF1209S-1WR2			9	111	6	106			1800	79
WRF1212S-1WR2			12	83	4	104			1000	80
WRF1215S-1WR2			15	67	3	104			680	80
WRE2405S-1WR2			±5	±100	±5	52			1000	80
WRE2412S-1WR2			±12	±42	±2	52			470	80
WRE2415S-1WR2			±15	±33	±2	52			330	80
WRF2403S-1WR2	24	40	3.3	303	15	56	6		2700	75
WRF2405S-1WR2	(18-36)	40	5	200	10	54	6	55	2200	77
WRF2412S-1WR2	1		12	83	4	51			1000	81
WRF2415S-1WR2			15	67	3	53			680	79
WRF2424S-1WR2			24	42	2	54			470	77
WRE4805S-1WR2			±5	±100	±5	27			1000	76
WRE4812S-1WR2	1		±12	±42	±2	26			470	80
WRE4815S-1WR2	48 (36-75)		80	±15	±33	±2	26	4 70	330	80
WRF4803S-1WR2	(55 75)		3.3	303	15	28			2700	75
WRF4805S-1WR2			5	200	10	27			2200	76

WRF4812S-1WR2	48	80	12	83	4	26	4	1	70	1000	81
WRF4815S-1WR2	(36-75)	80	15	67	3	26	4	70	680	80	
Note: 1) Absolute maximum rating without damage on the converter, but it isn't recommended:											

Note: ①. Absolute maximum rating without damage on the converter, but it isn't recommended; ②. For dual output converter, the given value is the same for each output.

Test Conditions	Min.	Тур.	Max.	Unit		
5V input	-0.7		12			
12V input	-0.7		25			
24V input	-0.7		50			
48V input	-0.7		100	VDC		
5V input	3.5	4	4.5	VDC		
12V input	4.5	8	9			
24V input	11	16	18			
48V input	24	33	36			
	C Filter					
Models ON	Ctrl open or be insulated					
Models OFF	Connect high level voltage, and ensure the current into Ctrl to be 5-10m/					
	12V input 24V input 48V input 5V input 12V input 24V input 48V input 48V input	12V input -0.7 24V input -0.7 48V input -0.7 5V input 3.5 12V input 4.5 24V input 11 48V input 24  Models ON	12V input -0.7 24V input -0.7 48V input -0.7 5V input 3.5 4 12V input 4.5 8 24V input 11 16 48V input 24 33  C F Models ON Ctrl open or	12V input     -0.7      25       24V input     -0.7      50       48V input     -0.7      100       5V input     3.5     4     4.5       12V input     4.5     8     9       24V input     11     16     18       48V input     24     33     36       C Filter       Models ON     Ctrl open or be insulated		

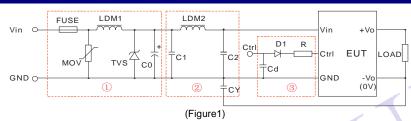
<b>OUTPUT SPECIFICATIO</b>	NS					
Item	Test Conditions	Min.	Тур.	Max.	Unit	
Output Voltage Accuracy	5% to 100% load		±1	±3		
No-load Output Voltage Accuracy	Input voltage range		±1.5	±5		
Output Voltage Balance	Dual output, balanced loads		±0.3	±0.5	%	
Line Regulation	Full load, Input voltage from low to high		±0.2	±0.5		
Load Regulation	5% to 100% load		±0.4	±0.75		
Transient Recovery Time	OFOV local step shows		0.5	2	ms	
Transient Response Deviation	25% load step change		±2.5	±5	%	
Temperature coefficient	100% load		±0.02	±0.03	%/°C	
Ripple *	20MHz Bandwidth		30	50	mVp-p	
Noise*	ZUWITZ DaliuwiuiTi		50	100	iiivp-p	
Output Short Circuit Protection	utput Short Circuit Protection Continuous, automatic recovery					
Note:*Ripple and noise tested with "pa	arallel cable" method. See detailed operation instructions at	DC-DC application	n notes.			

COMMON SPECIFICATIONS								
Item	Test Conditions	Min.	Тур.	Max.	Unit			
Isolation Voltage	Input-Output, Tested for 1 minute, leakage current less than 1 mA	3000			VDC			
Isolation Resistance	Input-Output, Test at 500VDC	1000			ΜΩ			
Isolation Capacitance	Input-Output,100KHz/0.1V		30	50	pF			
Switching Frequency(PFM Mode)	100% load, Nominal Input voltage		200		KHz			
MTBF	MIL-HDBK-217F@25°C	1000			K hours			
Case Material		Plastic (UL94-V0)						
Weight			4.9		g			

ENVIRONMENTAL SPECIFICATIONS							
Item	Test Conditions	Min.	Тур.	Max.	Unit		
Storage Humidity	Non condensing			95	%		
Operating Temperature	Power derating (above85℃, see Figure 5)	-40		85			
Storage Temperature		-55		125	°C		
Temp. rise at full load	Ta=25°C		25				
Lead Temperature	1.5mm from case for 10 seconds			300			
Cooling		Free air convection					

EMC SI	PECIFICATIONS			
EMI	CE	CISPR22/EN55022	CLASS B (Recommended Circuit Refer to Figure 1-2) or Figure 3)	
LIVII	RE	CISPR22/EN55022	CLASS B (Recommended Refer to Figure 1-2 or Figure 3)	
	ESD	IEC/EN61000-4-2	Contact ±4KV	perf. Criteria B
	RS	IEC/EN61000-4-3	10V/m	perf. Criteria A
	EFT	IEC/EN61000-4-4	±2KV (Recommended Circuit Refer to Figure1-①)	perf. Criteria B
EMS		IEC/EN61000-4-4	±4KV (Recommended Circuit Refer to Figure 3)	perf. Criteria B
	Surge	IEC/EN61000-4-5	±2KV (Recommended Circuit Refer to Figure1-① or Figure 3)	perf. Criteria B
	CS	IEC/EN61000-4-6	3 Vr.m.s	perf. Criteria A
	Voltage dips, short and interruptions immunity	IEC/EN61000-4-29	0%-70%	perf. Criteria B

# **EMC RECOMMENDED CIRCUIT**



#### Recommended external circuit parameters:

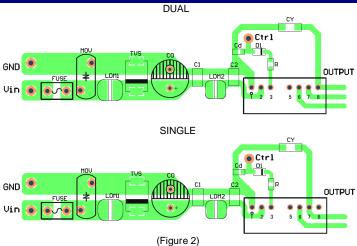
John Honaca ox	terrial elleant parameters.							
Model	Vin:5V	Vin:12V	Vin:24V	Vin:48V				
FUSE	Choose according to practical input current							
MOV			S14K35	S14K60				
LDM1			56µH	56µH				
TVS	SMCJ13A	SMCJ28A	SMCJ48A	SMCJ90A				
C0	680µF/16V	680μF/25V	330µF/50V	330μF/100V				
C1	4.7μF/50V 4.7μF/100V							
LDM2	12µH							
C2	4.7μF/50V 4.7μF/100V							
CY		1nF/3KV						
D1	RB160M-60/1A							
R	Follows: $R = \frac{V_C - V_D - 1.0}{I_C} - 300$							
Cd	47nF/100V							

Note:1.In Figure 1,part ① is EMS recommended external circuit, part ② is EMI recommended external circuit. Choose according to requirements;

2.V<sub>C</sub> is the voltage to GND from Ctrl,V<sub>D</sub> is the forward conduction voltage drop of D1,I<sub>C</sub> is the current through Ctrl pin which is normally 5-10mA, the external circuit of Ctrl is as shown in figure1-③;

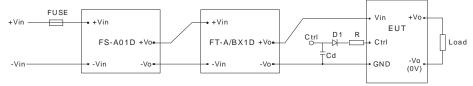
3. If there is no recommended parameters, the model no require the external component.

# **EMC RECOMMENDED CIRCUIT PCB LAYOUT**



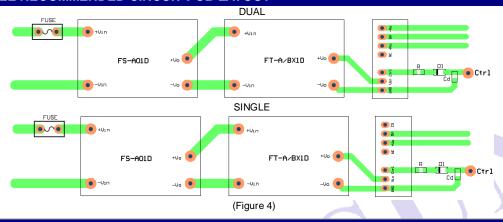
Note: The pad space between input and output GND (CY) must≥2mm.

# **EMC MODULE APPLICATION CIRCUIT**

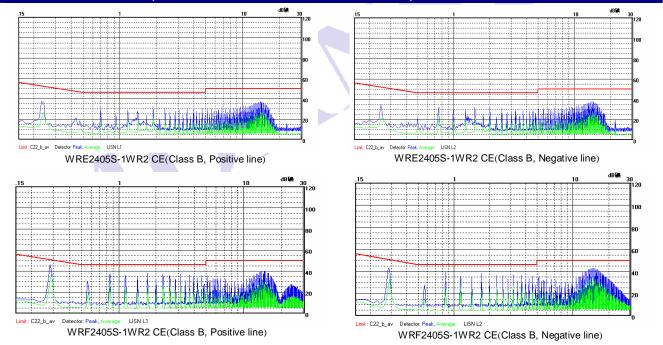


FS-A01D,FT-A/BX1D are MORNSUN's EFT suppressers (Figure 3)

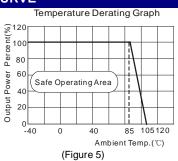
# **EMC MODULE RECOMMENDED CIRCUIT PCB LAYOUT**



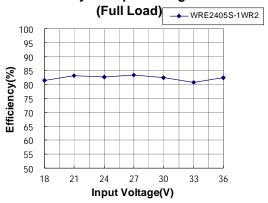
# EMI TEST WAVEFORM (RECOMMENDED CIRCUIT FINGURE 1-22)



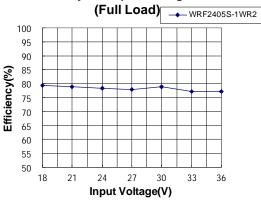
# PRODUCT TYPICAL PERFORMANCE CURVE



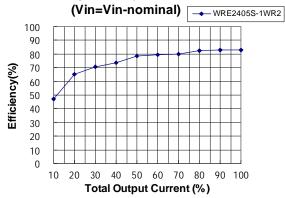
# **Efficiency VS Input Voltage curve**



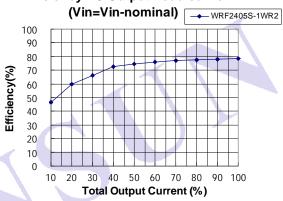
## Efficiency VS Input Voltage curve



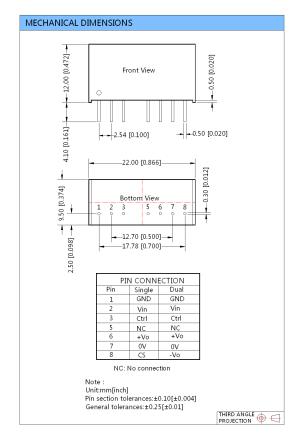
# **Efficiency VS Output Load curve**

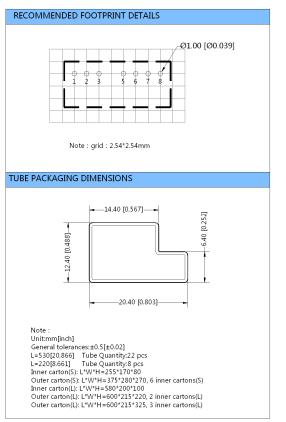


# **Efficiency VS Output Load curve**



# **OUTLINE DIMENSIONS, RECOMMENDED FOOTPRINT & PACKAGING**

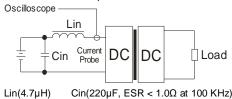




## **TEST CONFIGURATIONS**

## Input Reflected-Ripple Current Test Setup

Input reflected-ripple current is measured with an inductor Lin and Capacitor Cin to simulate the source impedance.



# **DESIGN CONSIDERATIONS**

#### 1) Requirement on output load

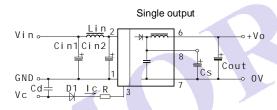
To ensure this module can operate efficiently and reliably, during operation, the minimum output load could not be less than 5% of the full load, otherwise output ripple maybe increase dramatically. If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load, suppose to use the resistance of 5% rated power,or use our company's products with a lower rated output power.

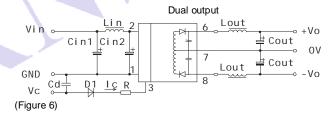
#### 2) Recommended Circuit

All the WRE\_S-1WR2 & WRF\_S-1WR2 series have been tested according to the following recommended test circuit before leaving the factory (see Figure 6).

If you want to further decrease the input/output ripple, you can increase a capacitance-values properly or choose capacitors with low ESR. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance must be less than the Max. Capacitive Load.

General: Cin1: 5V&12V 24V&48V 10µF Cin2: 5V&12V 47µF 24V&48V 1µF Lin: 4.7µH~12µH . 10μF~22μF Cs: 100μF(Typ.) Cout: Lout: 2.2µH~10µH Cd: 47nF/100V





#### 3) Ctrl Terminal

When open or high impedance, the converter works well; When this pin is 'high', the converter shut down. It should be note that the input current should be between 5-10mA, exceeding the maximum 20mA will cause permanent damage to the converter. The value of R can be derived as follows:

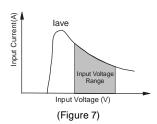
$$R = \frac{V_C - V_D - 1.0}{I_C} - 300$$

For Detailed parameter, please refer to "EMC RECOMMENDED CIRCUIT".

## 4) Input Current

When it is used in unregulated power supply, be sure that the fluctuating range of the power supply and the rippled voltage do not exceed the module standard. Input current of power supply should afford the flash startup average current of this kind of DC/DC module (Figure 7).

General: Vin=5V | lave =450mA Vin=12V | lave =220mA Vin=24V | lave =110mA Vin=48V | lave =55mA



5) It is not recommended to increase the output power capability by connecting two or more converters in parallel. The product is not hot-swappable

#### Note:

- 1. Min. load shouldn't be less than 5%, otherwise ripple maybe increased dramatically. If the product operates under min. load, it may not be guaranteed to meet all specifications listed. Operation under minimum load will not damage the converter.
- Recommended Dual output models unbalanced load is ≤±5%, if the product operates >±5%, it may not be guaranteed to meet all specifications listed. Please contact our technical support for more details.
- 3. Max. Capacitive Load is tested at input voltage range and full load.
- 4. All specifications measured at Ta=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.
- 5. In this datasheet, all test methods are based on our corporate standards.
- 6. All characteristics are for listed models, and non-standard models may perform differently. Please contact our technical support for more details.
- 7. Please contact our technical support for any specific requirement.
- 8. Specifications of this product are subject to changes without prior notice.

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