

Silicon NPN Power Transistors

2SC2365

DESCRIPTION

- With TO-3 package
- High breakdown voltage

APPLICATIONS

- For use in switch-mode CTV supply systems

PINNING(see Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

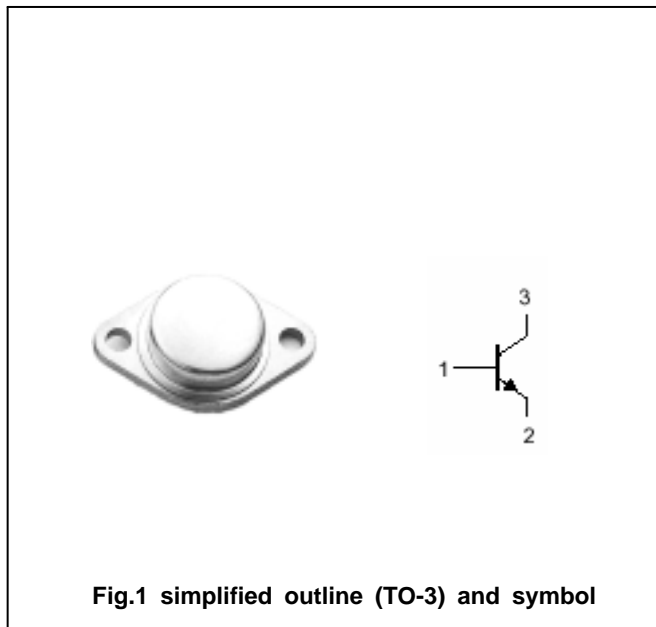


Fig.1 simplified outline (TO-3) and symbol

Absolute maximum ratings(Ta=)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	600	V
V_{CEO}	Collector-emitter voltage	Open base	500	V
V_{EBO}	Emitter-base voltage	Open collector	6	V
I_C	Collector current		6	A
I_{CM}	Collector current-peak		8	A
P_C	Collector power dissipation	$T_C=25$	50	W
T_j	Junction temperature		150	
T_{stg}	Storage temperature		-55~150	

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CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =10mA ; I _B =0	500			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =1mA ; I _C =0	6			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =4A; I _B =1.25A			3.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =4A; I _B =1.25A			1.6	V
I _{CBO}	Collector cut-off current	V _{CB} =600V; I _E =0			0.1	mA
I _{EBO}	Emitter cut-off current	V _{EB} =6V; I _C =0			0.1	mA
h _{FE}	DC current gain	I _C =3A ; V _{CE} =4V	12			
f _T	Transition frequency	I _C =0.5A ; V _{CE} =10V		10		MHz

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PACKAGE OUTLINE



Fig.2 outline dimensions (unindicated tolerance: ± 0.1 mm)