

Silicon NPN Power Transistors

2N6654

DESCRIPTION

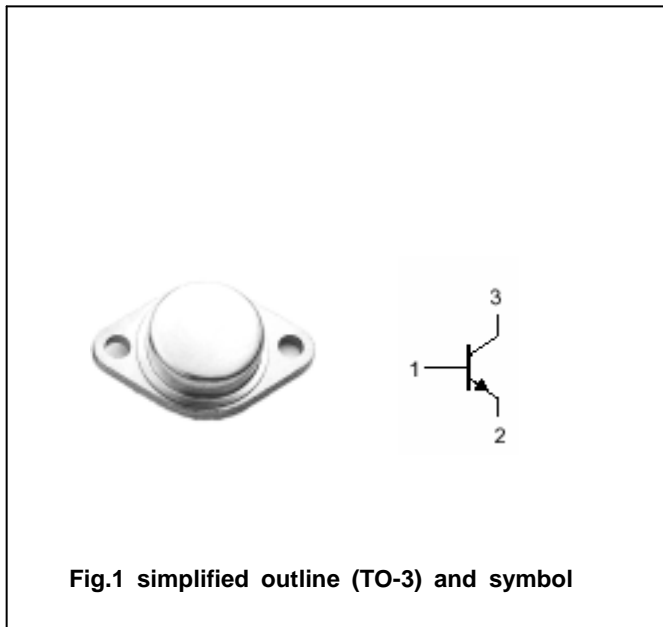
- With TO-3 package
- High voltage capability
- Fast switching speeds
- Low saturation voltage

APPLICATIONS

- Switching regulators
- Inverters
- Solenoid and relay drivers
- Deflection circuits

PINNING (See Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector



MAXIMUM RATINGS(Ta=25)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	500	V
V _{CEO}	Collector-emitter voltage	Open base	350	V
V _{EBO}	Emitter-base voltage	Open collector	6	V
I _C	Collector current		20	A
I _{CM}	Collector current-peak		30	A
P _T	Total power dissipation	T _c =25	150	W
T _j	Junction temperature		200	
T _{stg}	Storage temperature		-65~200	

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal resistance from junction to case	1.0	/W

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CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-emitter sustaining voltage	I _C =0.1A ; I _B =0	350			V
V _{(BR)CBO}	Collector-emitter breakdown voltage	I _C =1mA ; I _E =0	500			V
V _{CEsat-1}	Collector-emitter saturation voltage	I _C =10A; I _B =2A			1.8	V
V _{CEsat-2}	Collector-emitter saturation voltage	I _C =15A; I _B =3A			2.2	V
V _{BEsat}	Base-emitter saturation voltage	I _C =15A; I _B =3A			1.8	V
I _{CEV}	Collector cut-off current	V _{CE} =500V; V _{BE(off)} =-1.5V T _C =150			0.1 2.0	mA
I _{EBO}	Emitter cut-off current	V _{EB} =6V; I _C =0			0.1	mA
h _{FE-1}	DC current gain	I _C =1A ; V _{CE} =5V	15		50	
h _{FE-2}	DC current gain	I _C =10A ; V _{CE} =15V	10			

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PACKAGE OUTLINE

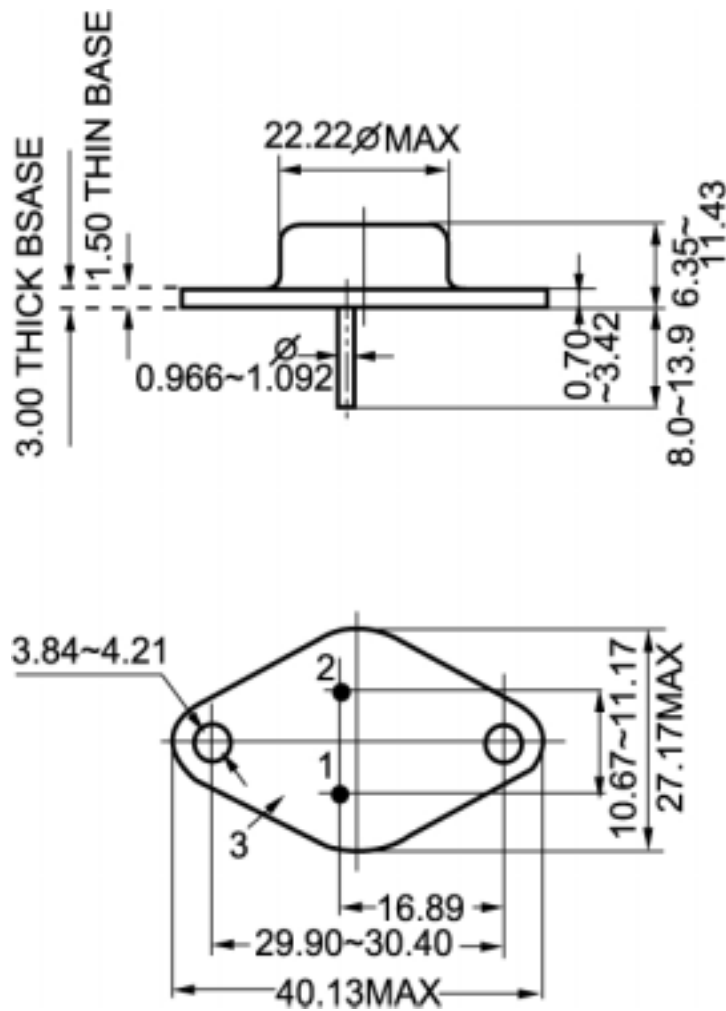


Fig.2 Outline dimensions