

Hutton Close, Crowther Ind Est, Dist 3, Washington, Tyne & Wear NE38 0AH, England Email: isocom@dial.pipex.com - Tel: +44 0191 4166546 - Fax: +44 0191 4155055

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MOC3030-3032 Optically Coupled Bilateral Switch Light Activated Zero Voltage Crossing Triac

Circuit



Features

Photo-Triac Output

7500 V Isolation 250 Vpeak Blocking Voltage Low Cost Dual-in-Line Package Zero Voltage Crossing U.L. Recognized, File No. E91231

Description

The MOC303X series are optically coupled isolators consisting of a Gallium Arsenide infrared emitting diode coupled with a monolithic silicon detector performing the functions of a zero crossing bilateral triac mounted in a standard 6-pin dual-in-line package. Surface Mount Option Available.

All electrical parameters are 100% tested. Specifications are guaranteed to a cumulative 0.65% AQL.

Absolute Maximum Ratings (Ta=25°C)

Storage Temperature: $-40^{\circ}\text{C} \text{ to } +150^{\circ}\text{C}$ Operating Temperature: $-40^{\circ}\text{C} \text{ to } +85^{\circ}\text{C}$

Lead Soldering: 260°C for 10s, 1.6mm from case

Input-to-Output Isolation Voltage (Peak): 7500Vac (60Hz, for 5s)

Input Diode

Forward DC Current: 50mA Reverse DC Voltage: 3V Power Dissipation: 120mW

Derate Linearly: 1.33mW/°C above 25°C

Output Photo Triac

Off-State Output Terminal Voltage: 250V RMS Forward Current: 100mA

Forward Current (Peak): 1.2A (p.w.=10ms)

Power Dissipation: 300mW

Derate Linearly: 4.0mW/°C above 25°C

Package

Total Power Dissipation: 330mW

Derate Linearly: 4.4mW/°C above 25°C

Electro-optical Characteristics (Ta=25°C)

INPUT	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
V_{F}	Forward Voltage	$I_F = 30 \text{mA}$			1.5	V
I_R	Reverse Current	$V_R = 3V$			100	μΑ
OUTPUT PHOTO TRIAC						
I DRM1	Peak Off-State Current	$V_{DRM} = 250V$, note 1			100	nA
V _{DRM}	Peak Blocking Voltage	I _{DRM1} =100nA	250			V
V _{TM}	On-State Voltage	I _{TM} =100mA (Peak)		2.3	3.0	V
dV/dt (C)	Critical Rate of Rise of Commutating Off-State Voltage			100		V/µs
COUPLED	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
I _{FT}	Input Current to Trigger					
	MOC3030	Main Terminal Voltage=3V, note 2			30	mA
	MOC3031				15	mA
	MOC3032				10	mA
	Holding Current, either direction			100		μΑ
	Input-Output Isolation Voltage		7500			Vac
ZERO CROSSING CHARACTERISTICS						
V _{IH}	Inhibit Voltage	I _F =Rated I _{FT} ; MT-1, MT-2 voltage above which device will not trigger		15	25	V
I DRM2	Leakage Inhibited State	I _F =Rated I _{FT} , V _{DRM} =250V, off-state		100	200	μΑ

Notes

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- 1. Test voltage must be applied with dV/dt rating.
- 2. Guaranteed to trigger @ I_F value <= max I_F . Recommended I_F lies between max I_F and absolute max I_F

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