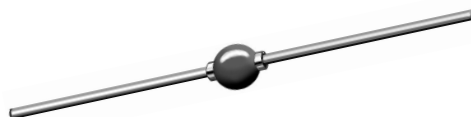


## High Voltage Sinterglas Rectifier

### Features

- High temperature metallurgically bonded construction
- Cavity-free glass passivated junction
- 1.0 ampere operation at  $T_{amb} = 100\text{ }^{\circ}\text{C}$  with no thermal runaway
- Typical  $I_R$  less than  $0.1\text{ }\mu\text{A}$
- Hermetically sealed package
- Capable of meeting environmental standards of MIL-S-19500
- High temperature soldering guaranteed:  $350\text{ }^{\circ}\text{C}$ /10 seconds, 0.375 " (9.5 mm) lead length, 5 lbs. (2.3 kg) tension



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### Mechanical Data

**Case:** JEDEC DO-204AP Solid glass body

**Terminals:** Solder plated axial leads, solderable per MIL-STD-750, Method 2026

**Polarity:** Color band denotes cathode end

**Mounting Position:** Any

**Weight:** 560 mg

### Parts Table

Part	Type differentiation
GI1-1200	$V_{RRM} = 1200\text{ V}$
GI1-1400	$V_{RRM} = 1400\text{ V}$
GI1-1600	$V_{RRM} = 1600\text{ V}$

### Absolute Maximum Ratings

$T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified

Parameter	Test condition	Sub type	Symbol	Value	Unit
Maximum repetitive peak reverse voltage		GI1-1200	$V_{RRM}$	1200	V
		GI1-1400	$V_{RRM}$	1400	V
		GI1-1600	$V_{RRM}$	1600	V
Maximum RMS voltage		GI1-1200	$V_{RMS}$	840	V
		GI1-1400	$V_{RMS}$	980	V
		GI1-1600	$V_{RMS}$	1120	V
Maximum DC blocking voltage		GI1-1200	$V_{DC}$	1200	V
		GI1-1400	$V_{DC}$	1400	V
		GI1-1600	$V_{DC}$	1600	V
Maximum average forward rectified current	0.375 " (9.5 mm) lead length at $T_{amb} = 75\text{ }^{\circ}\text{C}$		$I_{F(AV)}$	1.0	A
Peak forward surge current	8.3 ms single half sine-wave superimposed on rated load (JEDEC Method)		$I_{FSM}$	30	A

### Maximum Thermal Resistance

$T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified

Parameter	Test condition	Sub type	Symbol	Value	Unit
Typical thermal resistance <sup>1)</sup>			$R_{\theta JA}$	55	$^{\circ}\text{C}/\text{W}$
Operating junction and storage temperature range			$T_J, T_{STG}$	- 65 to + 175	$^{\circ}\text{C}$

<sup>1)</sup> Thermal resistance from junction to ambient and from junction to lead at 0.375 " (9.5mm) lead length, with both leads mounted between heat sinks.

### Electrical Characteristics

$T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified

Parameter	Test condition	Sub type	Symbol	Min	Typ.	Max	Unit
Maximum instantaneous forward voltage	$I_F = 1.0\text{ A}$		$V_F$			1.1	V
	$I_F = 3.14\text{ A}$		$V_F$			1.3	V
Maximum DC reverse current	at rated DC blocking voltage $T_{amb} = 25\text{ }^{\circ}\text{C}$		$I_R$			10	$\mu\text{A}$
	at rated DC blocking voltage $T_{amb} = 100\text{ }^{\circ}\text{C}$		$I_R$			100	$\mu\text{A}$
Typical junction capacitance	$V_R = 4.0\text{ V}$ , $f = 1\text{ MHz}$		$C_J$			15	pF
Maximum reverse recovery time	$I_{FM} = 20\text{ mA}$ , $I_R = 2\text{ mA}$		$t_{rr}$			25	$\mu\text{s}$
Maximum forward recovery time	$I_{FM} = 20\text{ mA}$		$t_{fr}$			1.0	$\mu\text{s}$

### Typical Characteristics ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)

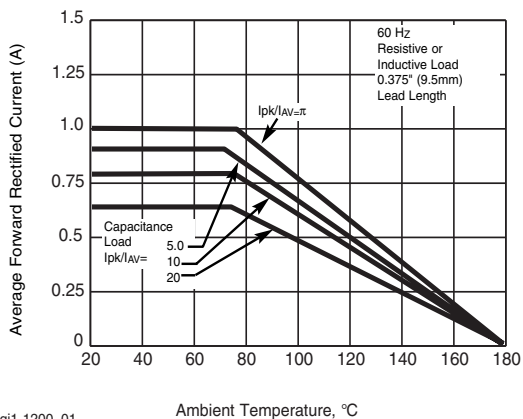


Figure 1. Forward Current Derating Curve

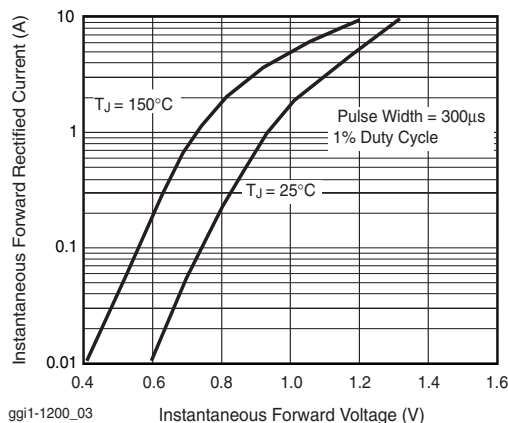


Figure 3. Typical Instantaneous Forward Characteristics

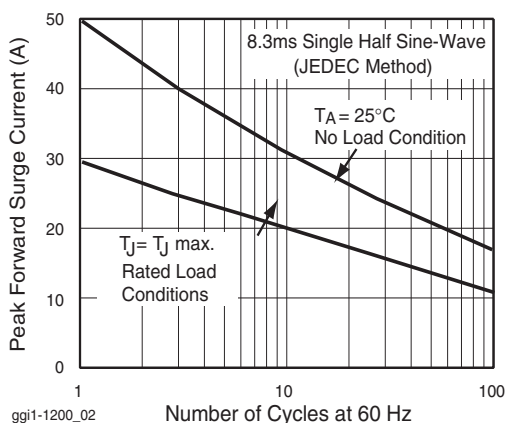


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current

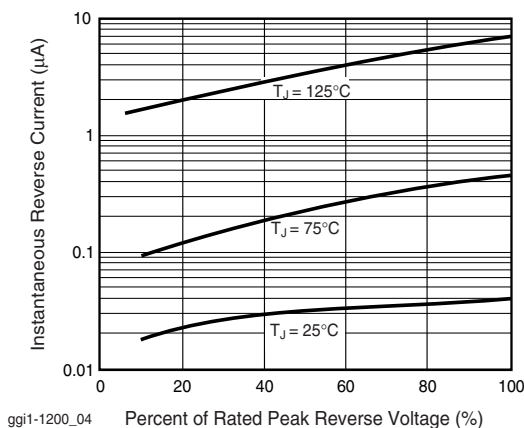


Figure 4. Typical Reverse Characteristics

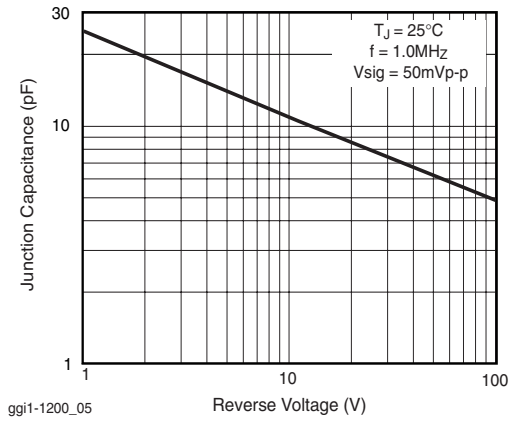
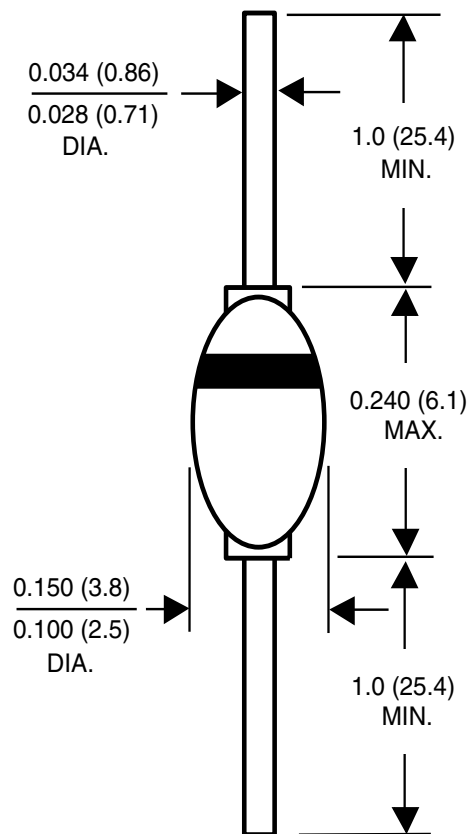


Figure 5. Typical Junction Capacitance

## Package Dimensions in mm



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### Ozone Depleting Substances Policy Statement

It is the policy of **Vishay Semiconductor GmbH** to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

**Vishay Semiconductor GmbH** has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

**Vishay Semiconductor GmbH** can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

**We reserve the right to make changes to improve technical design and may do so without further notice.**

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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