



**High-Speed CMOS  
32Kx8 SRAM with  
Fast Address Bit**

**QS83283  
ADVANCE  
INFORMATION**

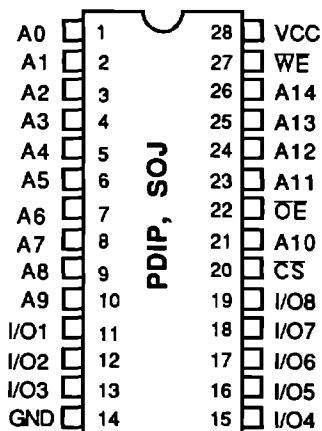
**FEATURES/BENEFITS**

- Equal access and cycle times
- 15ns/20ns/25/30 ns Commercial
- 20ns/25/30 ns Military
- Available in 28-pin 300/600-mil DIP, SOJ
- Military product compliant to MIL-STD-883
- Fast access on one address bit for MIPS R4000
- 6-Transistor cell for high reliability
- TTL compatible I/O
- High performance QCMOS™ technology

**DESCRIPTION**

The QS83283 is a high-speed 256K SRAM organized as 32K words of 8 bits. It has a fast access address pin (A10) which allows access to a second word in approximately half the access time for the first word. This is useful in MIPS R4000 RISC CPU secondary cache applications. It is manufactured in a high-performance CMOS process, and it based on a 6-transistor cell design for high reliability of data retention. The high-speed access times of the QS83283 make it useful in cache data RAM, cache tag RAMs, high-speed scratchpad memories, look-up tables, pipelined DSP and bit-slice systems. Low operating power and excellent latch-up and ESD protection are provided.

**PIN CONFIGURATION**



**ALL PINS TOP VIEW**