Ordering number : ENA1260A

SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

N-Channel Silicon MOSFET

MCH3477 — General-Purpose Switching Device Applications

Features

- Ultrahigh speed switching
- Halogen free compliance
- 1.8V drive
- Protection diode in

Specifications

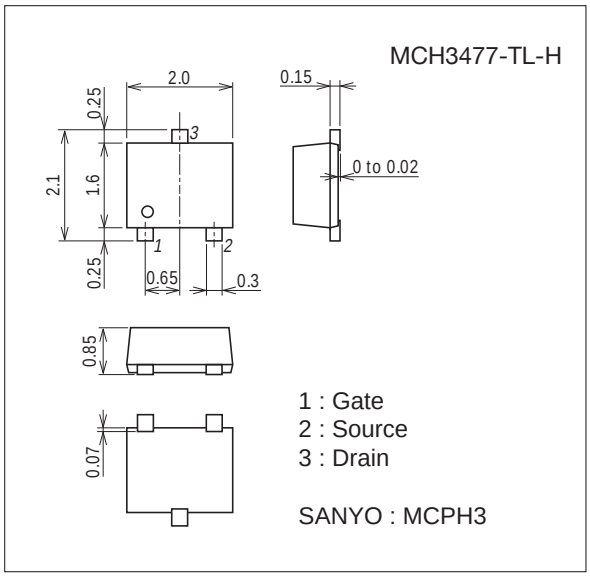
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		20	V
Gate-to-Source Voltage	V_{GSS}		±12	V
Drain Current (DC)	I_D		4.5	A
Drain Current (Pulse)	I_{DP}	PW≤10μs, duty cycle≤1%	18	A
Allowable Power Dissipation	P_D	When mounted on ceramic substrate (900mm²×0.8mm)	1.0	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		−55 to +150	°C

Package Dimensions

unit : mm (typ)

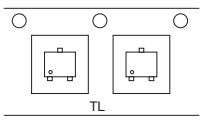
7019A-003



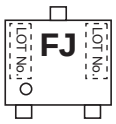
Product & Package Information

- Package : MCPH3
- JEITA, JEDEC : SC-70, SOT-323
- Minimum Packing Quantity : 3,000 pcs./reel

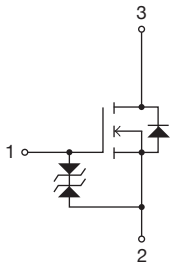
Packing Type : TL



Marking



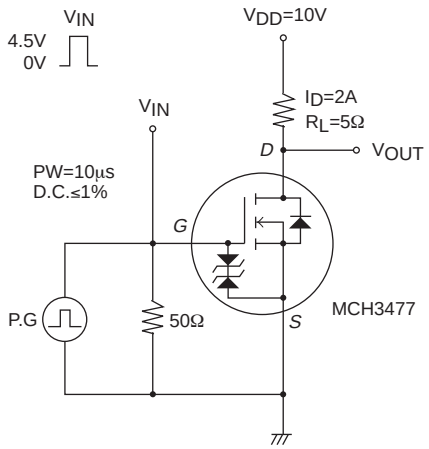
Electrical Connection



Electrical Characteristics at Ta=25°C

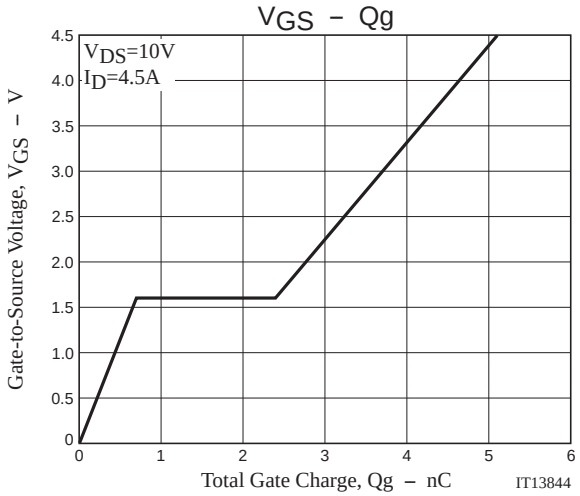
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=1mA, V_{GS}=0V	20			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=20V, V_{GS}=0V			1	μA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}=±8V, V_{DS}=0V			±10	μA
Cutoff Voltage	V_{GS}(off)	V_{DS}=10V, I_D=1mA	0.4		1.3	V
Forward Transfer Admittance	| yfs |	V_{DS}=10V, I_D=2A	2.0	3.4		S
Static Drain-to-Source On-State Resistance	R_{DS}(on)1	I_D=2A, V_{GS}=4.5V		29	38	mΩ
	R_{DS}(on)2	I_D=1A, V_{GS}=2.5V		43	61	mΩ
	R_{DS}(on)3	I_D=0.5A, V_{GS}=1.8V		69	99	mΩ
Input Capacitance	Ciss	V_{DS}=10V, f=1MHz		410		pF
Output Capacitance	Coss			84		pF
Reverse Transfer Capacitance	Crss			59		pF
Turn-ON Delay Time	t_{d}(on)	See specified Test Circuit.		7.5		ns
Rise Time	t_r			26		ns
Turn-OFF Delay Time	t_{d}(off)			38		ns
Fall Time	t_f			32		ns
Total Gate Charge	Qg	V_{DS}=10V, V_{GS}=4.5V, I_D=4.5A		5.1		nC
Gate-to-Source Charge	Qgs			0.7		nC
Gate-to-Drain “Miller” Charge	Qgd			1.7		nC
Diode Forward Voltage	V_{SD}	I_S=4.5A, V_{GS}=0V		0.78	1.2	V

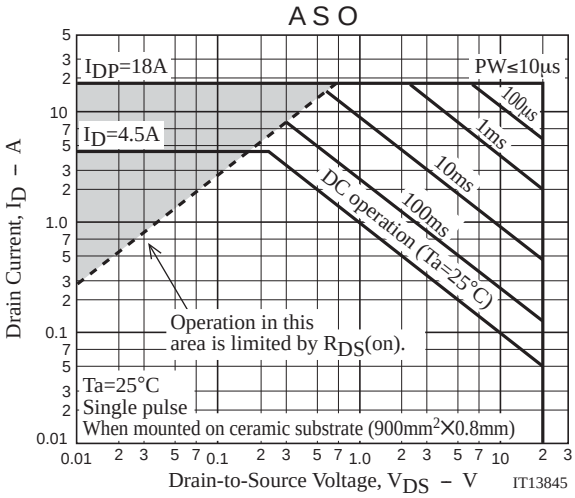
Switching Time Test Circuit

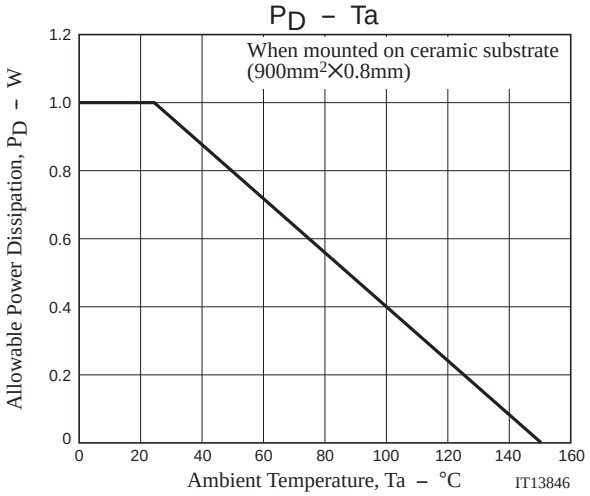


Ordering Information

Device	Package	Shipping	memo
MCH3477-TL-H	MCPH3	3,000pcs./reel	Pb Free and Halogen Free


VGS – Qg
VDS=10V
ID=4.5A
Gate-to-Source Voltage, VGS – V
Total Gate Charge, Qg – nC
IT13844


A S O
IDP=18A
PW≤10μs
ID=4.5A
100μs
1ms
10ms
100ms
DC operation (Ta=25°C)
Operation in this
area is limited by RDS(on).
Ta=25°C
Single pulse
When mounted on ceramic substrate (900mm2×0.8mm)
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V
IT13845


PD – Ta
When mounted on ceramic substrate
(900mm2×0.8mm)
Allowable Power Dissipation, PD – W
Ambient Temperature, Ta – °C
IT13846

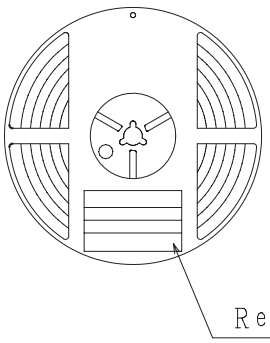
Taping Specification

MCH3477-TL-H

1. Packing Format

Package Name	Carrier Tape Type	Maximun Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
MCPH3	MCPH3	3,000	15,000	90,000	5 reels contained Dimensions:mm(external) 183×72×185	6 inner boxes contained Dimensions:mm(external) 440×195×210

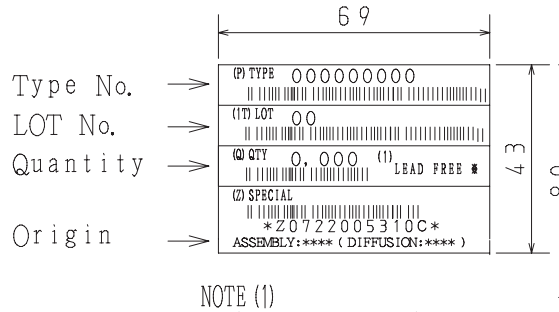
Packing method



Reel label

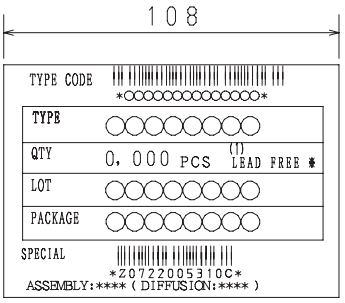
Reel label, Inner box label

(unit:mm)



Outer box label

It is a label at the time of factory shipments. The form of a label may change in physical distribution process.

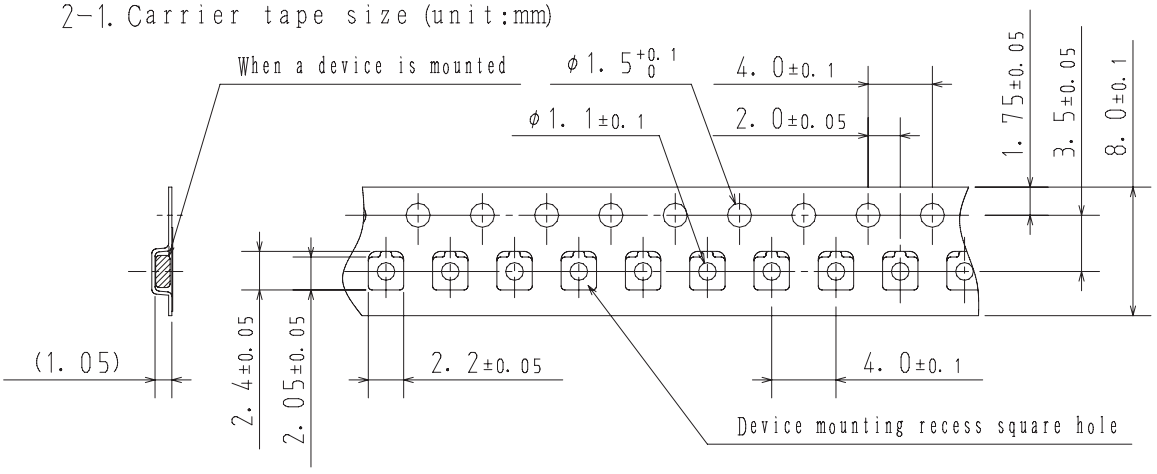


NOTE (1)
The LEAD FREE ※ description shows that the surface treatment of the terminal is lead free.

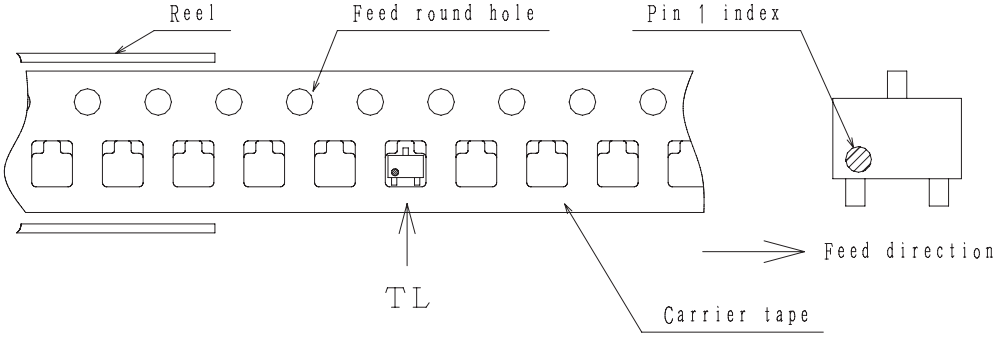
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



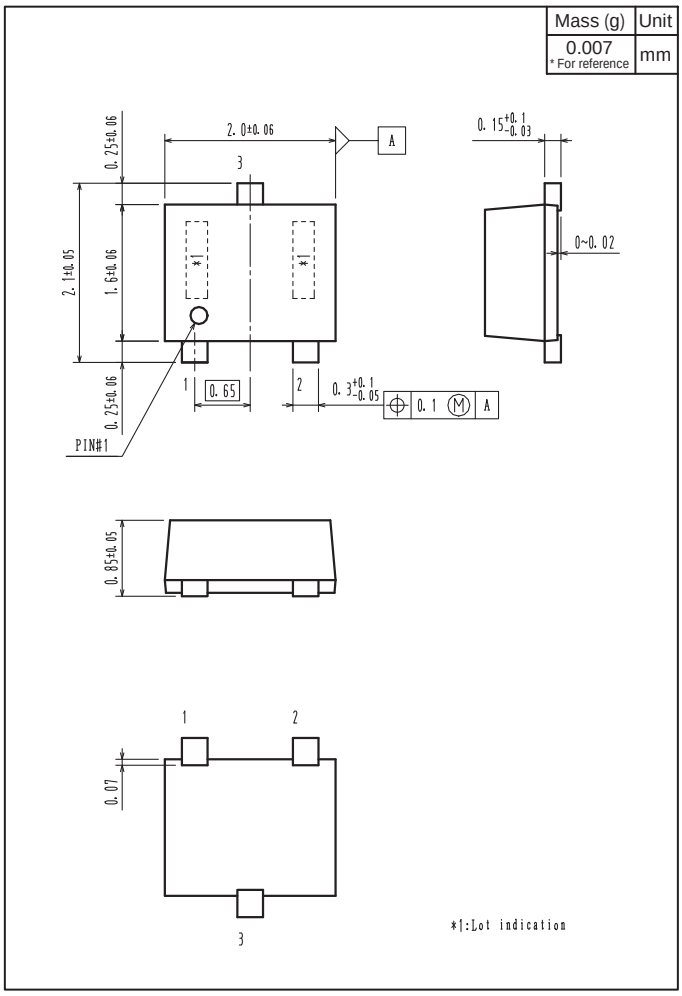
2-2. Device placement direction



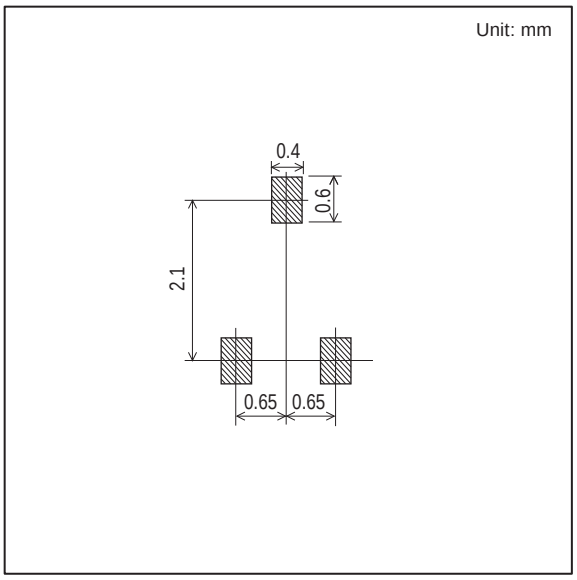
Those with pin 1 index on the feed hole side·····TL

Outline Drawing

MCH3477-TL-H



Land Pattern Example



Note on usage : Since the MCH3477 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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