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Silicon PNP Epitaxial

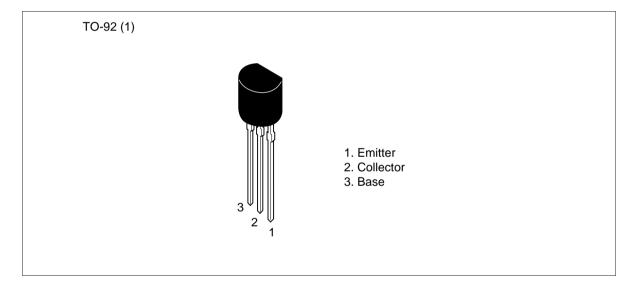


ADE-208-314 (Z) 1st. Edition Mar. 2001

Application

High voltage medium speed switching

Outline



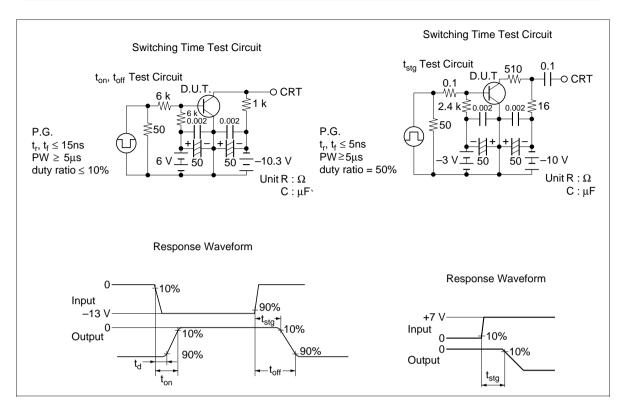
Absolute Maximum Ratings (Ta = 25° C)

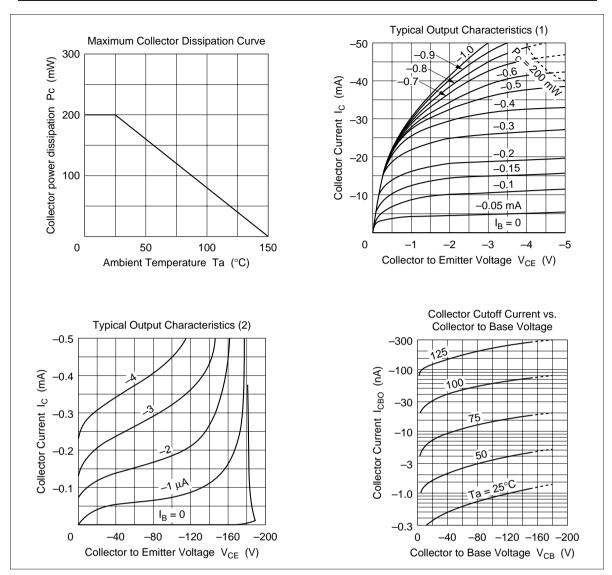
Item	Symbol	2SA778(K)	2SA778A(K)	Unit
Collector to base voltage	V _{CBO}	-150	-180	V
Collector to emitter voltage	V _{CEO}	-150	-180	V
Emitter to base voltage	V _{EBO}	-5	-5	V
Collector current	I _c	-50	-50	mA
Collector power dissipation	Pc	200	200	mW
Junction temperature	Tj	150	150	°C
Storage temperature	Tstg	-55 to +150	-55 to +150	°C

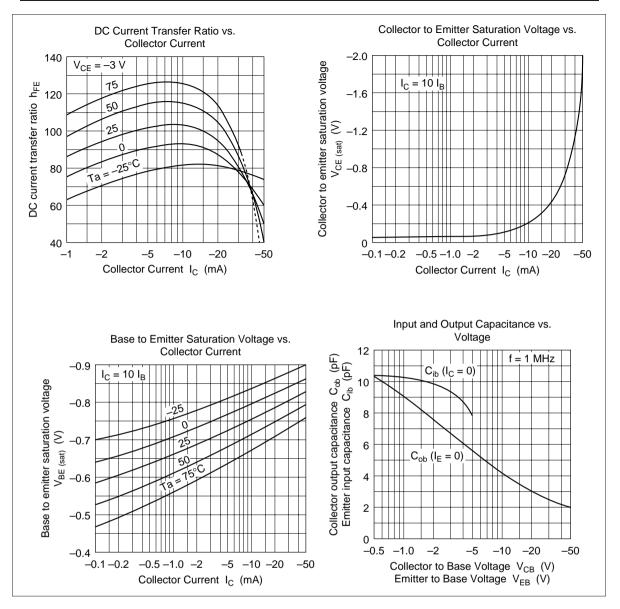
Electrical Characteristics (Ta = 25°C)

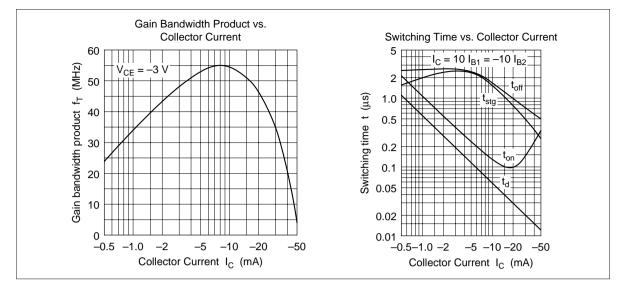
		2SA7	78(K)		2SA778A(K)				
ltem	Symbol	Min	Тур	Max	Min	Тур	Max	Unit	Test conditions
Collector to base breakdown voltage	$V_{(\text{BR})\text{CBO}}$	-150	_	_	-180	_	_	V	$I_{c} = -50 \ \mu A, \ I_{E} = 0$
Collector to emitter breakdown voltage	$V_{(\text{BR})\text{CER}}$	-150	_	_	-180	_	—	V	$I_c = -50 $ μA, R _{BE} = 30 kΩ
Collector cutoff current	I _{CBO}			-1.0				μΑ	$V_{CB} = -100 \text{ V}, I_{E} = 0$
							-1.0	μΑ	$V_{CB} = -150 \text{ V}, I_{E} = 0$
Emitter cutoff current	I _{EBO}	—	—	-1.0	_		-1.0	μΑ	$V_{\scriptscriptstyle EB}=-5~V,~I_{\scriptscriptstyle C}=0$
DC current transfer ratio	h _{FE}	30	100	—	40	100	200		$V_{ce} = -3 V,$ $I_e = -15 mA$
Collector to emitter saturation voltage	$V_{\text{CE(sat)}}$		-0.3	-1.0	_	-0.3	-1.0	V	$I_{c} = -15 \text{ mA},$ $I_{B} = -1 \text{ mA}$
Base to emitter saturation voltage	$V_{\text{BE(sat)}}$	—	-0.77	-1.0	_	-0.77	-1.0	V	I _c = −15 mA, I _B = −1 mA
Collector output capacitance	Cob	—	_	10	_	—	10	pF	$V_{CB} = -10 \text{ V}, I_E = 0,$ f = 1 MHz
Gain bandwidth product	f _T	—	50		_	50	_	MHz	$V_{ce} = -3 V,$ $I_{c} = -15 mA$
Turn on time	t _{on}	—	135		—	135	—	ns	$V_{cc} = -10.3 V$
Turn off time	t _{off}	_	1.7	_	_	1.7	—	μs	$I_{c} = 10 I_{B1} = -10$ $I_{B2} = -10 \text{ mA}$
Storage time	t _{stg}	_	_	1.0	_	_	1.0	μs	$V_{cc} = -10 V,$ $I_{c} = -17 mA$ $I_{B1} = -1mA,$ $I_{B2} = -12 mA$



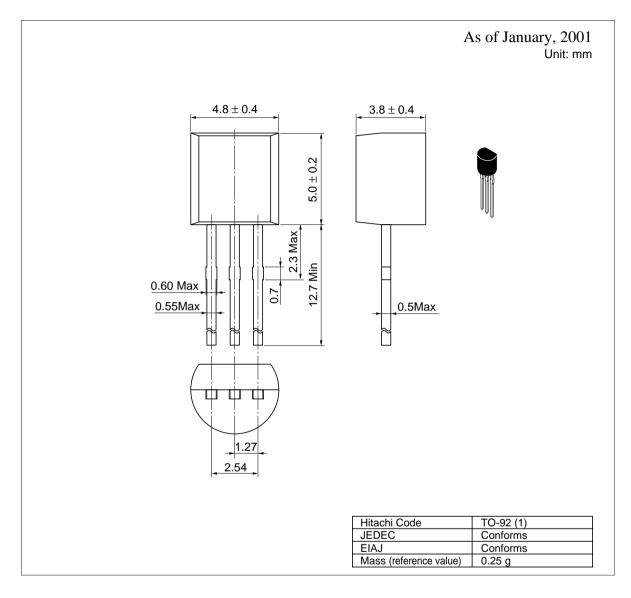








Package Dimensions



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