

BYW80-200

SWITCHMODE™ Power Rectifiers

This state-of-the-art device is designed for use in switching power supplies, inverters and as free wheeling diodes.

Features

- Ultrafast 35 Nanosecond Recovery Time
- 175°C Operating Junction Temperature
- Popular TO-220 Package
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Low Forward Voltage
- Low Leakage Current
- High Temperature Glass Passivated Junction
- Pb-Free Package is Available*

Mechanical Characteristics

- Case: Epoxy, Molded
- Weight: 1.9 Grams (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds

MAXIMUM RATINGS

| Rating | Symbol | Values | Unit |
|---|---------------------------------|-------------|------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | V_{RRM} V_{RWM} V_R | 200 | V |
| Average Rectified Forward Current Total Device, (Rated V_R), $T_C = 150^\circ\text{C}$ | $I_{F(AV)}$ | 8.0 | A |
| Peak Repetitive Forward Current (Rated V_R , Square Wave, 20 kHz), $T_C = 150^\circ\text{C}$ | I_{FM} | 16 | A |
| Nonrepetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz) | I_{FSM} | 100 | A |
| Operating Junction Temperature and Storage Temperature Range | T_J, T_{stg} | -65 to +175 | °C |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

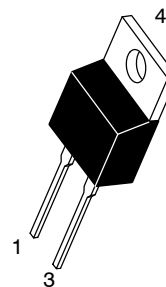
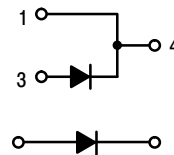
*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



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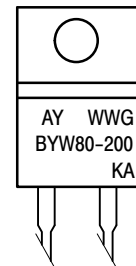
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ULTRAFAST RECTIFIERS 8.0 AMPERES, 200 VOLTS



**CASE 221B
TO-220B
PLASTIC**

MARKING DIAGRAM



A = Assembly Location
Y = Year
WW = Work Week
BYW80-200 = Device Code
G = Pb-Free Package
KA = Diode Polarity

ORDERING INFORMATION

| Device | Package | Shipping |
|------------|---------------------|---------------|
| BYW80-200 | TO-220 | 50 Units/Rail |
| BYW80-200G | TO-220 (Pb-Free) | 50 Units/Rail |

BYW80-200

THERMAL CHARACTERISTICS

| Rating | Symbol | Values | Unit |
|--|-----------------|--------|---------------|
| Maximum Thermal Resistance, Junction-to-Case | $R_{\theta JC}$ | 3.0 | $^{\circ}C/W$ |

ELECTRICAL CHARACTERISTICS

| | | | |
|---|----------|--------------|----|
| Maximum Instantaneous Forward Voltage (Note 1) ($i_F = 7.0$ Amps, $T_C = 100^{\circ}C$) ($i_F = 22$ Amps, $T_C = 25^{\circ}C$) | v_F | 0.85 1.25 | V |
| Maximum Instantaneous Reverse Current (Note 1) (Rated dc Voltage, $T_J = 100^{\circ}C$) (Rated dc Voltage, $T_J = 25^{\circ}C$) | i_R | 1 0.01 | mA |
| Maximum Reverse Recovery Time ($I_F = 1.0$ Amp, $di/dt = 50$ Amps/ μs) ($I_F = 0.5$ Amp, $i_R = 1.0$ Amp, $I_{REC} = 0.25$ Amp) | t_{rr} | 35 25 | ns |

1. Pulse Test: Pulse Width $\leq 300 \mu s$, Duty Cycle $\leq 2.0\%$.

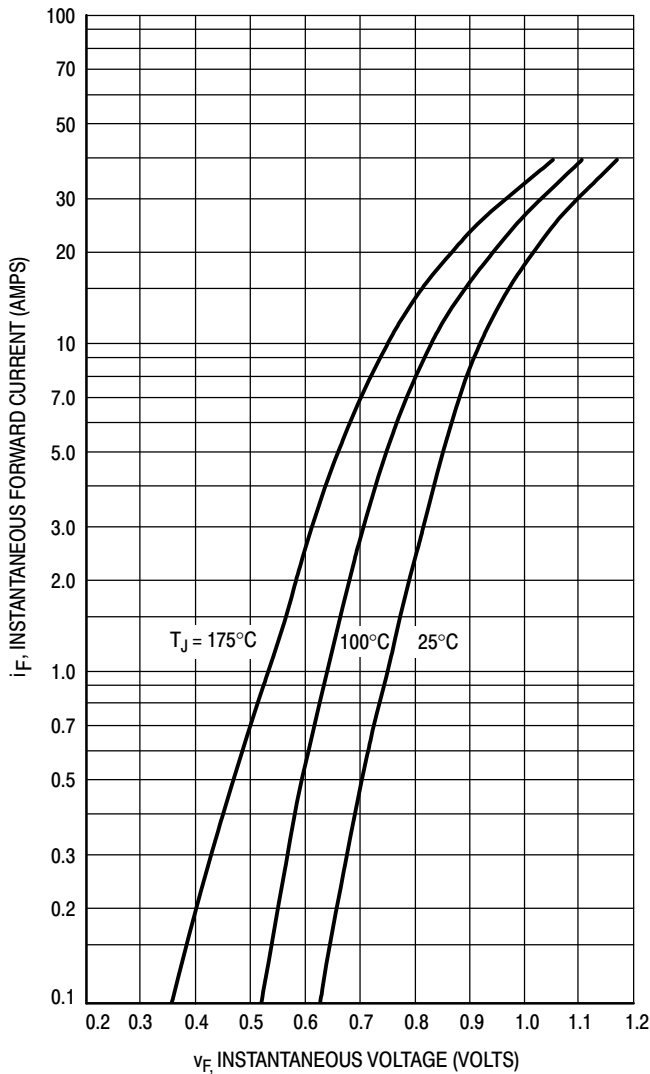


Figure 1. Typical Forward Voltage

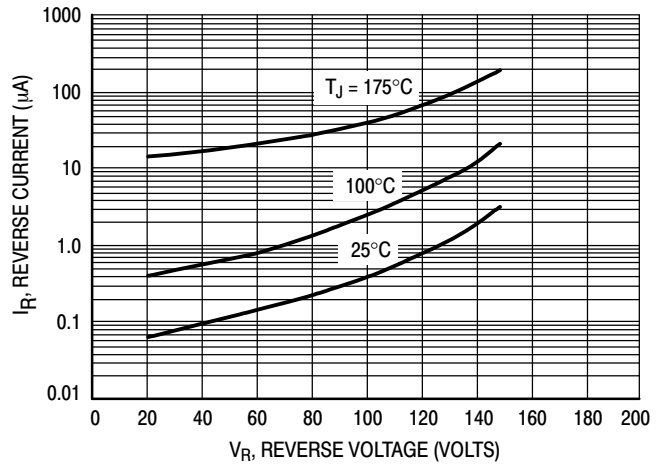


Figure 2. Typical Reverse Current*

* The curves shown are typical for the highest voltage device in the grouping. Typical reverse current for lower voltage selections can be estimated from these same curves if V_R is sufficiently below rated V_R .

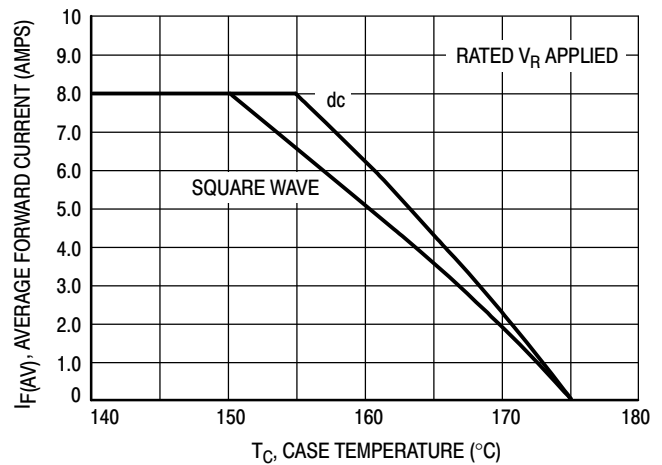


Figure 3. Current Derating, Case

BYW80-200

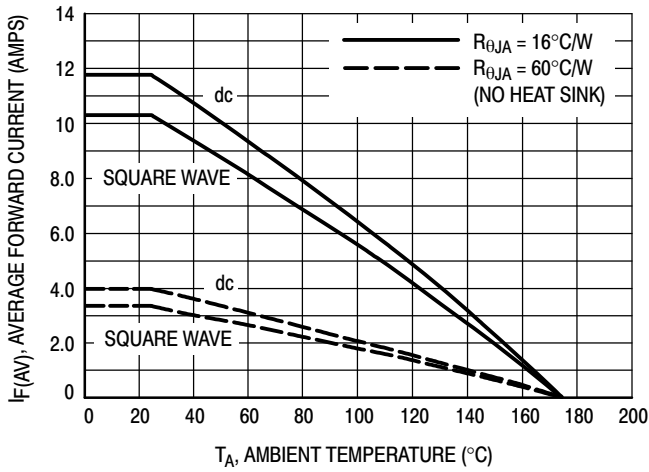


Figure 4. Current Derating, Ambient

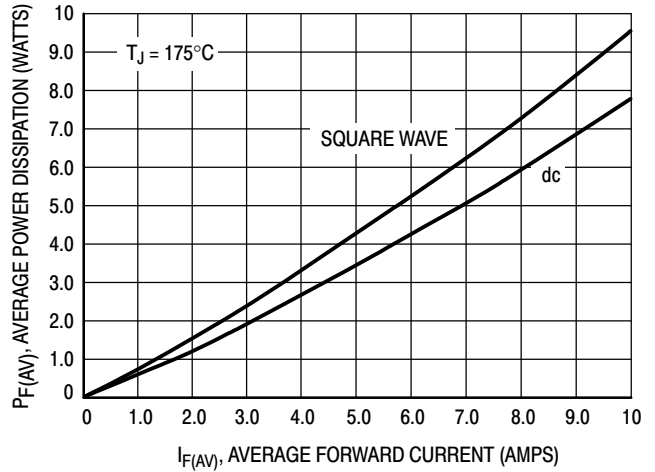


Figure 5. Power Dissipation

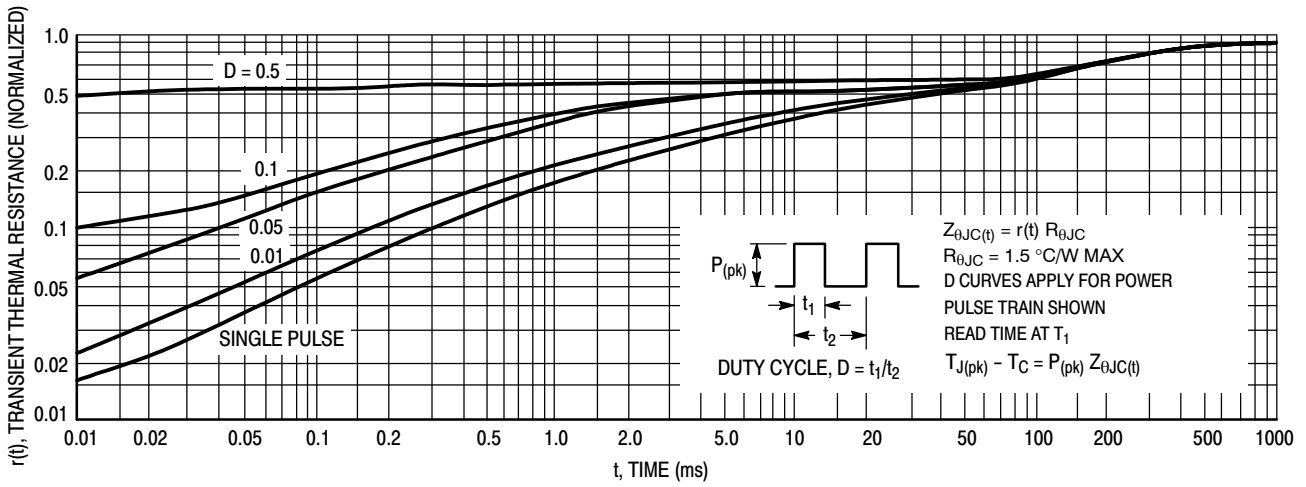


Figure 6. Thermal Response

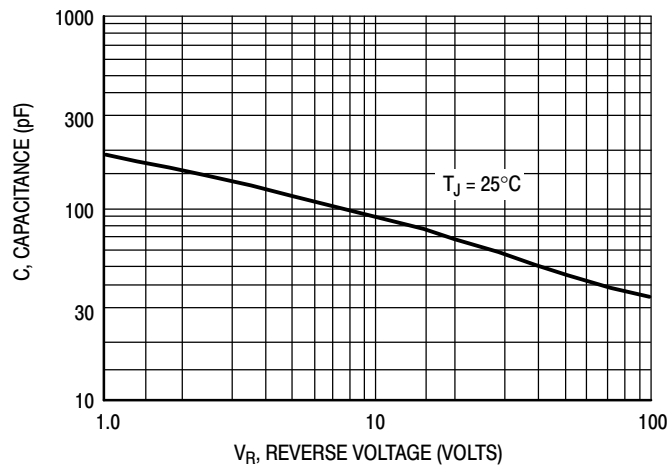
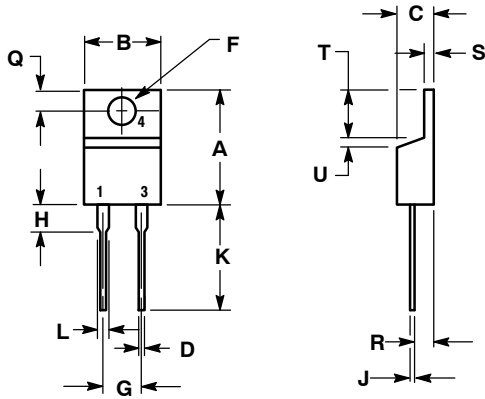


Figure 7. Typical Capacitance

BYW80-200

PACKAGE DIMENSIONS


TO-220
CASE 221B-04
ISSUE E



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

| DIM | INCHES | | MILLIMETERS | |
|-----|--------|-------|-------------|-------|
| | MIN | MAX | MIN | MAX |
| A | 0.595 | 0.620 | 15.11 | 15.75 |
| B | 0.380 | 0.405 | 9.65 | 10.29 |
| C | 0.160 | 0.190 | 4.06 | 4.82 |
| D | 0.025 | 0.035 | 0.64 | 0.89 |
| F | 0.142 | 0.161 | 3.61 | 4.09 |
| G | 0.190 | 0.210 | 4.83 | 5.33 |
| H | 0.110 | 0.130 | 2.79 | 3.30 |
| J | 0.014 | 0.025 | 0.36 | 0.64 |
| K | 0.500 | 0.562 | 12.70 | 14.27 |
| L | 0.045 | 0.060 | 1.14 | 1.52 |
| Q | 0.100 | 0.120 | 2.54 | 3.04 |
| R | 0.080 | 0.110 | 2.04 | 2.79 |
| S | 0.045 | 0.055 | 1.14 | 1.39 |
| T | 0.235 | 0.255 | 5.97 | 6.48 |
| U | 0.000 | 0.050 | 0.000 | 1.27 |

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