

Rail to Rail Output CMOS Single Operating Amplifier

Features

- **Operating Voltage**
Single Supply, 3V to 6V
- **Low Input Current, 2pA**
- **Rail to Rail Output Swing**
- **Push-Pull Output Driving**
- **High Output Current Drive, 310mA**
- **Bandwidth:7MHz**
- **Wide Temperature Range**
- **Available in the SOT-23-5 Package**
- **Single Version of APC308**
- **Lead Free and Green Devices Available (RoHS Compliant)**

General Description

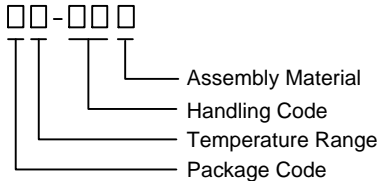
The APC207/208 brings performance and economy with high gain, CMOS operating amplifier, combining rail to rail output range with large output current. It provides a low input bias current 2pA.

The output swing of the amplifier, guaranteed for loads down to 1kΩ and output current to an 10Ω load from a 5V power supply. APC207/208 designed to operate at 3V is especially well-suited for low voltage application. The APC207/208 is offered in the space saving SOT23-5 package.

Applications

- **Amplifiers**
- **Filters**
- **Analog Circuit**

Ordering and Marking Information

<p>APC207/208 □□-□□□</p>  <p> Assembly Material Handling Code Temperature Range Package Code </p>	<p> Package Code B : SOT-23-5 Temperature Range I : - 40 to 85 °C Handling Code TR : Tape & Reel Assembly Material L : Lead Free Device G : Halogen and Lead Free Device </p>
<p>APC207/8 B : 207/8 X</p>	<p>X - Date Code</p>

Note: ANPEC lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. ANPEC lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020C for MSL classification at lead-free peak reflow temperature. ANPEC defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

ANPEC reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

Absolute Maximum Ratings (Note 1)

Symbol	Parameter	Rating	Unit
V_{DD}	Supply Voltage	7	V
$T_{SC(O)}$	Output Short-Circuit Duration, at $T_A=25^\circ\text{C}$, $P_{TOT}=1\text{W}$	20	S
T_A	Operating Ambient Temperature range	-40 to 85	$^\circ\text{C}$
T_J	Maximum Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-65 to +150	$^\circ\text{C}$
T_S	Maximum Lead Soldering Temperature, 10 Seconds	260	$^\circ\text{C}$

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Thermal Characteristics

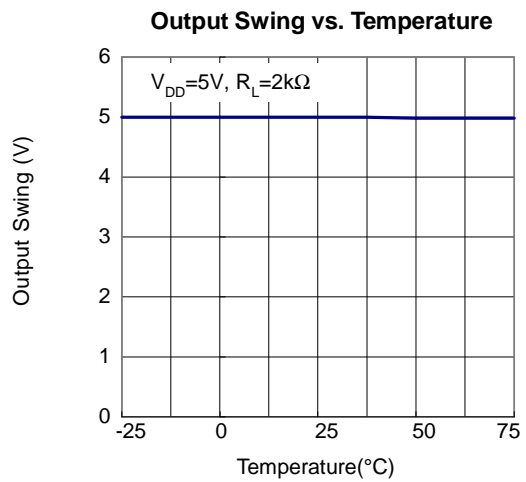
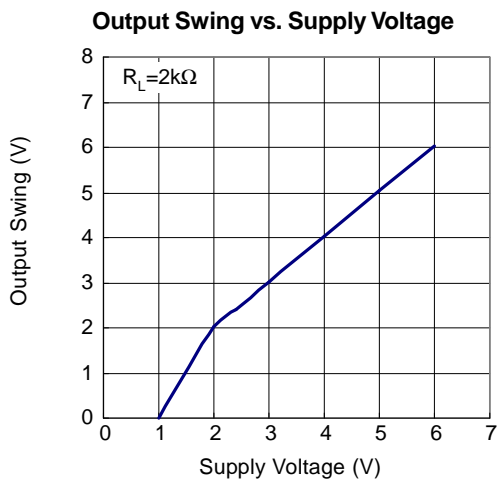
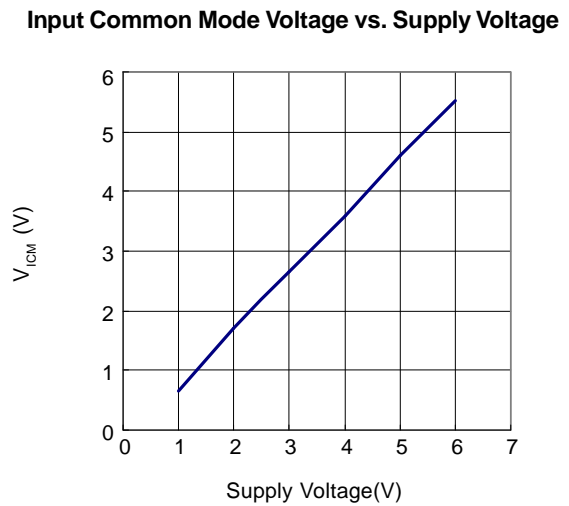
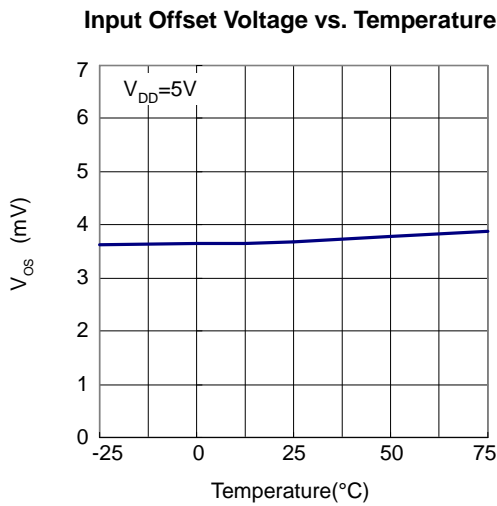
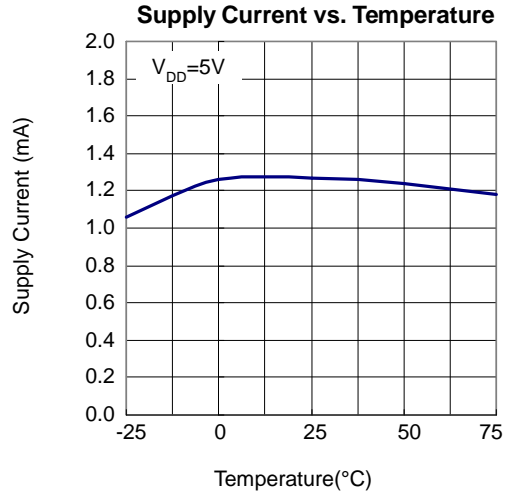
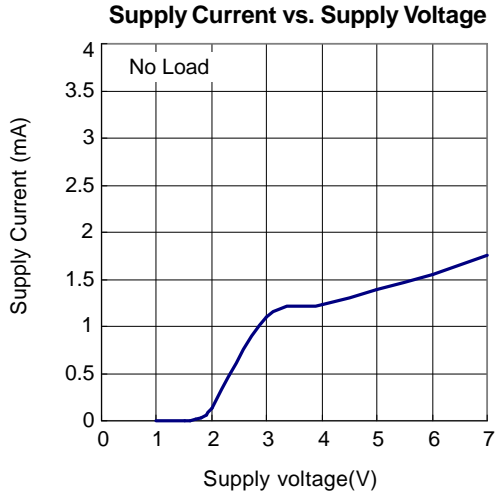
Symbol	Parameter	Typical Value	Unit
θ_{JA}	Thermal Resistance from Junction to Ambient in Free Air SOT-23-5	357	$^\circ\text{C}/\text{W}$

Electrical Characteristics

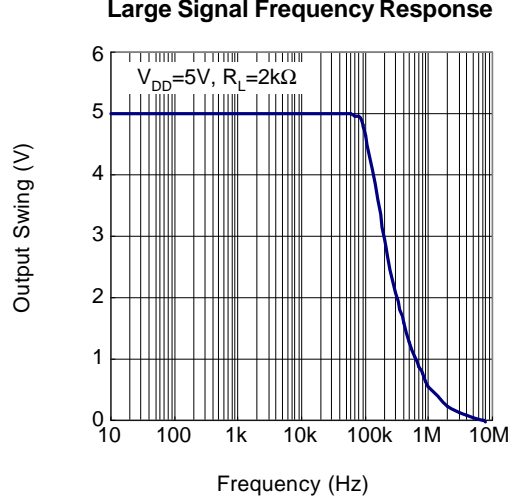
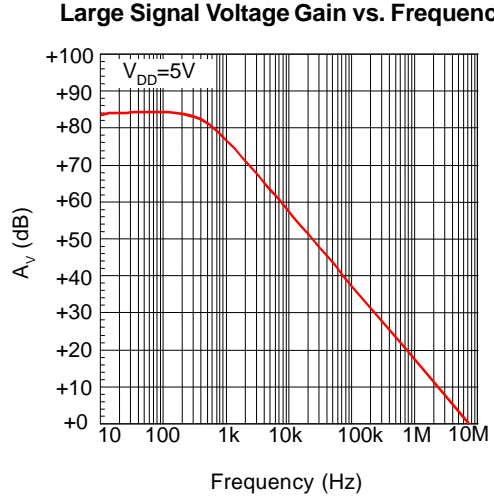
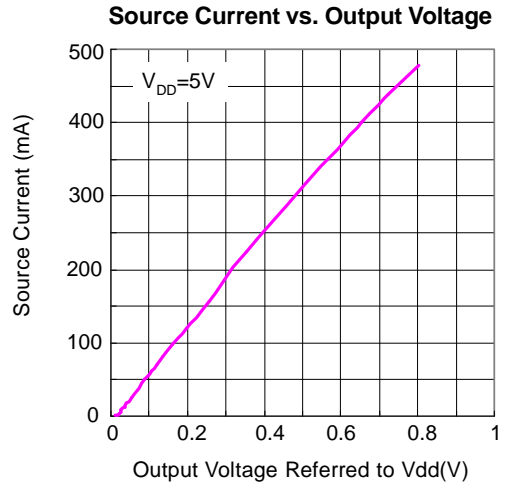
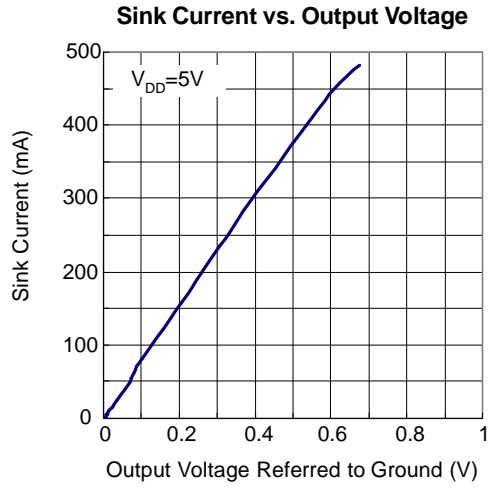
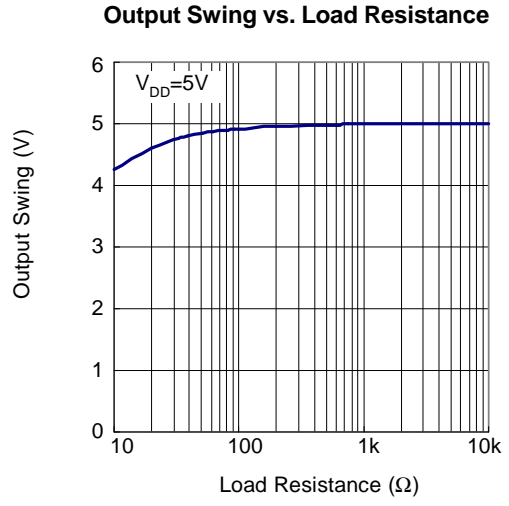
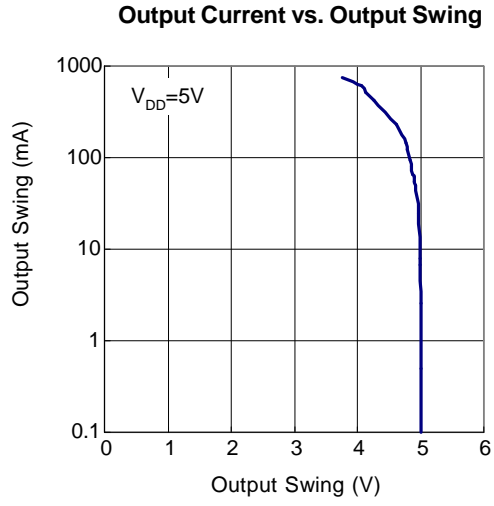
$V_{DD}=5\text{V}$, $T_A=25^\circ\text{C}$ (unless otherwise noted)

Symbol	Parameter	Test Conditions	APC207/8			Unit
			Min.	Typ.	Max.	
V_{OS}	Input Offset Voltage	$T_A=25^\circ\text{C}$	-	4	10	mV
		$-25^\circ\text{C} < T_A < 75^\circ\text{C}$	-	5	-	mV
I_{BIAS}	Input Bias Current	$T_A=25^\circ\text{C}$	-	2	-	pA
V_{ICM}	Input Common Mode Voltage Range	$T_A=25^\circ\text{C}$	0	-	$V_{DD}-0.8$	V
R_{IN}	Input Resistance		-	5	-	$\text{m}\Omega$
V_O	Output Voltage Swing	$R_L=2\text{k}\Omega$	0	-	5	V
V_{OH}	Output Voltage High	$R_L=10\text{k}\Omega$, $-25^\circ\text{C} < T_A < 75^\circ\text{C}$	-	5	-	V
V_{OL}	Output Voltage Low	$R_L=10\text{k}\Omega$, $-25^\circ\text{C} < T_A < 75^\circ\text{C}$	-	0	-	V
I_{OUT}	Output Current Source	$V_O=4.5\text{V}$	-	-310	-	mA
	Output Current Sink	$V_O=0.5\text{V}$	-	370	-	mA
CMRR	Common Mode Rejection Ratio		-	-80	-	dB
PSRR	Supply Voltage Rejection Ratio	$V_{RR}=100\text{mV}_{PP}$, $f_{in}=100\text{Hz}$, $R_L=2\text{k}\Omega$	-	-50	-	dB
A_V	Large Signal Voltage Gain		-	85	-	dB
GBW	Gain Bandwidth Product		-	7	-	MHz
SR	Slew Rate		-	5.6	-	$\text{V}/\mu\text{s}$
I_{CC}	Supply Current	$-25^\circ\text{C} < T_A < 75^\circ\text{C}$	-	2.0	4	mA
		$2.4\text{V} < V_{CC} < 6\text{V}$, $T_A=25^\circ\text{C}$	-	2.5	5	mA

Typical Operating Characteristics

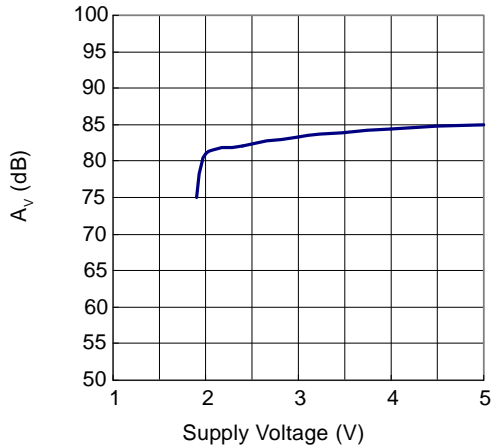


Typical Operating Characteristics (Cont.)

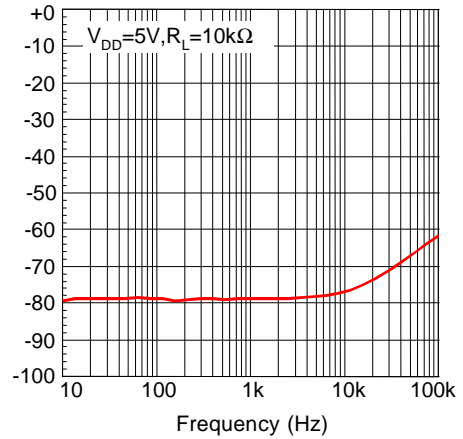


Typical Operating Characteristics (Cont.)

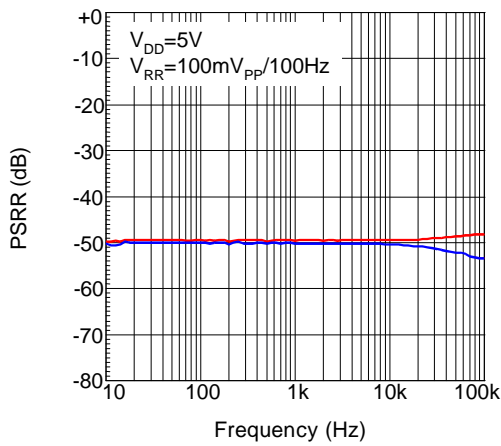
Large Signal Voltage Gain vs. Supply Voltage



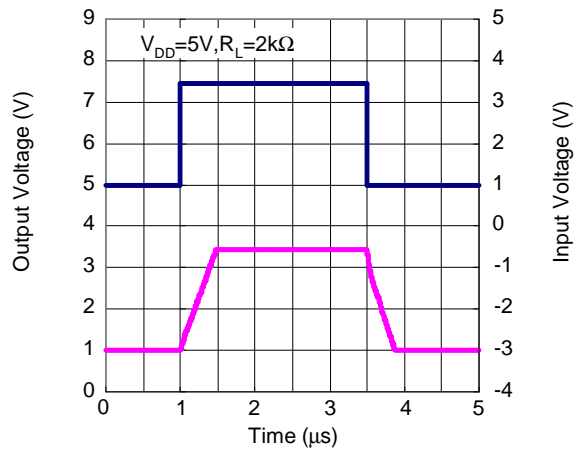
CMRR vs. Frequency



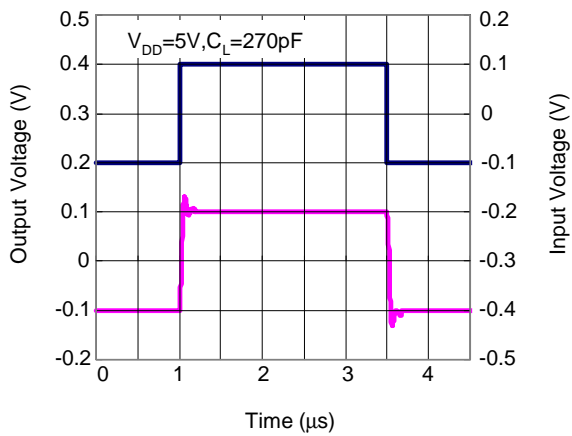
PSRR vs. Frequency



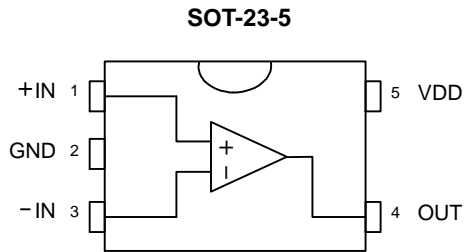
Voltage Follower Pulse Response



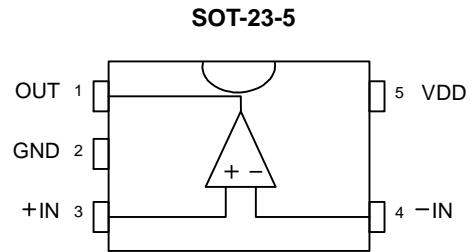
Voltage Follower Pulse Response



Block Diagram



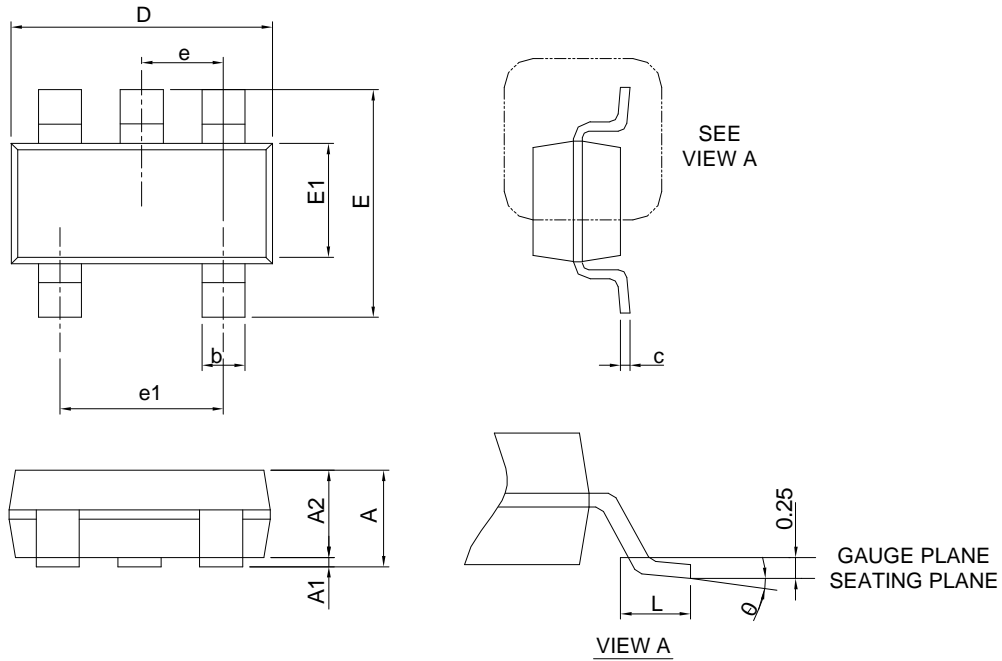
APC207
TOP View



APC208
TOP View

Package Information

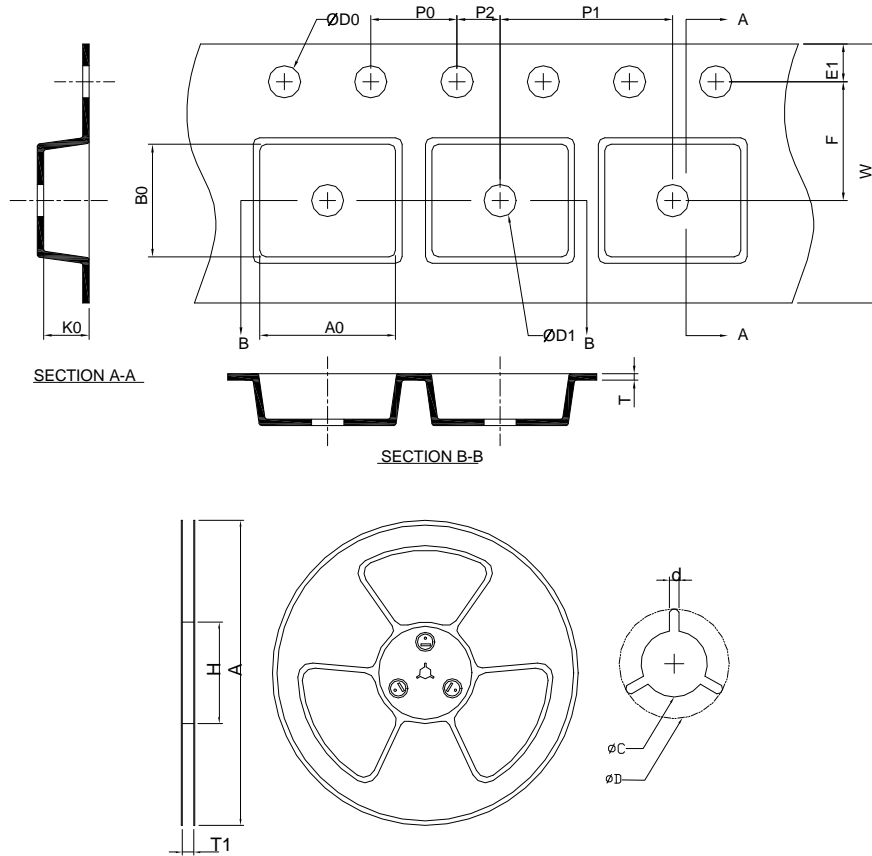
SOT-23-5



SYMBOL	SOT-23-5			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A		1.45		0.057
A1	0.00	0.15	0.000	0.006
A2	0.90	1.30	0.035	0.051
b	0.30	0.50	0.012	0.020
c	0.08	0.22	0.003	0.009
D	2.70	3.10	0.106	0.122
E	2.60	3.00	0.102	0.118
E1	1.40	1.80	0.055	0.071
e	0.95 BSC		0.037 BSC	
e1	1.90 BSC		0.075 BSC	
L	0.30	0.60	0.012	0.024
θ	0°	8°	0°	8°

Note : 1. Follow JEDEC TO-178 AA.
 2. Dimension D and E1 do not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 10 mil per side.

Carrier Tape & Reel Dimensions



Application	A	H	T1	C	d	D	W	E1	F
SOT-23-5	178.0 ±0.00	50 MIN.	8.4+2.00 -0.00	13.0+0.50 -0.20	1.5 MIN.	20.2 MIN.	8.0 ±0.30	1.75 ±0.10	3.5 ±0.05
	P0	P1	P2	D0	D1	T	A0	B0	K0
	4.0 ±0.10	4.0 ±0.10	2.0 ±0.05	1.5+0.10 -0.00	1.0 MIN.	0.6+0.00 -0.40	3.20 ±0.20	3.10 ±0.20	1.50 ±0.20

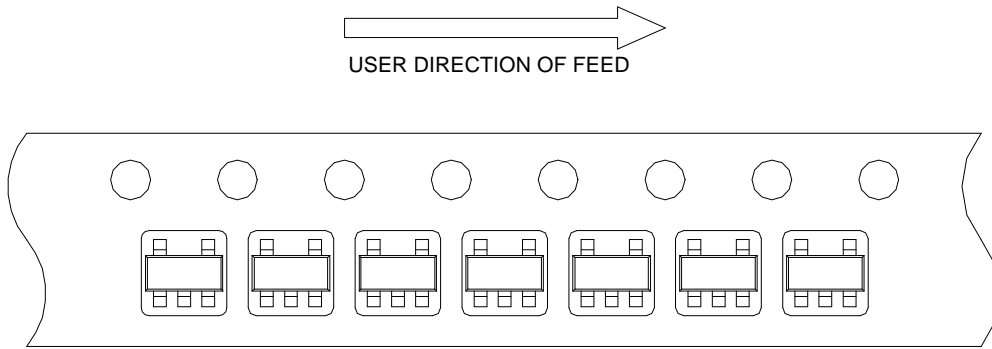
(mm)

Devices Per Unit

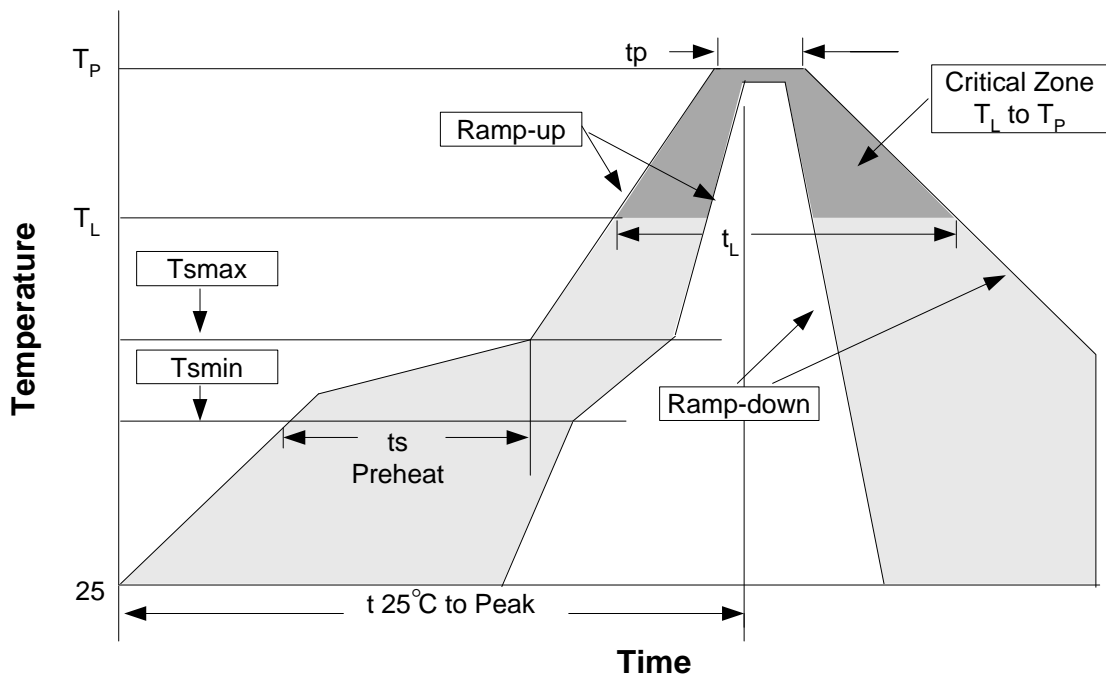
Package Type	Unit	Quantity
SOT-23-5	Tape & Reel	3000

Taping Direction Information

SOT-23-5



Reflow Condition (IR/Convection or VPR Reflow)



Reliability Test Program

Test item	Method	Description
SOLDERABILITY	MIL-STD-883D-2003	245°C, 5 sec
HOLT	MIL-STD-883D-1005.7	1000 Hrs Bias @ 125°C
PCT	JESD-22-B, A102	168 Hrs, 100%RH, 121°C
TST	MIL-STD-883D-1011.9	-65°C~150°C, 200 Cycles
ESD	MIL-STD-883D-3015.7	VHBM > 2KV, VMM > 200V
Latch-Up	JESD 78	10ms, 1 _{tr} > 100mA

Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T _L to T _P)	3°C/second max.	3°C/second max.
Preheat - Temperature Min (T _{min}) - Temperature Max (T _{max}) - Time (min to max) (t _s)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
Time maintained above: - Temperature (T _L) - Time (t _L)	183°C 60-150 seconds	217°C 60-150 seconds
Peak/Classification Temperature (T _p)	See table 1	See table 2
Time within 5°C of actual Peak Temperature (t _p)	10-30 seconds	20-40 seconds
Ramp-down Rate	6°C/second max.	6°C/second max.
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.

Note: All temperatures refer to topside of the package. Measured on the body surface.

Table 1. SnPb Eutectic Process – Package Peak Reflow Temperatures

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	240 +0/-5°C	225 +0/-5°C
≥2.5 mm	225 +0/-5°C	225 +0/-5°C

Table 2. Pb-free Process – Package Classification Reflow Temperatures

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 +0°C*	260 +0°C*	260 +0°C*
1.6 mm – 2.5 mm	260 +0°C*	250 +0°C*	245 +0°C*
≥2.5 mm	250 +0°C*	245 +0°C*	245 +0°C*

*Tolerance: The device manufacturer/supplier **shall** assure process compatibility up to and including the stated classification temperature (this means Peak reflow temperature +0°C. For example 260°C+0°C) at the rated MSL level.

Customer Service

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