Voltage Mode PWM Controller with EMI Reduction
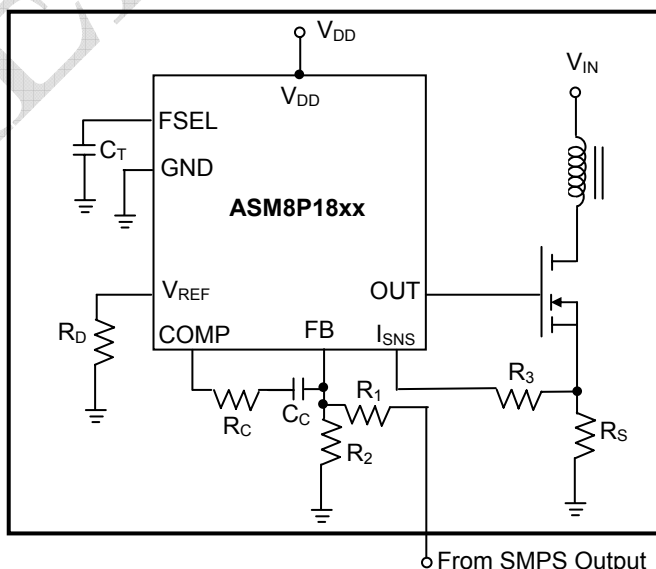
General Description

ASM8P18xx is a high performance, adjustable frequency, PWM controller with an integrated spread spectrum modulator for EMI reduction. It contains all the functions of a standard PWM controller along with a user configurable spread spectrum modulation with adjustable spread. ASM8P18xx allows significant system cost savings by reducing the number of PCB layers and shielding that are traditionally required to pass EMI regulation.

ASM8P18xx is the industry's first general purpose EMI reduction IC, specifically designed for use in SMPS systems. ASM8P18xx is pin compatible to 384x family of PWM controllers.

ASM8P18xx is capable of driving 1A maximum current output and it covers a wide supply voltage range from 7V DC to 30V DC (25V_{MAX} for Micro SO). The PWM frequency is selectable from 40 KHz to 400 KHz.

ASM8P18xx provides under voltage lockout, thermal shutdown, and overload and cycle-by-cycle current limit. AS8P18S4xx provides cycle slip feature which reduces no load power consumption of converters. It is available in 8-pin MicroSO, P-DIP and SOIC Package.

Features

- 30V maximum operating voltage with BiCMOS technology (25V_{MAX} for Micro SO)
- Adjustable PWM frequencies
- 40KHz to 400KHz Operation
- Maximum Output drive current of 1A
- Wide duty cycle range
  (0% minimum to 99% maximum)
- Spread Spectrum Modulation with adjustable spread for EMI Reduction
- Under voltage lockout with hysteresis
- Low startup current: 700µA maximum
- Pin compatible with industry standard 3842 PWM controller
- Temperature range –40°C to +85°C
- Thermal shutdown and overload protection
- Frequency skip mode at Light load condition
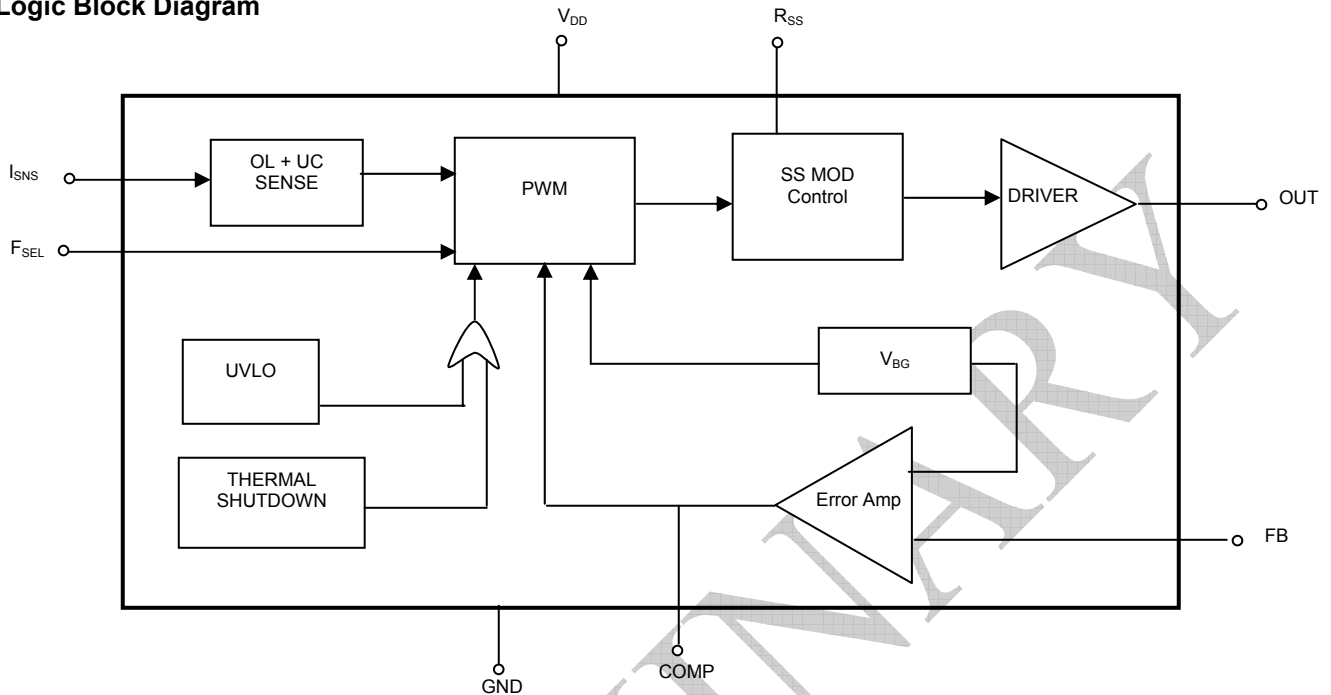- Available in 8-pin plastic MicroSO, P-DIP and SOIC Packages

Applications

- Off-line converter
- DC-DC converter
- Monitor power supply
- Computer/DVD/STB power supply
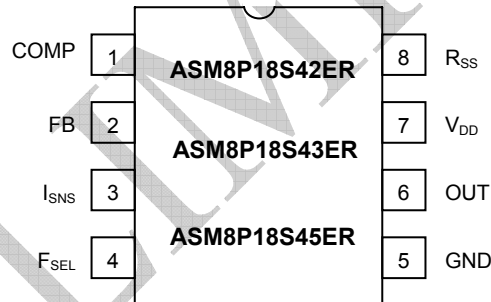- Wireless base station power supply
- Telecom power supply

Typical Operating Circuit

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Logic Block Diagram

Pin Diagram

Pin Description

Pin #	Pin Name	Function
1	COMP	Input of the PWM Comparator and output of the error amplifier
2	FB	Feedback, inverting input of the error amplifier
3	I_{SNS}	Current sense comparator input
4	FSEL	External capacitor selects PWM frequency
5	GND	Ground
6	OUT	SS modulated PWM output
7	V_{DD}	Supply voltage
8	R_{SS}	External resistor "R_{SS}" to GND sets Depth of Modulation.

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Application Information

Spread Spectrum Deviation

The deviation can be determined by selecting the proper resistor at R_{SS} to GND (Refer "R_{SS} Resistor Vs. % Modulation Depth Plot" for resistor selection)

PWM Frequency

The PWM frequency can be determined by selecting the proper capacitance (C_T) at the FSEL pin.

Start-up Current

ASM8P18xx allows a substantial reduction in the start up current. Low start up current allows high resistance, lower wattage start-up resistor, to supply controller start up power.

Under Voltage Lockout (UVLO)

When the power supply voltage is below the start up threshold voltage, internal circuitry puts the output into low impedance state and sets the output to zero.

Thermal Shutdown

The output of ASM8P18xx goes down to zero when the junction temperature of the device rises above 160°C. The device automatically resumes operation when temperature drops to 130°C. This protects the device from thermal breakdown.

Overload and under current protection

ASM8P18xx provides cycle by cycle current limit and pulls down PWM output to low as soon as I_{SNS} pin senses a peak voltage of 1V, with a delay to output of 125 nS maximum.

At no load condition when the device senses the peak voltage level of less than 0.1V at I_{SNS} pin for a period of 200mS, the oscillator enters in to cycle skip mode. Normal condition is restored once I_{SNS} increases beyond 0.1V for more than three cycles. Details of cycle skip for different options are provided in the Electrical Characteristics table.

Absolute Maximum Ratings

Symbol	Parameter	Min	Max	Units
V_{DD}	Supply Voltage		30	V
I_{DD}	Supply Current		15	mA
I_{OUT}	Maximum Output Current		1	A
I_{SNS}	Current Sense Inputs and feedback I_{SNS}, FB, COMP		5	V
V_{OSC}	Oscillator Voltage		4	V
V_{OUT}	Output Voltage		30	V
	Operation Junction Temperature	-45	150	°C
	Storage Temperature	-65	150	°C
	Lead Soldering Temperature (10 Seconds)		300	°C
ESD Rating	HBM		2	KV
	MM		200	V

Note: For MicroSO Devices Maximum V_{DDMAX} = 25V & I_{DDMAX} =10mA

Package Thermal Resistance	
8-Pin Plastic DIP (θ_{JA})	125°C/W
8-Pin MicroSO (θ_{JA})	250°C/W
8-Pin SO (θ_{JA})	170°C/W

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Electrical Characteristics

Unless otherwise noted, V_{DD}=15V, Capacitor on F_{SEL} = 330pf, I_{SENS} = 0.5V. Specifications are over the –40°C to +85°C ambient temperature range. Typical values are at 25°C.

Parameter	Symbol	Conditions	Min	Typ	Max	Units
ERROR AMPLIFIER SECTION						
Input Bias Current	I_{BIAS}			-170		µA
Input Voltage	V_I	V_{pin1} = 2.5V	1.19	1.25	1.29	V
Open Loop Voltage Gain	A_{VOL}			37		dB
Power Supply Rejection Ratio	PSRR	V_{START} < V_{CC} < V_{CC} max		80		dB
Output Sink Current	I_{OL}	V_{FB} = 1.32V, V_{COMP} = 0.15V		-0.06		mA
Output Source Current	I_{OH}	V_{FB} = 1.18V, V_{COMP} = 4.17V		-0.13		mA
High Output Voltage	V_{OH}	V_{FB} = 1.25V, R_L = 15KΩ	3.6			V
Low Output Voltage	V_{OL}	V_{FB} = 1.25V		0.35		V
CURRENT SENSE SECTION						
Over Current Protection Threshold	$V_{I(MAX)}$	FB = 0V (V_{COMP} = 5V)	0.90	1.00	1.10	V
Delay to output	T_{PD}	V_{FB} = 0V, Tested with 2V,20nS I_{SNS} pulse			125	ns
Under Current Sense Period	T_{UCS}		175	200	375	ms
Under Current Recovery Period	T_{UCSD}			3		Cycles of PWM Frequency
Cycle Skip	Cycle Skip condition: I_{SENS} ≤ 0.1V	ASM8P18S42ER		1/4		x f_S
		ASM8P18S43ER		1/10		
		ASM8P18S45ER		No Skip		
OUTPUT SECTION						
Low Output Voltage	V_{OL}	I_{SINK} = 0.2A	0.312	0.424	0.57	V
High Output Voltage	V_{OH}	I_{SOURCE} = 0.2A	13.4	13.91	14.37	V
On Resistance, High	$R_{DS(ON)H}$	I_{SOURCE} = 0.2A		6.5		Ω
On Resistance, Low	$R_{DS(ON)L}$	I_{SINK} = 0.2A		2.5		Ω
Rise Time	t_R	T_A = 25°C, C_L=1nF		20		nS
Fall Time	t_F	T_A = 25°C, C_L=1nF		20		nS
Shoot Through Current				0		
UVLO SECTION						
Start threshold	$V_{th(START)}$	ASM8P18S43ER, ASM8P18S45ER,	5.8		9.5	V
		ASM8P18S42ER	12		16.5	
Stop Threshold	$V_{th(STOP)}$	ASM8P18S43ER, ASM8P18S45ER	5.5		7.9	V
		ASM8P18S42ER,	7		11.9	
PWM frequency	f_S		40		400	kHz

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Parameter	Symbol	Conditions	Min	Typ	Max	Units
Duty Cycle Range		ASM8P18S45ER	0	40	47	%
		ASM8P18S42ER, ASM8P18S43ER	0		99	
TOTAL DEVICE						
Start up Current	I_{ST}			250	700	µA
Peak Output Current	$I_{OUT(PK)}$			1		A
Operating Current	$I_{CC(OPR)}$	$V_{FB} = 0$; $I_{SNS} = 0.5$; $V_{DD} = 15V$		5	6.2	mA
Thermal Shutdown	V_Z	Junction Temp		155		°C
Thermal Recovery				126		°C
SPREAD SPECTRUM SECTION						
Modulation Rate		ASM8P18S43ER, ASM8P18S45ER		1/20		of Switching Frequency (KHz)
		ASM8P18S42ER,		1/10		
Modulation Depth		Refer R_{SS} Resistor Vs. % Modulation Depth Plot				
OSCILLATOR SECTION						
Initial Accuracy	f_{INIT}	$T_A =25°C$	47	48	49	KHz
Frequency change with Voltage	Df/d V_{CC}			0.04	0.05	%
Oscillator Amplitude			1.7	1.76	1.8	V (Pk-Pk)
Temperature Stability	T_{OSC}			0.12		%/°C

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Frequency Selection Curve
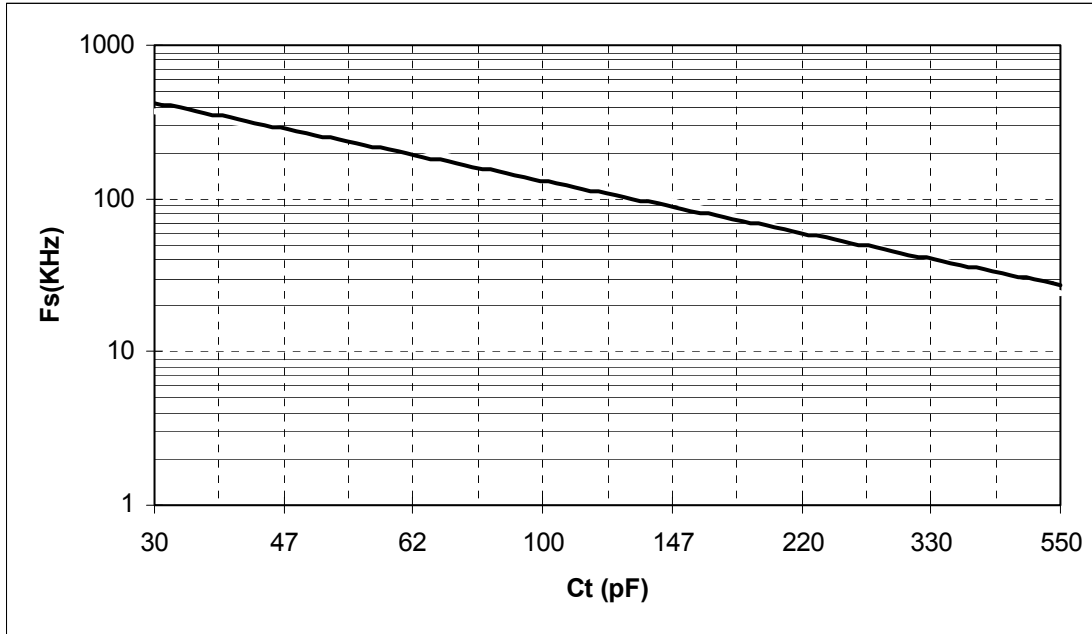
R$_{SS}$ Resistor vs. Modulation Depth (MD)

ASM8P18S42ER   **ASM8P18S43ER_ ASM8P18S45ER**

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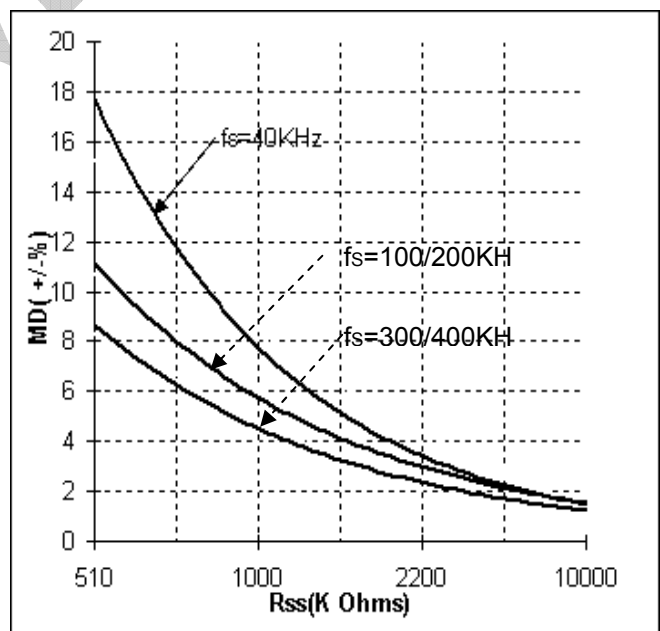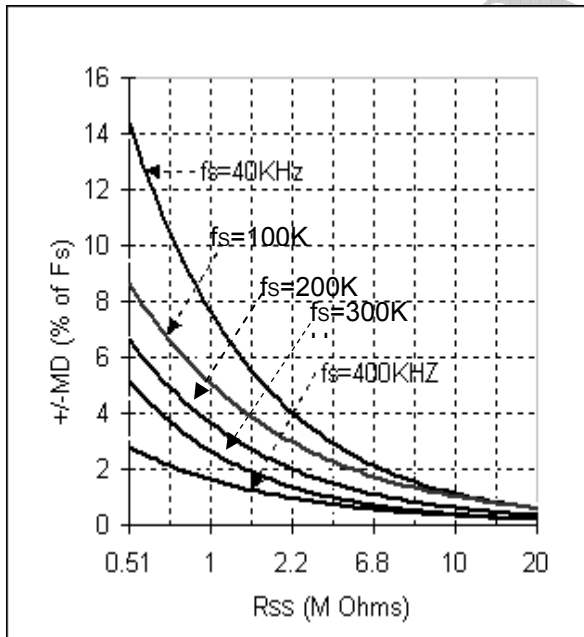
Package Information

8-lead MSOP Package
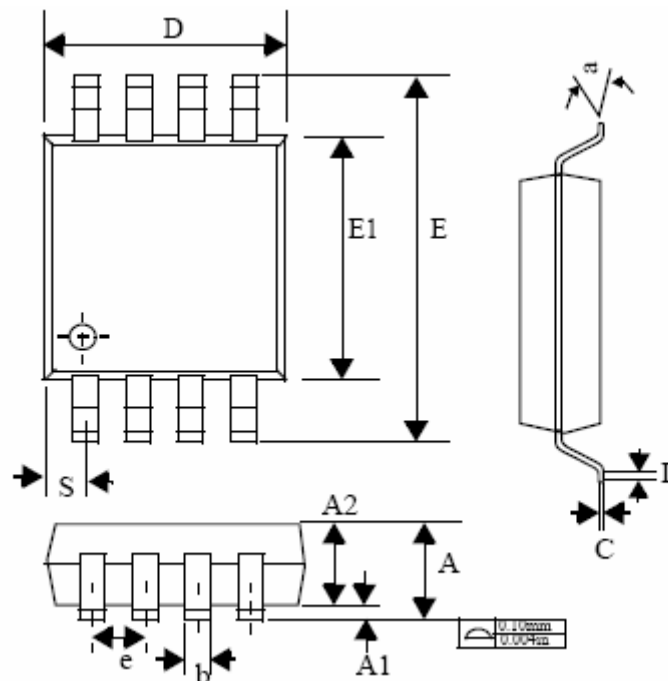
Symbol	Dimensions			
	Inches		Millimeters	
	Min	Max	Min	Max
A	0.032	0.044	0.81	1.10
A1	0.002	0.006	0.05	0.15
A2	0.030	0.038	0.76	0.97
b	0.012 BSC		0.30 BSC	
C	0.004	0.008	0.10	0.20
D	0.114	0.122	2.90	3.10
e	0.0256 BSC		0.65 BSC	
E1	0.114	0.122	2.90	3.10
E	0.184	0.200	4.67	5.08
L	0.016	0.026	0.41	0.66
θ	0°	6°	0°	6°
S	0.0206 BSC		0.52 BSC	

8-lead (150-mil) SOIC Package
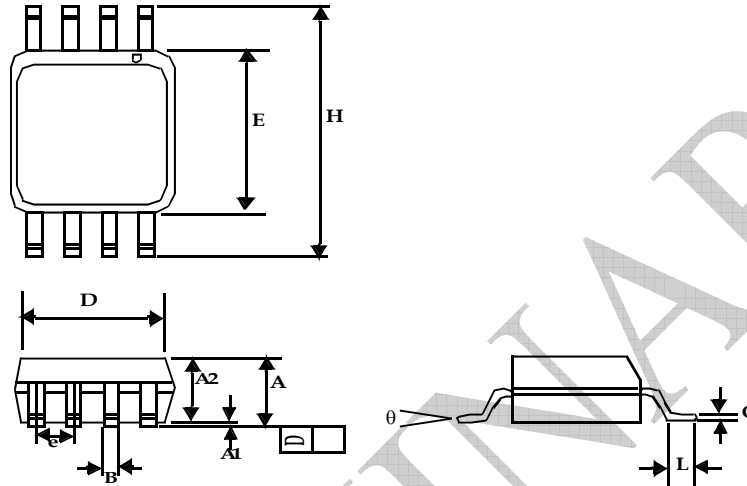
Symbol	Dimensions			
	Inches		Millimeters	
	Min	Max	Min	Max
A1	0.004	0.010	0.10	0.25
A	0.053	0.069	1.35	1.75
A2	0.049	0.059	1.25	1.50
B	0.012	0.020	0.31	0.51
C	0.007	0.010	0.18	0.25
D	0.193 BSC		4.90 BSC	
E	0.154 BSC		3.91 BSC	
e	0.050 BSC		1.27 BSC	
H	0.236 BSC		6.00 BSC	
L	0.016	0.050	0.41	1.27
θ	0°	8°	0°	8°

8-lead PDIP Package
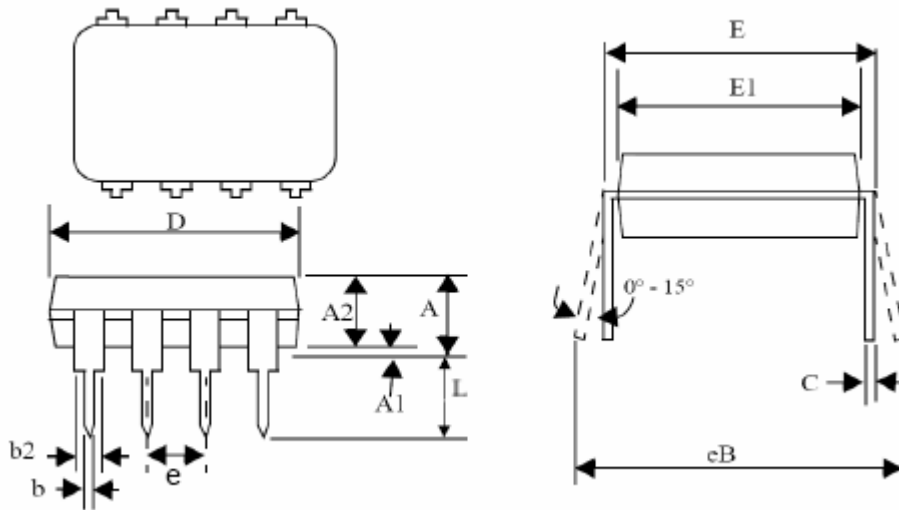
Symbol	Dimensions			
	Inches		Millimeters	
	Min	Max	Min	Max
A		0.210		5.33
A1	0.015		0.38	
A2	0.115	0.195	2.92	4.95
b	0.014	0.022	0.36	0.56
b2	0.045	0.070	1.14	1.78
C	0.008	0.014	0.20	0.36
D	0.355	0.400	9.02	10.16
E	0.300	0.325	7.62	8.26
E1	0.240	0.280	6.10	7.11
e	0.10 BSC		2.54 BSC	
eB		0.430		10.92
L	0.115	0.150	2.92	3.81

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Ordering Information

Part Number	Package	Operating Temp Range	Start-up Voltage (Typ)	Minimum Operating Voltage (Typ)	Maximum Duty Cycle	Top Mark
ASM8I18S42ERF-08-Px	8-PDIP	-40 to 85°C	15.4V	10.2V	99	8I18S42ERF
ASM8I18S42ERF-08-Sx	8-SOIC	-40 to 85°C	15.4V	10.2V	99	8I18S42ERF
ASM8P18S42ERF-08-Px	8-PDIP	0 to 70°C	15.4V	10.2V	99	8P18S42ERF
ASM8P18S42ERF-08-Mx	8-MSOP	0 to 70°C	15.4V	10.2V	99	8P18S42ERF
ASM8P18S42ERF-08-Sx	8-SOIC	0 to 70°C	15.4V	10.2V	99	8P18S42ERF
ASM8I18S43ERF-08-Px	8-PDIP	-40 to 85°C	7.8V	6.7V	99	8I18S43ERF
ASM8I18S43ERF-08-Sx	8-SOIC	-40 to 85°C	7.8V	6.7V	99	8I18S43ERF
ASM8P18S43ERF-08-Px	8-PDIP	0 to 70°C	7.8V	6.7V	99	8P18S43ERF
ASM8P18S43ERF-08-Mx	8-MSOP	0 to 70°C	7.8V	6.7V	99	8P18S43ERF
ASM8P18S43ERF-08-Sx	8-SOIC	0 to 70°C	7.8V	6.7V	99	8P18S43ERF
ASM8I18S45ERF-08-Px	8-PDIP	-40 to 85°C	7.8V	6.7V	47	8I18S45ERF
ASM8I18S45ERF-08-Sx	8-SOIC	-40to 85°C	7.8V	6.7V	47	8I18S45ERF
ASM8P18S45ERF-08-Px	8-PDIP	0 to 70°C	7.8V	6.7V	47	8P18S45ERF
ASM8P18S45ERF-08-Mx	8-MSOP	0 to 70°C	7.8V	6.7V	47	8P18S45ERF
ASM8P18S45ERF-08-Sx	8-SOIC	0 to 70°C	7.8V	6.7V	47	8P18S45ERF
ASM8I18S42ERG-08-Px	8-PDIP	-40 to 85°C	15.4V	10.2V	99	8I18S42ERG
ASM8I18S42ERG-08-Sx	8-SOIC	-40 to 85°C	15.4V	10.2V	99	8I18S42ERG
ASM8P18S42ERG-08-Px	8-PDIP	0 to 70°C	15.4V	10.2V	99	8P18S42ERG
ASM8P18S42ERG-08-Mx	8-MSOP	0 to 70°C	15.4V	10.2V	99	8P18S42ERG
ASM8P18S42ERG-08-Sx	8-SOIC	0 to 70°C	15.4V	10.2V	99	8P18S42ERG
ASM8I18S43ERG-08-Px	8-PDIP	-40 to 85°C	7.8V	6.7V	99	8I18S43ERG
ASM8I18S43ERG-08-Sx	8-SOIC	-40 to 85°C	7.8V	6.7V	99	8I18S43ERG
ASM8P18S43ERG-08-Px	8-PDIP	0 to 70°C	7.8V	6.7V	99	8P18S43ERG
ASM8P18S43ERG-08-Mx	8-MSOP	0 to 70°C	7.8V	6.7V	99	8P18S43ERG
ASM8P18S43ERG-08-Sx	8-SOIC	0 to 70°C	7.8V	6.7V	99	8P18S43ERG
ASM8I18S45ERG-08-Px	8-PDIP	-40 to 85°C	7.8V	6.7V	47	8I18S45ERG
ASM8I18S45ERG-08-Sx	8-SOIC	-40 to 85°C	7.8V	6.7V	47	8I18S45ERG
ASM8P18S45ERG-08-Px	8-PDIP	0 to 70°C	7.8V	6.7V	47	8P18S45ERG
ASM8P18S45ERG-08-Mx	8-MSOP	0 to 70°C	7.8V	6.7V	47	8P18S45ERG
ASM8P18S45ERG-08-Sx	8-SOIC	0 to 70°C	7.8V	6.7V	47	8P18S45ERG

Note: All Alliance Semiconductor Lead Free Parts are RoHS Compliant. The symbol "F" (at the end of the Ordering Information) indicates Lead Free parts, whereas "G" indicates Green parts.

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Licensed under US Patent numbers 5,488,627 and 5,631,921 and other US Patents pending.

Note: This product utilizes US Patent # 6,646,463 Impedance Emulator Patent issued to Alliance Semiconductor, dated 11-11-2003, and Other US Patents pending.

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