

Silicon NPN Power Transistors

BU500

DESCRIPTION

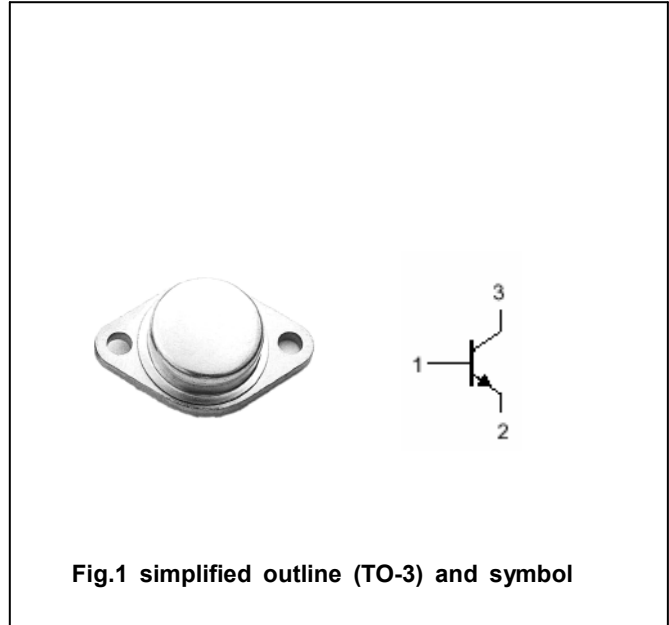
- With TO-3 package
- Low collector saturation voltage

APPLICATIONS

- Designed for use in large screen color deflection circuits.

PINNING(see fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

Absolute maximum ratings (T_c=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	1500	V
V _{CEO}	Collector-emitter voltage	Open base	700	V
V _{EBO}	Emitter-base voltage	Open collector	5	V
I _C	Collector current		6	A
I _{CM}	Collector current-peak		16	A
I _B	Base current		4	A
P _D	Total power dissipation	T _c =25°C	75	W
T _j	Junction temperature		-65~150	°C
T _{stg}	Storage temperature		-65~150	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal resistance junction to case	1.66	°C/W

Silicon NPN Power Transistors

BU500

CHARACTERISTICS

T_j=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-emitter sustaining voltage	I _C =0.5A; I _B =0; L=10mH	700			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =100mA; I _C =0	5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =4.5A; I _B =2A			1.0	V
V _{BE}	Base-emitter on voltage	I _C =4.5A; V _{CE} =5V			1.3	V
I _{CBO}	Collector cut-off current	V _{CE} =1000V; V _{BE} =-2V			0.02	mA
I _{CEX}	Collector cut-off current	V _{CE} =1500V; V _{BE} =-2V			1.0	mA
I _{EBO}	Emitter cut-off current	V _{EB} =4V; I _C =0			10	mA
h _{FE-1}	DC current gain	I _C =1A; V _{CE} =5V	8		36	
h _{FE-2}	DC current gain	I _C =4.5A; V _{CE} =5V	3.0			

Switching times

t _s	Storage time	I _C =4.5A; I _{B1} =-I _{B2} =1.5A V _{CC} =100V;			1.2	μs
t _f	Fall time				1.0	μs

Silicon NPN Power Transistors

BU500

PACKAGE OUTLINE

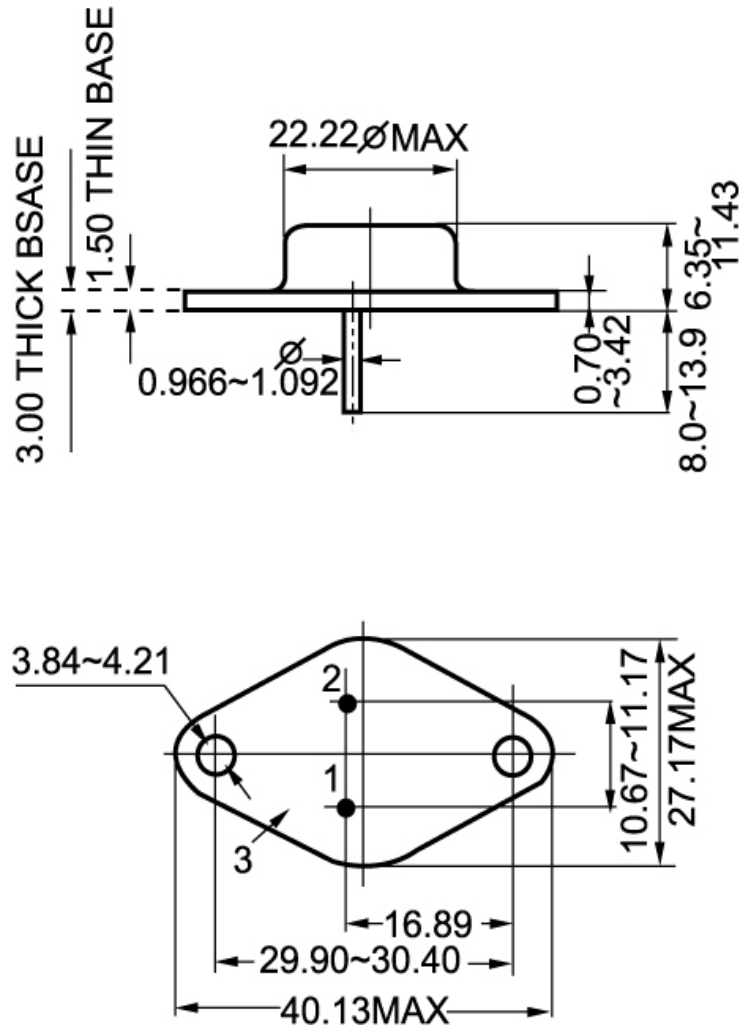


Fig.2 Outline dimensions