

# ISDN S-INTERFACE TRANSFORMER

## Through Hole, Single, 3000Vrms



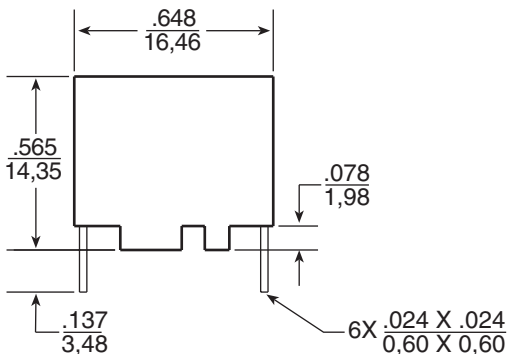
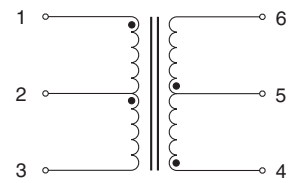
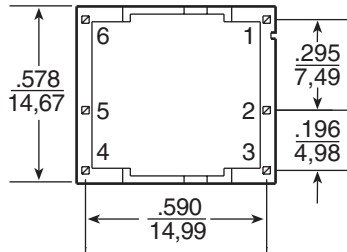
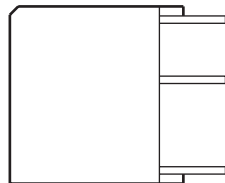
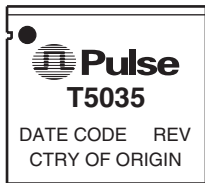
- Meets the pulse waveform template of CCITT I.430 when recommended transformer and chip pair are used
- Industry standard footprint
- 5mA  $\Delta I_{dc}$  - Ideal for NT1 applications

**Electrical Specifications @ 25°C — Operating Temperature 0°C to 70°C**

Part Number	Transformer							$\Delta I_{dc}$ (mA MAX)	Secondary Pins	Package/ Schematic
	Ratio $\pm 2\%$ (Pri:Sec)	OCL Pri (mH MIN)	L <sub>L</sub> Sec ( $\mu$ H MAX)	C <sub>w/w</sub> (pF MAX)	CD Pri (pF Max)	DCR Pri ( $\Omega$ + 25% MAX)	DCR Sec ( $\Omega$ MAX)			
T5035	1CT:2CT	30	15	75	50	3.4	5.4	5	1 - 3	MAX-1

### Mechanical

### Schematic

**MAX-1**


Weight ..... .6.01 grams  
 Tube ..... .38/tube

Dimensions:  $\frac{\text{Inches}}{\text{mm}}$   
 Unless otherwise specified, all tolerances are  $\pm \frac{.005}{0,13}$

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## Transformer Selection Guide

IC Manufacturer	IC Part Number	Pulse Part Number
AMD	AM79C 30A/32A	T5035
Lucent	T7903	T5035
Mietec	MTC-2072	T5035
Mitel	MT8930 / 8931	T5035
National	TP3420 / 3421	T5035
SGS Thomson	ST5420 / 5421	T5035
Siemens	PEB2080 / 2081 / 2084 / 2085 / 2086 PSB 2186	T5035
Yamaha	YM7405B	T5035

## Thru Hole Common Mode Chokes

Part Number	Number of Lines	Inductance	Peak Common Mode Attenuation	Application	Isolation Voltage	DCR ( $\Omega$ MAX)	Data Sheet
<b>LOW FREQUENCY CHOKES</b>							
PE-65950	4-Line	4.7mH $\pm$ 30%	1MHz	Improve Balance	500Vrms	1.00	G002
<b>HIGH FREQUENCY CHOKES</b>							
PE-67501	4-Line	50 $\mu$ H Min	50MHz	EMI Reduction	500Vrms	0.30	G002

## Application Notes

### Flammability

Materials used in the products are recognized UL 94-VO. Products meet IEC 695-2-2 requirements (needle flame test).

### Definition of Terms

**Ratio** – This is the turns ratio, expressed as “Primary: Secondary”. The term “CT” designates center-tapped winding.

**OCL** – Open circuit inductance, measured 20kHz, 100mV.

**L<sub>L</sub> Sec** – Leakage inductance measured across the secondary with the respective primary winding short circuited.

**C<sub>w/w</sub>** – Winding capacitance, formed by the primary and secondary wire. These wires form the “plates” of this capacitor. Measured at 100kHz, 20mV.

**CD Pri** – This is the distributed capacitance.

**DCR** – This is the resistance of the windings when measured in DC conditions.

**$\Delta I_{DC}$**  – The maximum specified unbalanced DC current capability of the device.

The minimum primary inductance and the maximum distributed capacitance satisfy the transmitter output and receiver input impedance requirements of CCITT I.430 for both TE and NT. The maximum distributed capacitance allows sufficient margin for the capacitance of the IC and a protection diode network. It is consistent with the overall maximum value specified and the permitted length of the basic access TE cord.

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