

# DATA SHEET

## **BUK107-50DL** PowerMOS transistor Logic level TOPFET

Product specification  
Supersedes data of September 1994  
File under Discrete Semiconductors, SC13a

March 1997

**PowerMOS transistor  
Logic level TOPFET**

**BUK107-50DL**

**DESCRIPTION**

Monolithic overload protected logic level power MOSFET in a surface mount plastic envelope, intended as a general purpose switch for automotive systems and other applications.

**APPLICATIONS**

- General controller for driving
- lamps
  - small motors
  - solenoids

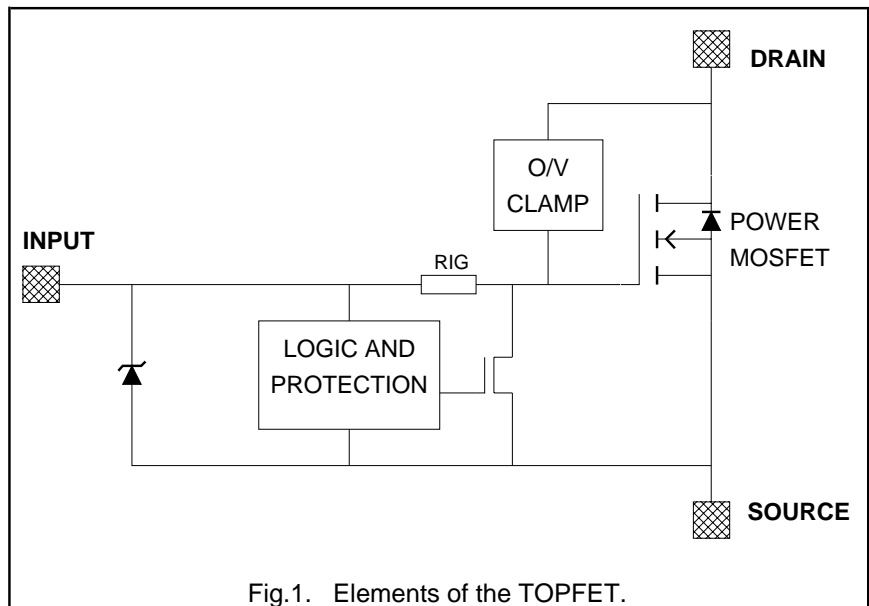
**FEATURES**

- Vertical power DMOS output stage
- Overload protected up to 85°C ambient
- Overload protection by current limiting and overtemperature sensing
- Latched overload protection reset by input
- 5 V logic compatible input level
- Control of power MOSFET and supply of overload protection circuits derived from input
- Low operating input current permits direct drive by micro-controller
- ESD protection on all pins
- Overvoltage clamping for turn off of inductive loads

**QUICK REFERENCE DATA**

SYMBOL	PARAMETER	MAX.	UNIT
$V_{DS}$	Continuous drain source voltage	50	V
$I_D$	Continuous drain current	0.7	A
$P_D$	Total power dissipation	1.8	W
$T_j$	Continuous junction temperature	150	°C
$R_{DS(ON)}$	Drain-source on-state resistance	200	mΩ

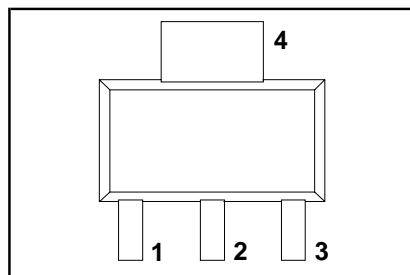
**FUNCTIONAL BLOCK DIAGRAM**



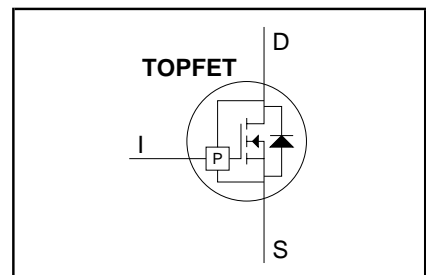
**PINNING - SOT223**

PIN	DESCRIPTION
1	input
2	drain
3	source
4	drain (tab)

**PIN CONFIGURATION**



**SYMBOL**



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### LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{DS}$	Continuous drain source voltage <sup>1</sup>	-	-	50	V
$I_D$	Continuous drain current <sup>2</sup>	-	-	self limiting	A
$I_I$	Continuous input current	clamping	-	3	mA
$I_{IRM}$	Non-repetitive peak input current	$t_p \leq 1$ ms	-	10	mA
$P_D$	Total power dissipation	$T_{amb} = 25$ °C	-	1.8	W
$T_{stg}$	Storage temperature	-	-55	150	°C
$T_j$	Continuous junction temperature	normal operation <sup>3</sup>	-	150	°C

### ESD LIMITING VALUE

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_C$	Electrostatic discharge capacitor voltage	Human body model; C = 250 pF; R = 1.5 kΩ	-	2	kV

### OVERVOLTAGE CLAMPING LIMITING VALUES

At a drain source voltage above 50 V the power MOSFET is actively turned on to clamp overvoltage transients.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$E_{DSM}$	Non-repetitive clamping energy	$T_b \leq 25$ °C; $I_{DM} < I_{D(lim)}$ ; inductive load	-	100	mJ
$E_{DRM}$	Repetitive clamping energy	$T_b \leq 75$ °C; $I_{DM} = 50$ mA; f = 250 Hz	-	4	mJ

### OVERLOAD PROTECTION LIMITING VALUES

With the protection supply provided via the input pin, TOPFET can protect itself from short circuit loads. Overload protection operates by means of drain current limiting and activating the overtemperature protection.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{DDP}$	Protected drain source supply voltage	$V_{IS} = 5$ V	-	35	V
		$V_{IS} = 4$ V	-	16	V

### OVERLOAD PROTECTION CHARACTERISTICS

TOPFET switches off to protect itself when there is an overload fault condition. It remains latched off until reset by the input.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
	<b>Overload protection</b>					
$I_{D(lim)}$	Drain current limiting	$V_{IS} = 5$ V	0.7	1.1	1.5	A
$T_{j(TO)}$	<b>Overtemperature protection</b> Threshold junction temperature	only in drain current limiting $V_{IS} = 5$ V	100	130	160	°C

<sup>1</sup> Prior to the onset of overvoltage clamping. For voltages above this value, safe operation is limited by the overvoltage clamping energy.

<sup>2</sup> Refer to OVERLOAD PROTECTION CHARACTERISTICS.

<sup>3</sup> Not in an overload condition with drain current limiting.

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### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{th\ j-sp}$	<b>Thermal resistance</b> Junction to solder point		-	12	18	K/W
$R_{th\ j-b}$	Junction to board <sup>1</sup>	Mounted on any PCB	-	40	-	K/W
$R_{th\ j-a}$	Junction to ambient	Mounted on PCB of fig. 19	-	-	70	K/W

### STATIC CHARACTERISTICS

$T_b = 25\text{ °C}$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{(CL)DSS}$	Drain-source clamping voltage	$V_{IS} = 0\text{ V}; I_D = 10\text{ mA}$	50	55	-	V
$V_{(CL)DSS}$	Drain-source clamping voltage	$V_{IS} = 0\text{ V}; I_{DM} = 200\text{ mA};$ $t_p \leq 300\text{ }\mu\text{s}; \delta \leq 0.01$	-	56	70	V
$I_{DSS}$	Off-state drain current	$V_{DS} = 45\text{ V}; V_{IS} = 0\text{ V}$	-	0.5	2	$\mu\text{A}$
$I_{DSS}$	Off-state drain current	$V_{DS} = 50\text{ V}; V_{IS} = 0\text{ V}$	-	1	20	$\mu\text{A}$
$I_{DSS}$	Off-state drain current	$V_{DS} = 40\text{ V}; V_{IS} = 0\text{ V}; T_j = 100\text{ °C}$	-	10	100	$\mu\text{A}$
$R_{DS(ON)}$	Drain-source on-state resistance <sup>2</sup>	$V_{IS} = 5\text{ V}; I_{DM} = 100\text{ mA};$ $t_p \leq 300\text{ }\mu\text{s}; \delta \leq 0.01$	-	150	200	$\text{m}\Omega$

### INPUT CHARACTERISTICS

$T_b = 25\text{ °C}$  unless otherwise specified. The supply for the logic and overload protection is taken from the input.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{IS(TO)}$	Input threshold voltage	$V_{DS} = 5\text{ V}; I_D = 1\text{ mA}$	1.7	2.2	2.7	V
$I_{IS}$	Input supply current	normal operation;	$V_{IS} = 5\text{ V}$ -	330	450	$\mu\text{A}$
$I_{ISL}$	Input supply current	protection latched;	$V_{IS} = 4\text{ V}$ -	170	270	$\mu\text{A}$
$I_{ISL}$	Input supply current		$V_{IS} = 5\text{ V}$ -	500	650	$\mu\text{A}$
$I_{ISL}$	Input supply current		$V_{IS} = 3.5\text{ V}$ -	250	400	$\mu\text{A}$
$V_{ISR}$	Protection latch reset voltage <sup>3</sup>		1	2.2	3.5	V
$V_{(CL)IS}$	Input clamping voltage	$I_l = 1.5\text{ mA}$	6	7.5	-	V
$R_{IG}$	Input series resistance	to gate of power MOSFET	-	33	-	$\text{k}\Omega$

### SWITCHING CHARACTERISTICS

$T_{amb} = 25\text{ °C}$ ; resistive load  $R_L = 50\text{ }\Omega$ ; adjust  $V_{DD}$  to obtain  $I_D = 250\text{ mA}$ ; refer to test circuit and waveforms

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$t_{d\ on}$	Turn-on delay time	$V_{IS} = 0\text{ V}$ to $V_{IS} = 5\text{ V}$	-	8	-	$\mu\text{s}$
$t_r$	Rise time		-	30	-	$\mu\text{s}$
$t_{d\ off}$	Turn-off delay time	$V_{IS} = 5\text{ V}$ to $V_{IS} = 0\text{ V}$	-	3	-	$\mu\text{s}$
$t_f$	Fall time		-	6	-	$\mu\text{s}$

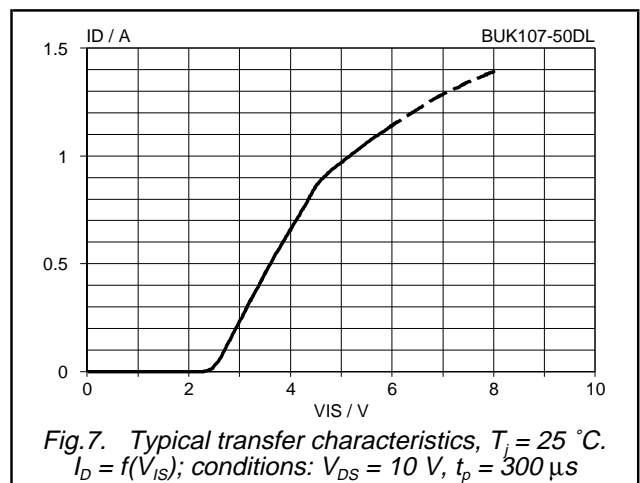
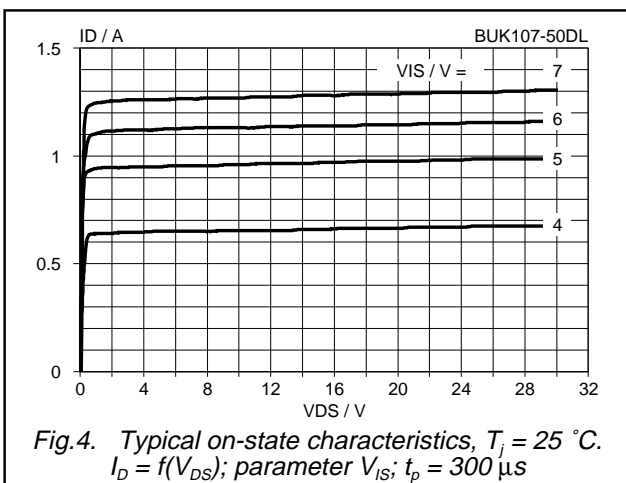
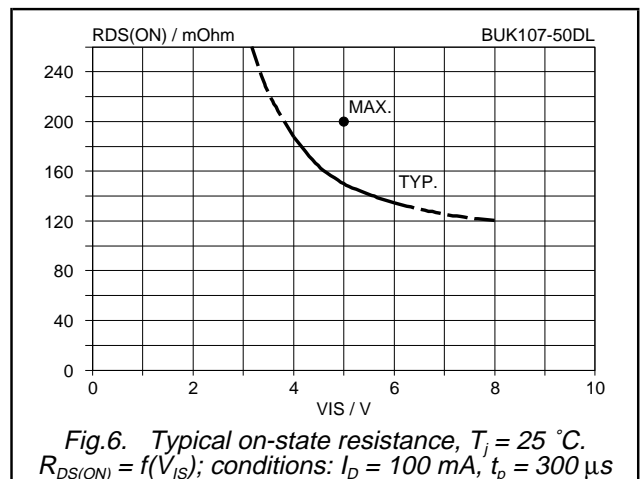
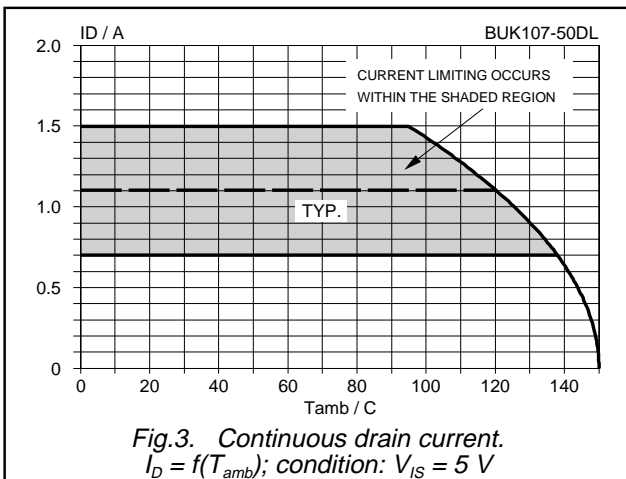
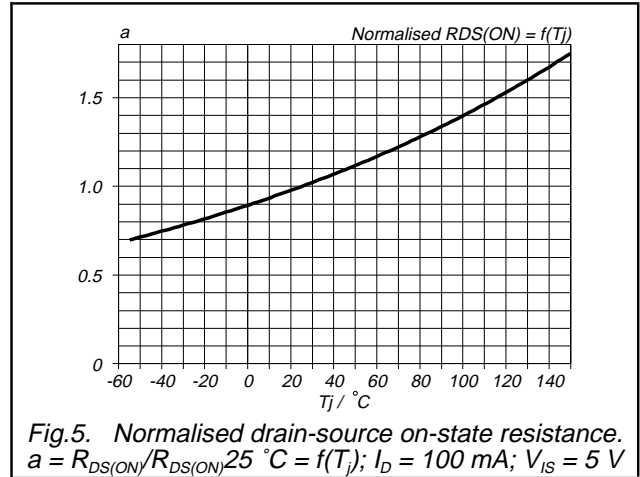
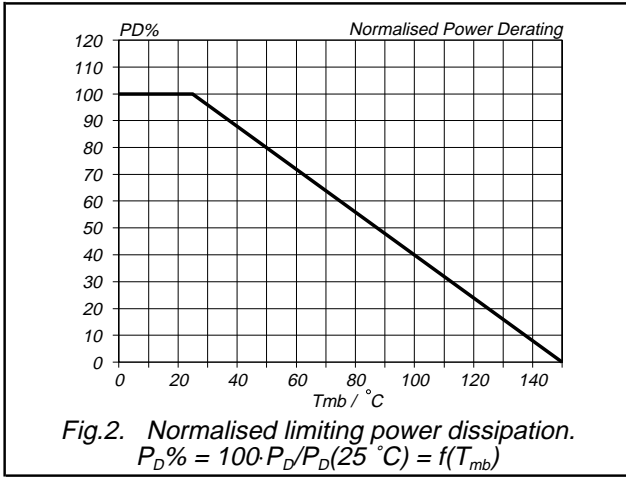
<sup>1</sup> Temperature measured 1.3 mm from tab.

<sup>2</sup> Continuous input voltage. The specified pulse width is for the drain current.

<sup>3</sup> The input voltage below which the overload protection circuits will be reset.

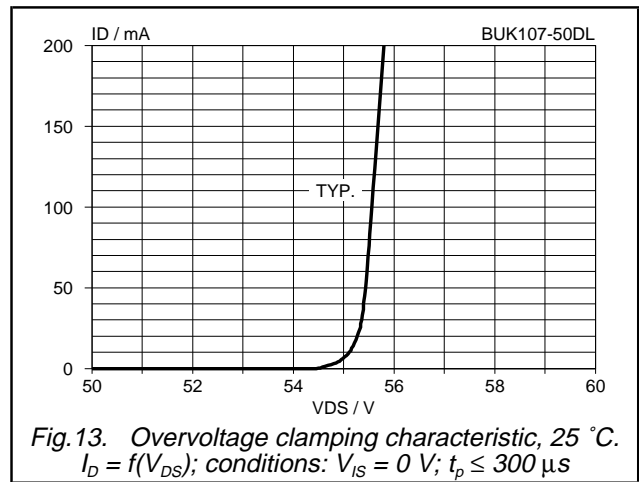
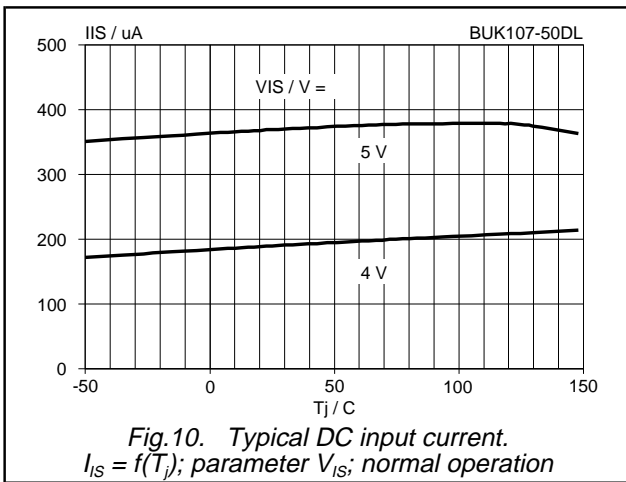
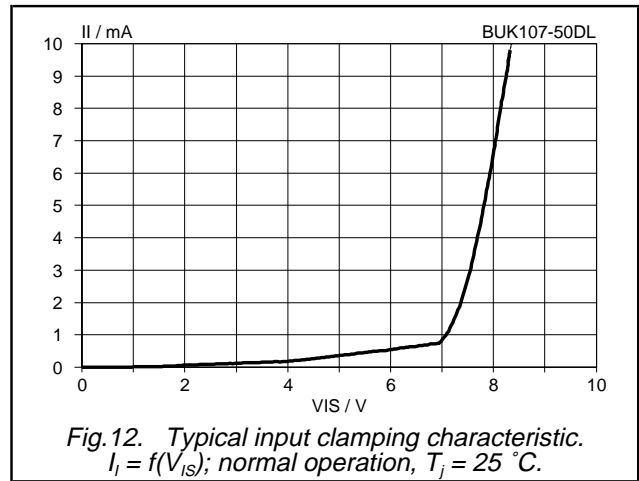
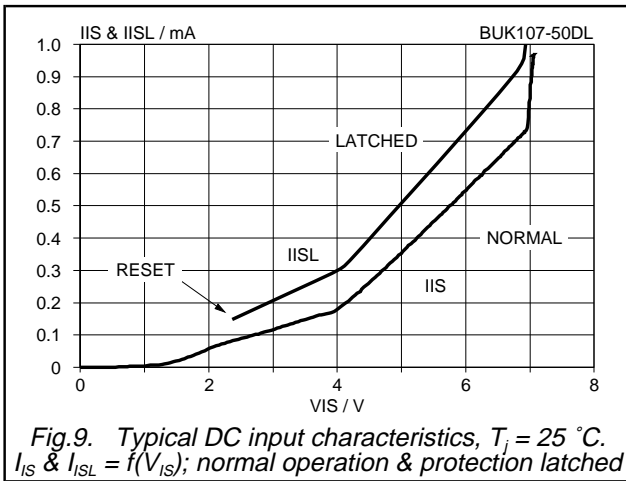
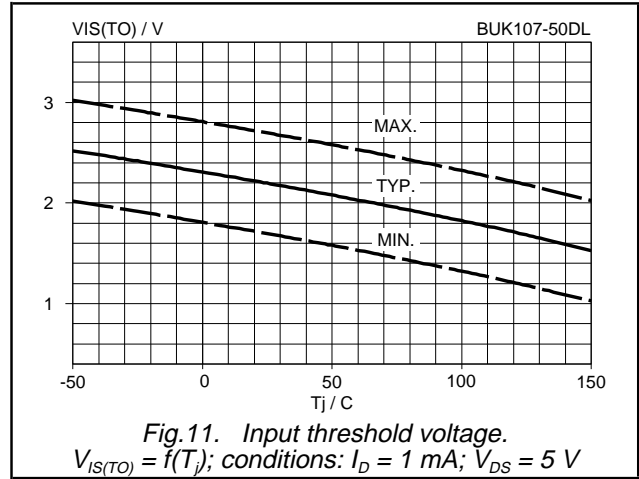
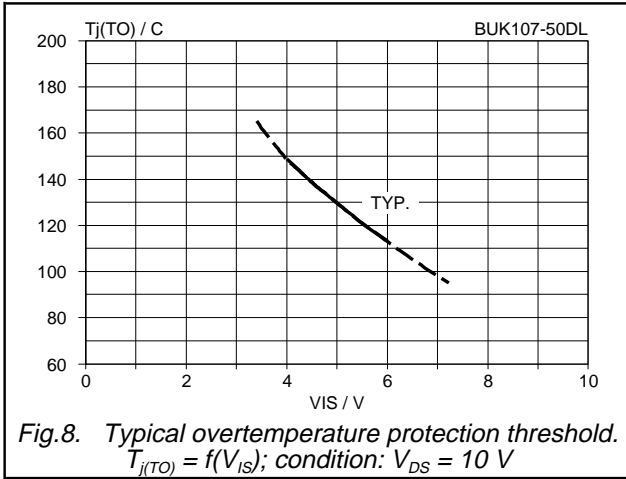
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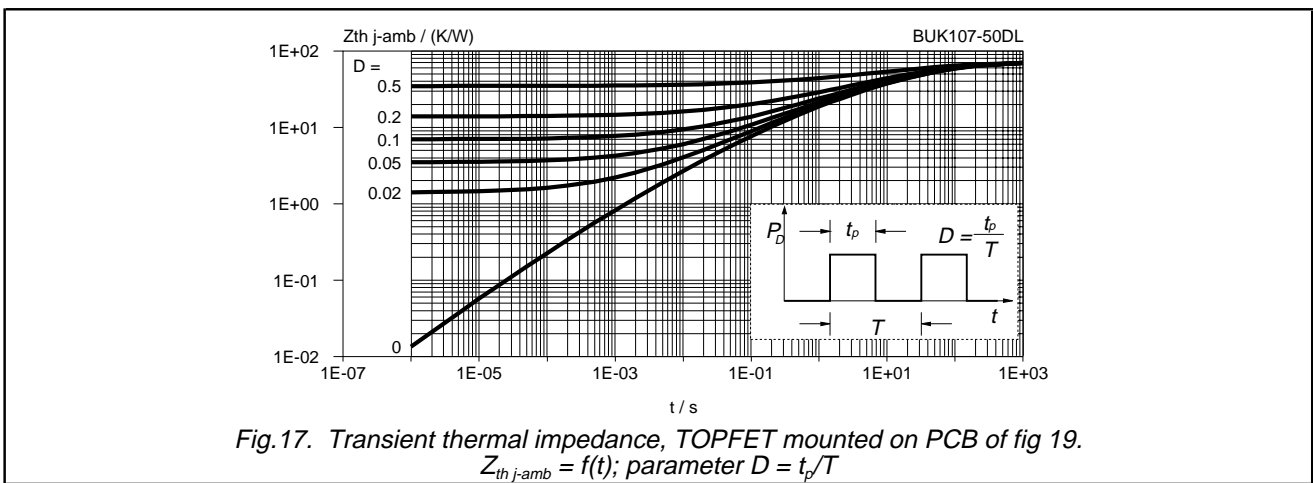
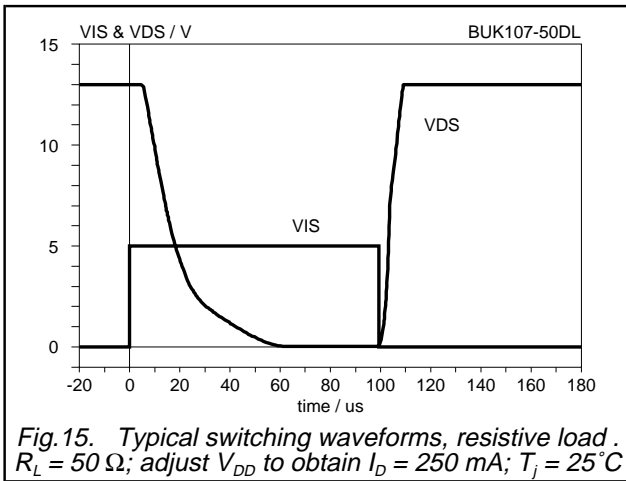
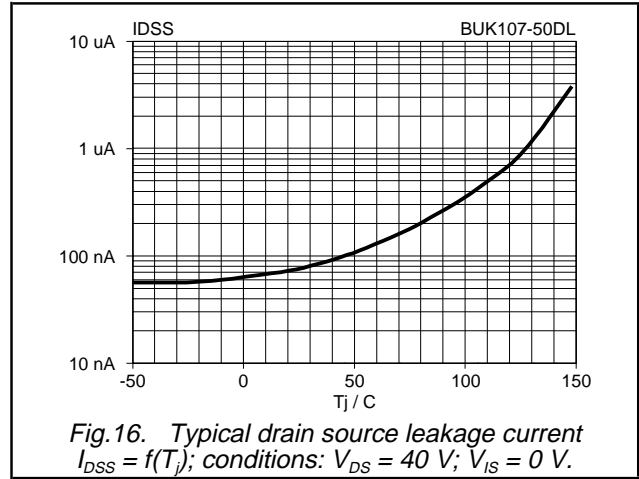
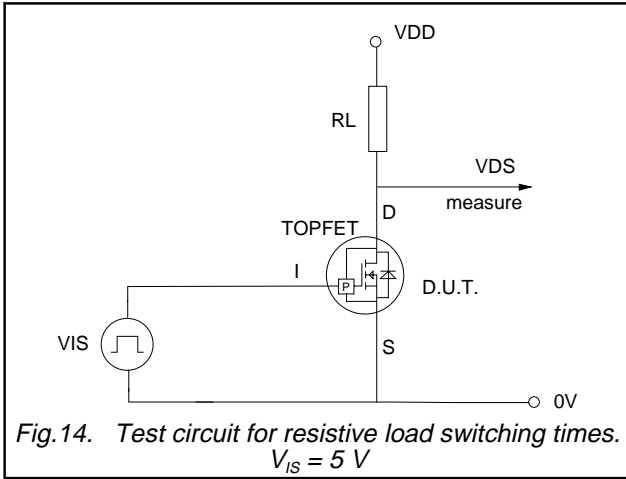
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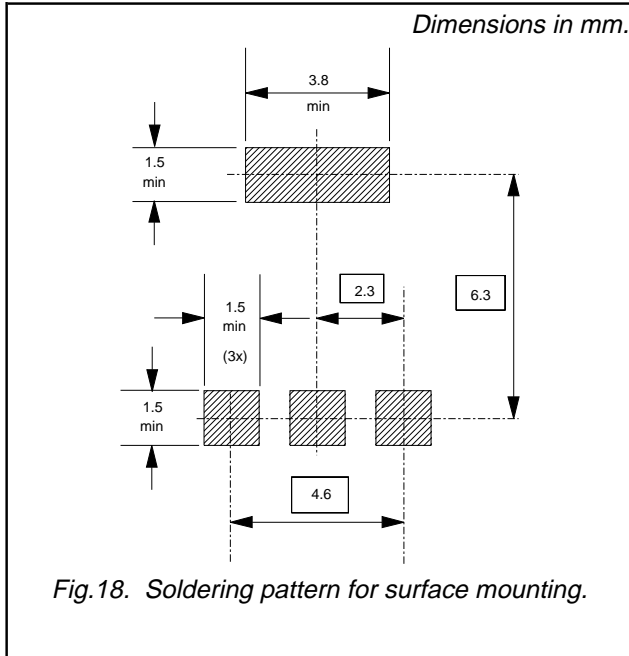
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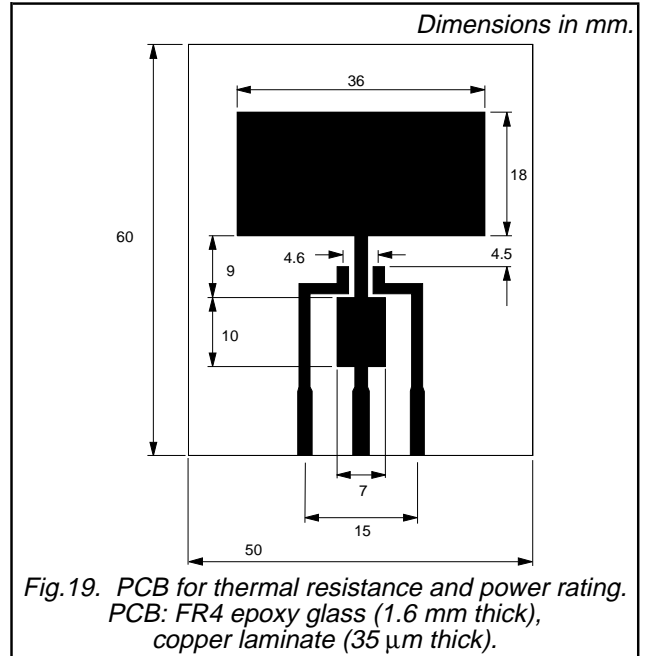
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**MOUNTING INSTRUCTIONS**



**PRINTED CIRCUIT BOARD**





PowerMOS transistor  
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**MECHANICAL DATA**

Dimensions in mm

Net Mass: 0.11 g

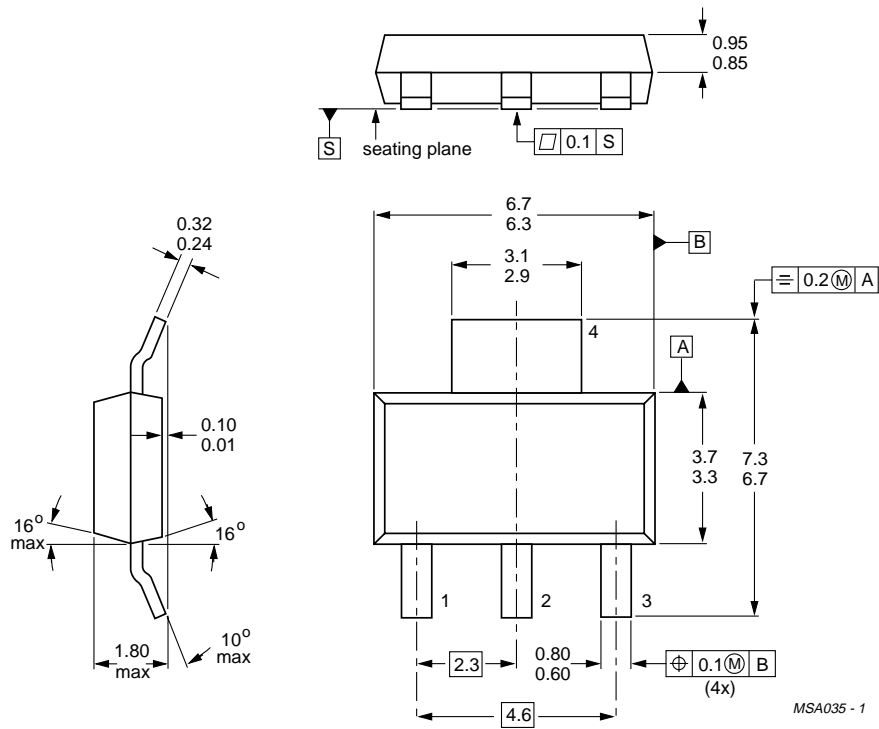


Fig.20. SOT223 surface mounting package<sup>1</sup>.

<sup>1</sup> For further information, refer to surface mounting instructions for SOT223 envelope. Epoxy meets UL94 V0 at 1/8".

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**BUK107-50DL****DEFINITIONS**

<b>Data sheet status</b>	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
<b>Limiting values</b>	
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
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**Mexico:** 5900 Gateway East, Suite 200, EL PASO, TEXAS 79905,  
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**Norway:** Box 1, Manglerud 0612, OSLO,  
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**Philippines:** Philips Semiconductors Philippines Inc.,  
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**Spain:** Balmes 22, 08007 BARCELONA,  
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