# MASW-008322



## GaAs SPDT Switch DC - 20 GHz

Rev. V3

#### **Features**

- Very Broadband Performance
- Low Insertion Loss: 1.9 dB Typical @ 20 GHz
- High Isolation: 40 dB Typical @ 20 GHz
- Fast Switching Speed
- Reflective Configuration
- Ultra Low DC Power Consumption
- Lead-Free 4 mm 24-Lead PQFN Package
- Halogen-Free "Green" Mold Compound
- RoHS Compliant\* and 260°C Reflow Compatible

### **Description**

The MASW-008322 is a versatile, broadband, high isolation SPDT switch offered in a lead-free 4 mm 24-lead PQFN surface mount plastic package. The design incorporates both series and shunt circuit elements with the locations optimized to achieve outstanding broadband performance to 20 GHz. The combination of broadband performance along with very fast switching and excellent settling time make this device ideal for many applications, including Test & Measurement, EW and broadband communication systems.

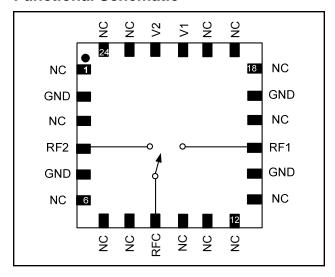
The MASW-008322 is fabricated using M/A-COM Technology's mature 0.5um Low Gate-Lag pHEMT process. This robust process features full surface passivation for a high performance and high reliability.

## Ordering Information<sup>1,2</sup>

Part Number	Package		
MASW-008322-TR0500	500 piece reel		
MASW-008322-TR1000	1000 piece reel		
MASW-008322-001SMB	Sample Board		

- 1. Reference Application Note M513 for reel size information.
- 2. All sample boards include 2 loose parts.

#### **Functional Schematic**



## Pin Configuration<sup>3</sup>

Pin No.	Function	Pin No.	Function	
1	NC	14	GND	
2	GND	15	RF1	
3	NC	16	NC	
4	RF2	17	GND	
5	GND	18	NC	
6	NC	19	NC	
7	NC	20	NC	
8	NC	21	V1	
9	RFC	22	V2	
10	NC	23	NC	
11	NC	24	NC	
12	NC	25	Paddle <sup>4</sup>	
13	NC			

- 3. M/A-COM Technology recommends that all unused (N/C) pins be connected to ground. All data on this datasheet was taken with N/C pins connected to ground.
- 4. The exposed pad centered on the package bottom must be connected to RF and DC ground.

 <sup>\*</sup> Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.



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# Electrical Specifications<sup>5</sup>: $T_A = +25^{\circ}C^6$ , $Z_0 = 50\Omega$ , $V_{1,2} = -5/0V$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Insertion Loss	1 GHz 10 GHz 18 GHz 20 GHz	dB		0.4 1.0 1.5 1.9	0.55 — — 2.5
Isolation	1 GHz 10 GHz 18 GHz 20 GHz		50 — — 39	52 48 43 40	
VSWR	1 - 20 GHz	Ratio	_	2.0:1	_
Trise, Tfall	10% to 90% RF and 90% to 10% RF	ns	_	9	_
Ton, Toff	50% control to 90% RF, and 50% control to 10% RF	ns	_	12	25 <sup>7</sup>
Input P1dB	0.5 - 20 GHz, 0 to -5 V	dBm	_	30	_
IIP3	Two Tone, +5 dBm/Tone, 5 MHz Spacing, >50 MHz 0.5 - 20 GHz	dBm	_	48	_
Settling Time	50% Vctl to 0.1 dB of final value	μs	_	1	
Control Current (Complementary Logic)	V <sub>IN</sub> Low, -5 V V <sub>IN</sub> High, 0 V	μA	_	3	50 20

- 5. All specifications apply with 50 Ω impedance connected to all RF ports, 0 and –5 VDC control voltages.
- 6. Loss changes 0.0025 dB/°C (from -55°C to +85°C).
- 7. Max switching speed guaranteed by design.

## **Absolute Maximum Ratings<sup>8</sup>**

Parameter	Absolute Maximum		
Control Voltage	-8.5 VDC		
Input RF Power	+33 dBm		
Operating Temperature	-40 to +85°C		
Storage Temperature	-65°C to +150°C		

8. Exceeding any one or combination of these limits may cause permanent damage to this device.

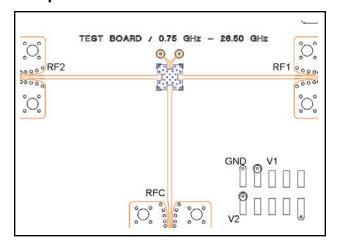
## Truth Table 9

Contro	Control Inputs		Condition of Switch	
V1	V2	RF1	RF2	
High	Low	Off	On	
Low	High	On	Off	

9. Vlow = -5 V, Vhigh = 0 V

Commitment to produce in volume is not guaranteed.

### Sample Board



typical. Mechanical outline has been fixed. Engineering samples and/or test data may be available.

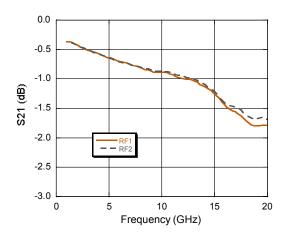


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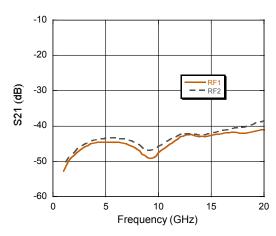
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## **Typical Performance Curves**

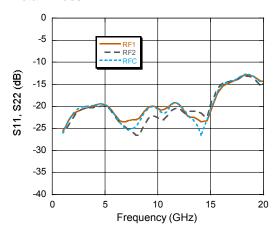
#### Insertion Loss



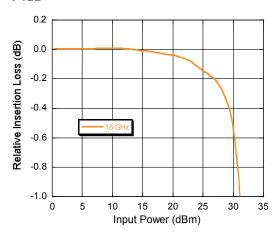
#### Isolation



#### Return Loss



#### P1dB



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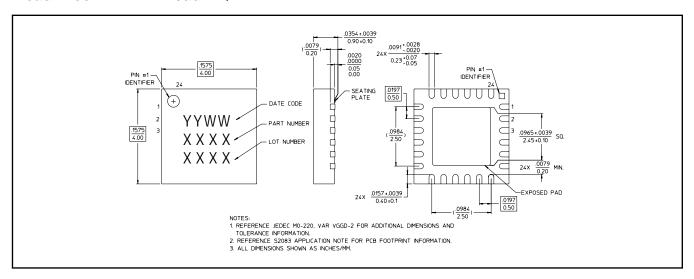
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### Lead Free 4 mm 24-Lead PQFN †



<sup>&</sup>lt;sup>†</sup> Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 1 requirements. Plating is 100% matte tin over copper.

### **Handling Procedures**

Please observe the following precautions to avoid damage:

#### **Static Sensitivity**

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Commitment to produce in volume is not guaranteed.

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