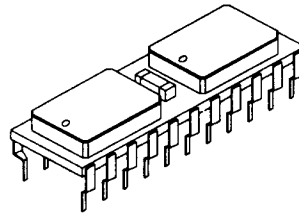


NOT RECOMENDED FOR NEW DESIGNS

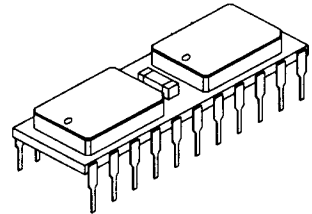
DESCRIPTION:

The DPS7M464/DPS8M464 are 16K X 4 SRAM module built with four 16K X 1 CMOS SRAMs. The memory utilizes asynchronous circuitry and may be maintained in any state for an indefinite period of time. All pins are TTL compatible, and a single +5V power supply is required.

The DPS7M464 has leads formed to allow it to be inserted in a socket or board with 0.300" centers.



DPS7M464

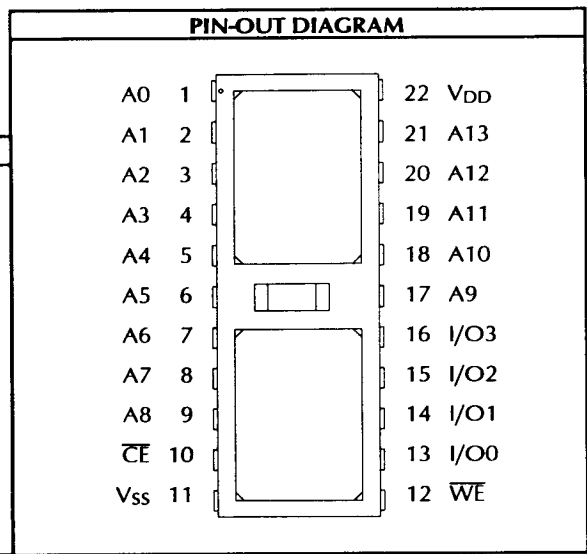
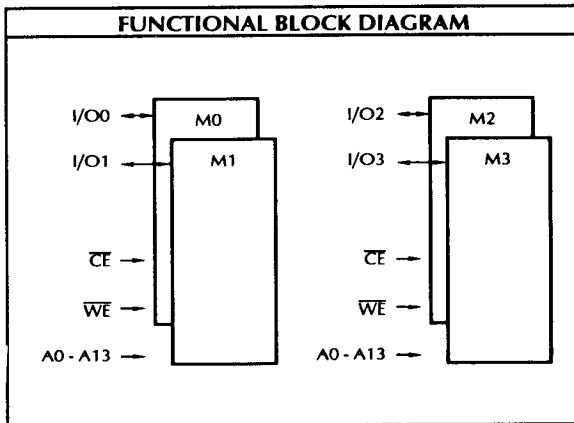


DPS8M464

FEATURES:

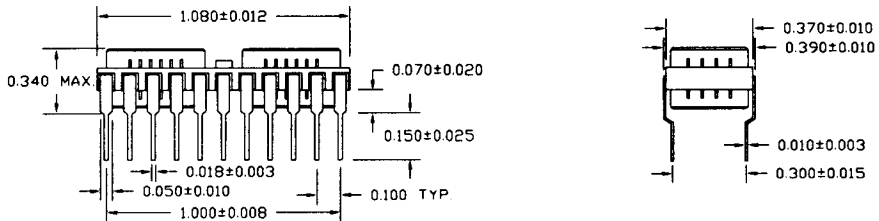
- 4K X 8 Organization
- Access Times: 25, 35, 45ns (max.)
- Fully Static Operation; No Clock or Refresh Required
- TTL-compatible Inputs and Outputs
- Common Data Inputs and Outputs
- Single +5V Operation ($\pm 10\%$ Tolerance)
- Available with All Semiconductor Components used in the Construction of the Module Compliant to MIL-STD-883; Class B
- 22-Pin, Ceramic DIP Package

PIN NAMES	
A0 - A13	Address Inputs
I/O0 - I/O3	Data Inputs/Outputs
\overline{CE}	Chip Enable
\overline{WE}	Write Enable
V _{DD}	Power (+5V)
V _{SS}	Ground

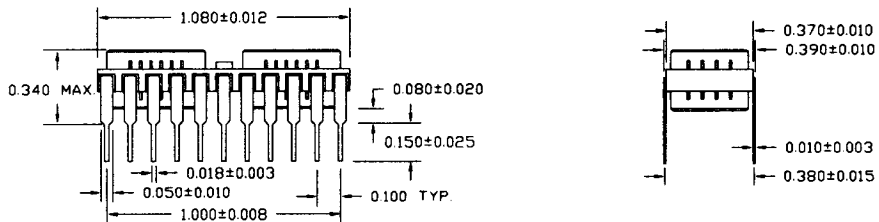


NOT RECOMENDED FOR NEW DESIGNS

DPS7M464 - MECHANICAL DRAWING



DPS8M464 - MECHANICAL DRAWING



- CONTACT DENSE-PAC SALES FOR MORE INFORMATION -

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