

**TECHNICAL DESCRIPTION**

**FEATURES**

- 1930 - 1990 MHz
- LOW LOSS
- LOW VSWR
- SURFACE MOUNT
- LOW COST

**MULTI-MIX<sup>®</sup> DELAY LINES**

**APPLICATIONS**

- PCS
- BASE STATION POWER AMPLIFIER LINEARIZATION NETWORKS
  - FEED FORWARD
  - PRE-DISTORTION
- ADAPTIVE INTERFERENCE CANCELLATION



The DLL-115R-1.96G, a member of the Multi-Mix<sup>®</sup> DLL family of delay lines, provides a mean delay of 11.45 nS in the PCS band and features low insertion loss and low VSWR. The DLL-115R-1.96G exhibits excellent phase linearity and amplitude flatness over the 1930 - 1990 MHz frequency range. The Multi-Mix<sup>®</sup> DLL-115R-1.96G is intended for use in power amplifier linearization networks such as feedforward and predistortion.

DLL delay lines are fusion bonded multilayer stripline structures. The DLL series offers an excellent alternative to expensive coaxial and delay filter structures. The fusion bonding process yields a homogeneous monolithic dielectric structure with reliability, ruggedness and electrical and thermal performance that is superior to conventional adhesive bonding techniques.

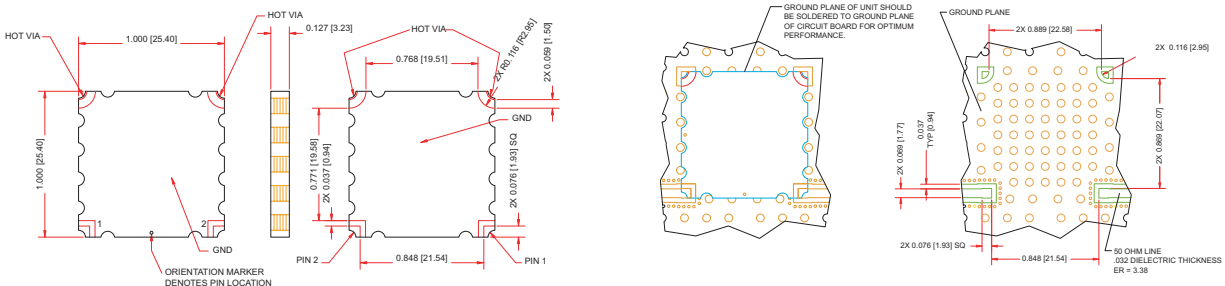
**GENERAL SPECIFICATIONS**

FREQUENCY RANGE MHz	MEAN DELAY* (nS)	DEVIATION FROM LINEAR PHASE (DEGREES MAX)	AMPLITUDE FLATNESS (dB p-p)	
1930 - 1990	11.45 ± 0.2	± 0.5	0.1	
RETURN LOSS (dB MIN)	INSERTION LOSS (dB MAX)	POWER HANDLING (WATTS)	RF INTERFACE	SIZE/OUTLINE (Inches - l,w,h)
20	6.4	15	Surface Mount	1.0 x 1.0 x 0.135

Specifications are based upon unit mounted on printed circuit board with 50 Ohm nominal impedance.

\*Mean delay refers to the group delay of the applied input signal through the network. The specified tolerance relates to unit-unit group delay variation.

**PACKAGE OUTLINE / MOUNTING CONFIGURATION**



**THE MULTI-MIX MICROTECHNOLOGY<sup>®</sup> GROUP IS ISO 9001:2000 REGISTERED**



U.S. Patent 6,099,677 and other Patents Pending.

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