PQ1CG3032FZ/ PQ1CG3032RZ

■ Features

- 1. Maximum switching current:3.5A
- 2. Built-in ON/OFF control function
- 3. Built-in soft start function to suppress overshoot of output voltage in power on sequence or ON/OFF control sequence
- Built-in oscillation circuit (Oscillation frequency:TYP. 150kHz)
- 5. Built-in overheat/overcurrent protection function
- 6. TO-220 package
- 7. Variable output voltage

 (Output variable range: V_{ref} to 35V/–V_{ref} to -30V)

 [Possible to select step-down output/inversing output according to external connection circuit]
- 8. PQ1CG3032FZ:Zigzag forming PQ1CG3032RZ:Self-stand forming

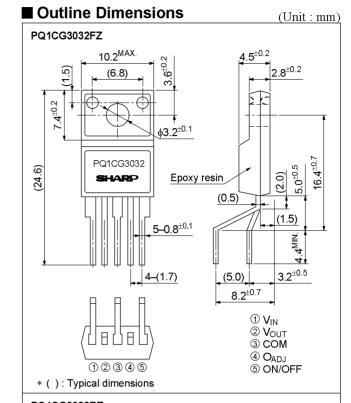
■ Applications

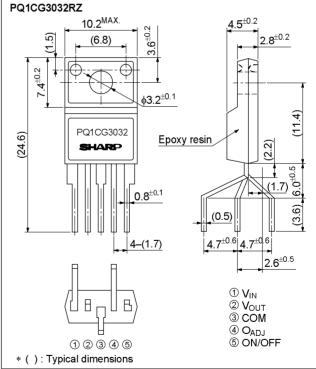
- 1. CTV
- 2. Digital OA equipment
- 3. Facsimiles, printers and other OA equipment
- 4. Personal computers and amusement equipment

■ Absolute Maximum Ratings (Ta=25					
Parameter	Symbol	Rating	Unit		
*1 Input voltage	Vin	40	V		
Output adjustment terminal voltage	V _{ADJ}	7	V		
Dropout voltage	V _{I-O}	41	V		
*2Output-COM voltage	Vout	-1	V		
*3ON/OFF control voltage	Vc	-0.3 to +40	V		
Switching current	Isw	3.5	A		
*4 Dozyan diagination	Pdi	1.4	W		
*4Power dissipation	P _{D2}	14	W		
*5 Junction temperature	Tj	150	°C		
Operating temperature	Topr	-20 to +80	°C		
Storage temperature	Tstg	-40 to +150	°C		
*6 Soldering temperature	Tsol	260	°C		

- * l Voltage between V $_{
 m IN}$ terminal and COM terminal
- *2 Voltage between Vout terminal and COM terminal
- *3 Voltage between ON/OFF control and COM terminal
- *4 PD: With infinite heat sink
- *5 Over heat protection may operate at the condition $T_j\!\!=\!\!125\,^{\circ}\!\mathrm{C}$ to $150\,^{\circ}\!\mathrm{C}$
- *6 For 10s

TO-220 Type Chopper Regulator





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Internet address for Electronic Components Group http://www.sharp.co.jp/ecg/

■ Electrical Characteristics (Unless otherwise specified, condition shall be V _{IN} =12V, Io=0.5A, Vo=5V, ON-OFF terminals is open, Ta=25°C)								
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit		
Output saturation voltage	Vsat	Isw=3A	_	1.4	1.8	V		
Reference voltage	Vref	_	1.235	1.26	1.285	V		
Reference voltage temperature fluctuation	ΔV_{ref}	Tj=0 to 125°C	_	±0.5	_	%		
Load regulation	RegL	Io=0.5 to 3A	_	0.2	1.5	%		
Line regulation	RegI	V _{IN} =8 to 35V	-	1	2.5	%		
Efficiency	η	Io=3A	_	80	_	%		
Oscillation frequency	fo	-	135	150	165	kHz		
Oscillation frequency temperature fluctuation	Δfo	Tj=0 to 125°C	_	±2	_	%		
Overcurrent detecting level	IL	_	3.6	4.7	5.8	Α		
Charge current	Ichg	②,4 terminals is open,5 terminal	_	-10	_	μА		
Threshold input voltage	V _{THL}	Duty ratio=0%,4 terminal=0V,5 terminal	-	1.3	_	V		
	VTHH	Duty ratio=100%, 4 terminals is open, 5 terminal	_	2.3	_	V		
ON threshold voltage	V _{TH(ON)}	4) terminal=0V, 5) terminal	0.7	0.8	0.9	V		
Stand-by current	Isd	V _{IN} =40V, 5 terminal=0V	_	140	400	μA		
Output OFF-state consumption current	Iqs	V _{IN} =40V, 5 terminal=0.9V	_	8	16	mA		

Fig.1 Standard Test Circuit

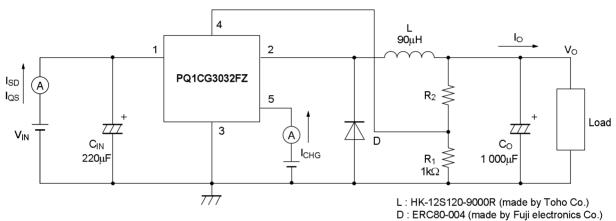


Fig.2 Power Dissipation vs. Ambient Temperature

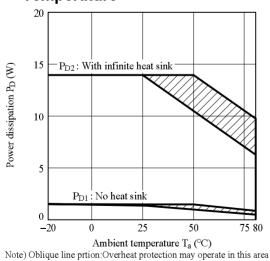


Fig.3 Overcurrent Protection Characteristics (Typical Value)

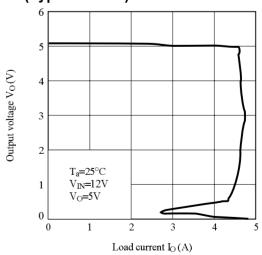


Fig.4 Efficiency vs. Input Voltage

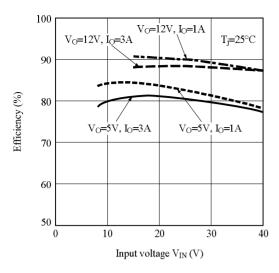


Fig.6 Stand by Current vs. Intput Voltage

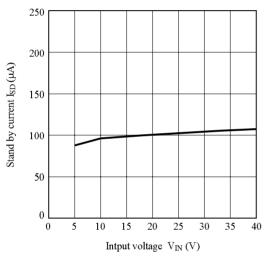


Fig.8 Load Regulation vs. Output Current

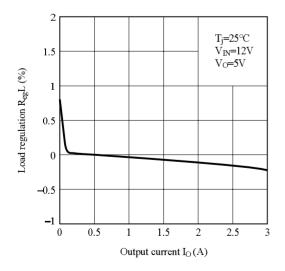


Fig.5 Output Saturation Voltage vs. Switching Current

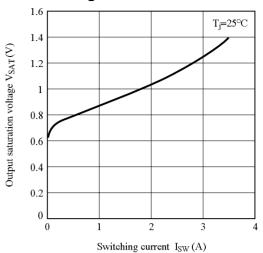


Fig.7 Reference Voltage Fluctuation vs. Junction Temperature

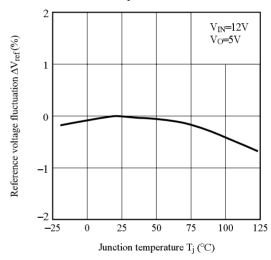


Fig.9 Line Regulation vs. Input Voltage

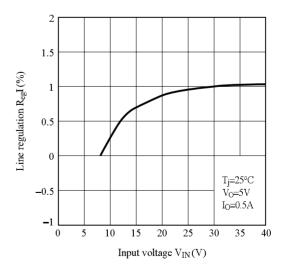


Fig.10 Oscillation Frequency Fluctuation vs. Junction Temperature

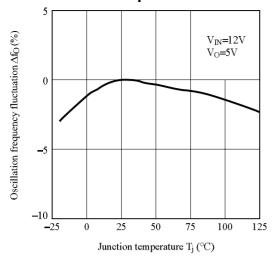


Fig.12 Threshold Voltage vs. Junction Temperature

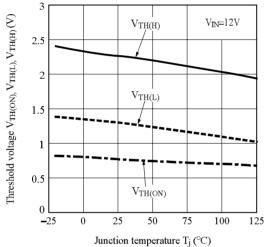


Fig.14 Block Diagram

Fig.11 Overcurrent Detection Level Fluctuation vs. Junction Temperature

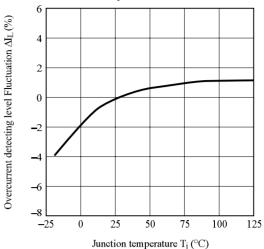
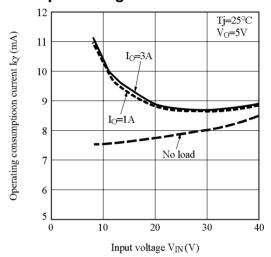


Fig.13 Operating Consumption Current vs. Input Voltage



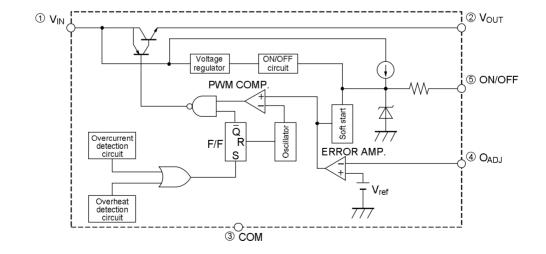


Fig.15 Step Down Type Circuit Diagram

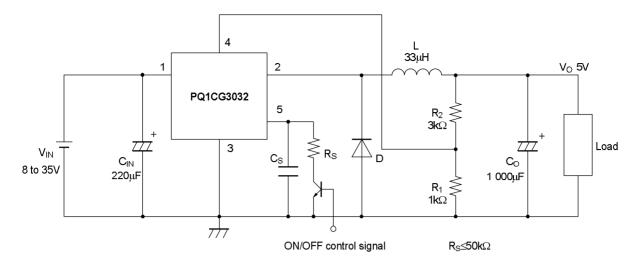
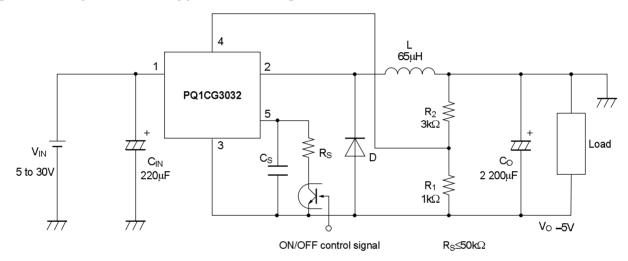
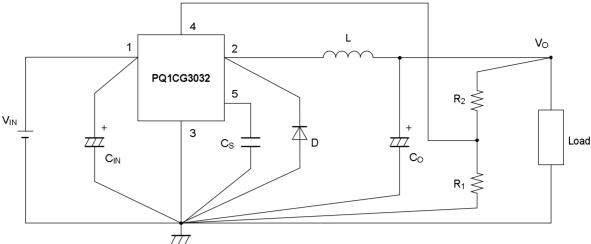


Fig.16 Polarity Inversion Type Circuit Diagram

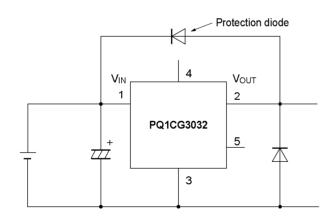


■ Precautions for Use



1. External connection

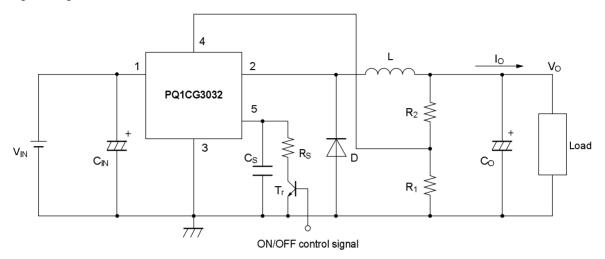
- (1) Wiring condition is very important. Noise associated with wiring inductance may cause problems. For minimizing inductance, it is recommended to design the thick and short pattern (between large current diodos, input/output capacitors, and terminal 1,2.) Single-point grounding (as indicated) should be used for best results.
- (2) High switching speed and low forward voltage type schottky barrier diode should be recommended for the catch-diode D because it affects the efficiency. Please select the diode which the current rating is at least 1.2 times greater than maximum swiching current.
- (3) The output ripple voltage is highly influenced by ESR (Equivalent Series Resistor) of output capacitor, and can be minimized by selecting Low ESR capacitor.
- (4) An inductor should not be operated beyond its maximum rated current so that it may not saturate.
- (5) When voltage that is higher than V_{IN} \bigcirc , is applied to V_{OUT} \bigcirc , there is the case that the device is broken. Especially, in case V_{IN} \bigcirc is shorted to GND in normal condition, there is the case that the device is broken since the charged electric charge in output capacitor (C_O) flows into input side. In such case a schottly barrier diode or a silicon diode shall be recommended to connect as the following circuit.



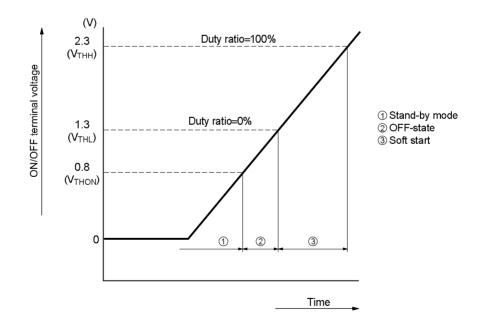
■ ON/OFF Control Terminal

- 1. In the following circuit, when ON/OFF control terminal ⑤ becomes low by switching transistor Tr on, output voltage may be turned OFF and the device becomes stand-by mode. Dissipation current at stand-by mode becomes Max.400μA.
- 2. Soft start
 When capacitor Cs is attached, output pulse gradually expanded and output voltage will start softly.
- 3. ON/OFF control with soft startup

 For ON/OFF control with capacitor C_S, be careful not to destroy a transistor Tr by discharge current from C_S, adding a resistor restricting discharge current of C_S.



■ ON-OFF Terminal Voltage vs. Time



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