TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE (π-MOSV)

2SK2417

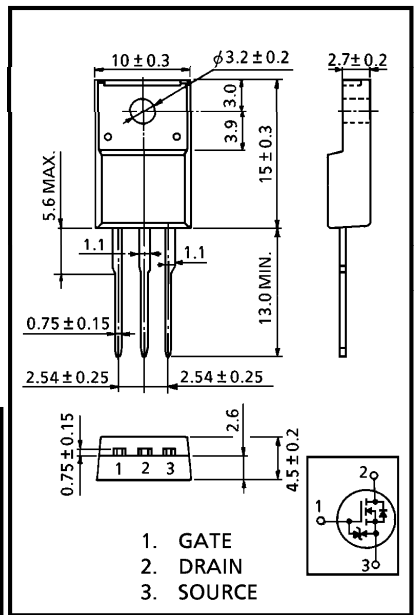
HIGH SPEED, HIGH CURRENT SWITCHING APPLICATIONS

CHOPPER REGULATOR, DC-DC CONVERTER AND MOTOR DRIVE APPLICATIONS

INDUSTRIAL APPLICATIONS

- Low Drain-Source ON Resistance : $R_{DS\,(ON)} = 0.42\Omega$ (Typ.)
- High Forward Transfer Admittance : $|Y_{fs}| = 7.5S$ (Typ.)
- Low Leakage Current
  : $I_{DSS} = 100\mu A$ (Max.) ($V_{DS} = 250V$)
- Enhancement-Mode
  : $V_{th} = 1.5 \sim 3.5V$ ($V_{DS} = 10V$, $I_D = 1mA$)

Unit in mm



JEDEC	—
EIAJ	SC-67
TOSHIBA	2-10R1B

Weight : 1.9g

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain-Source Voltage		V_{DSS}	250	V
Drain-Gate Voltage ($R_{GS} = 20k\Omega$)		V_{DGR}	250	V
Gate-Source Voltage		V_{GSS}	±20	V
Drain Current	DC	I_D	7.5	A
	Pulse	I_{DP}	30	A
Drain Power Dissipation (Tc = 25°C)		P_D	30	W
Single Pulse Avalanche Energy**		E_{AS}	110	mJ
Avalanche Current		I_{AR}	7.5	A
Repetitive Avalanche Energy*		E_{AR}	3	mJ
Channel Temperature		T_{ch}	150	°C
Storage Temperature Range		T_{stg}	−55~150	°C

THERMAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	$R_{th\,(ch\text{-}c)}$	4.16	°C / W
Thermal Resistance, Channel to Ambient	$R_{th\,(ch\text{-}a)}$	62.5	°C / W

Note ;

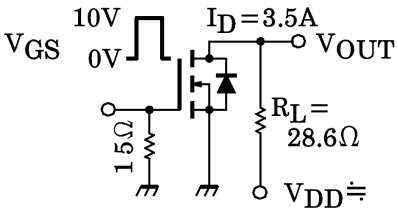
* Repetitive rating ; Pulse Width Limited by Max. junction temperature.

** $V_{DD} = 50V$, Starting $T_{ch} = 25°C$, $L = 3.3mH$, $R_G = 25\Omega$, $I_{AR} = 7.5A$

This transistor is an electrostatic sensitive device. Please handle with caution.

961001EAA2

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

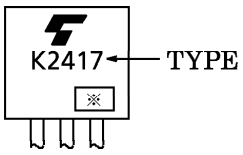
CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage Current		I_{GSS}	$V_{GS}=\pm16V$, $V_{DS}=0V$	—	—	±10	μA
Drain Cut-off Current		I_{DSS}	$V_{DS}=250V$, $V_{GS}=0V$	—	—	100	μA
Drain-Source Breakdown Voltage		$V_{(BR)\,DSS}$	$I_D=10mA$, $V_{GS}=0V$	250	—	—	V
Gate Threshold Voltage		V_{th}	$V_{DS}=10V$, $I_D=1mA$	1.5	—	3.5	V
Drain-Source ON Resistance		$R_{DS\,(ON)}$	$V_{GS}=10V$, $I_D=3.5A$	—	0.42	0.5	Ω
Forward Transfer Admittance		$\|Y_{fs}\|$	$V_{DS}=10V$, $I_D=3.5A$	4	7.5	—	S
Input Capacitance		C_{iss}	$V_{DS}=10V$, $V_{GS}=0V$, f=1MHz	—	700	—	pF
Reverse Transfer Capacitance		C_{rss}		—	80	—	
Output Capacitance		C_{oss}		—	270	—	
Switching Time	Rise Time	t_r		—	10	—	ns
	Turn-on Time	t_{on}		—	20	—	
	Fall Time	t_f		—	10	—	
	Turn-off Time	t_{off}		—	70	—	
Total Gate Charge (Gate-Source Plus Gate-Drain)		Q_g	$V_{DD}\fallingdotseq200V$, $V_{GS}=10V$, $I_D=7.5A$	—	20	—	nC
Gate-Source Charge		Q_{gs}		—	13	—	
Gate-Drain ("Miller") Charge		Q_{gd}		—	7	—	

V_{IN} : t_r, $t_f<5ns$,
Duty ≦ 1%, $t_w=10\mu s$

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	I_{DR}	—	—	—	7.5	A
Pulse Drain Reverse Current	I_{DRP}	—	—	—	30	A
Diode Forward Voltage	V_{DSF}	$I_{DR}=7.5A$, $V_{GS}=0V$	—	—	−2.0	V
Reverse Recovery Time	t_{rr}	$I_{DR}=7.5A$, $V_{GS}=0V$	—	180	—	ns
Reverse Recovery Charge	Q_{rr}	$dI_{DR}/dt=100A/\mu s$	—	1.1	—	μC

MARKING

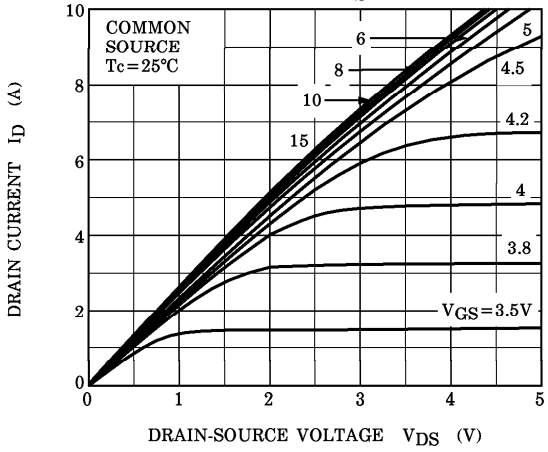


※ Lot Number

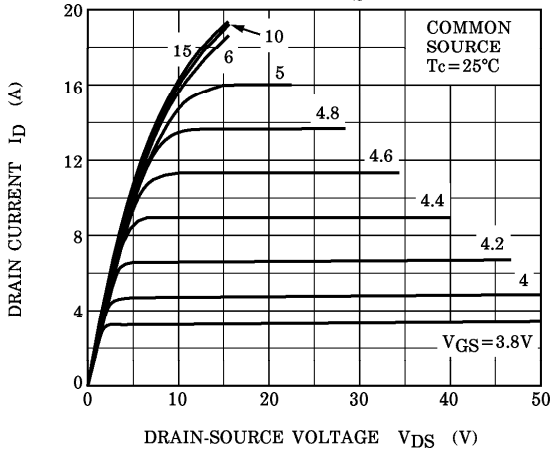
□□ — Month (Starting from Alphabet A)

Year (Last Number of the Christian Era)

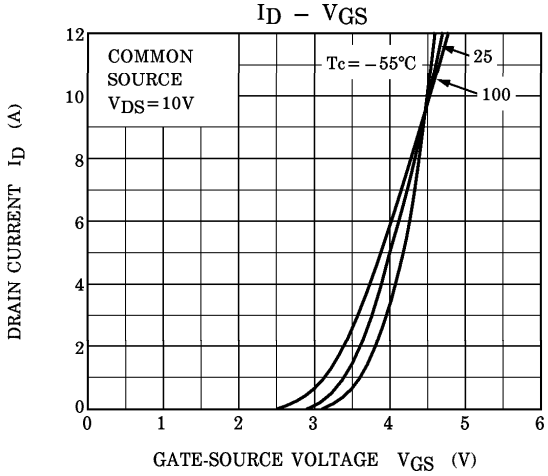
$I_D - V_{DS}$



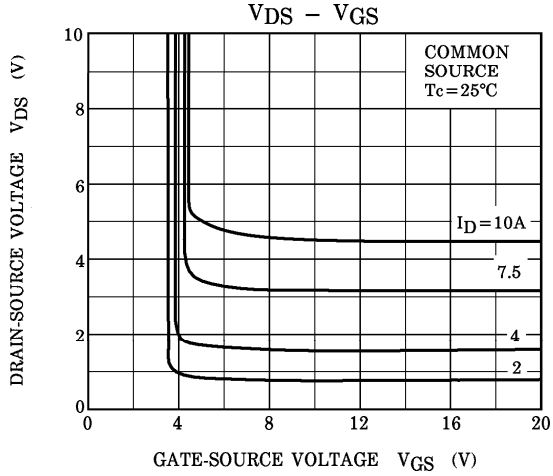
$I_D - V_{DS}$



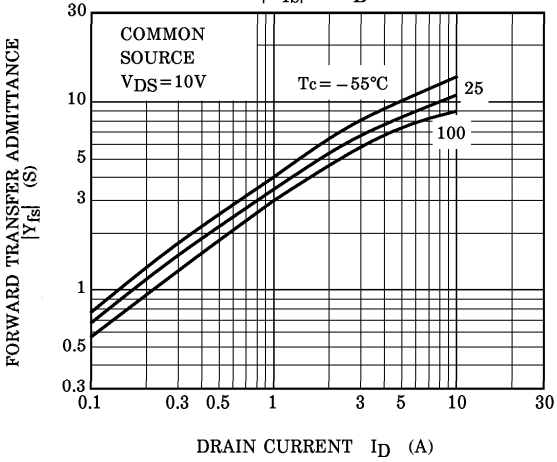
$I_D - V_{GS}$



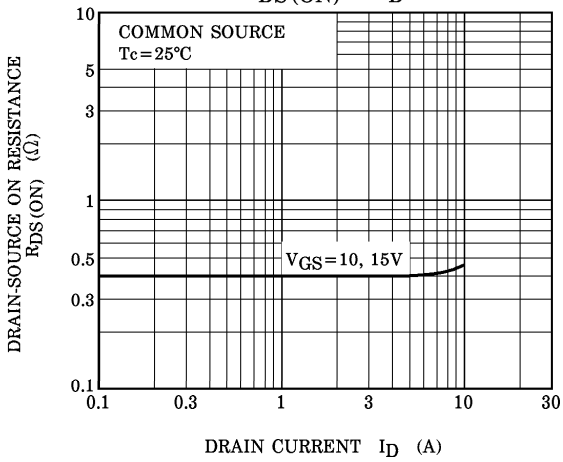
$V_{DS} - V_{GS}$



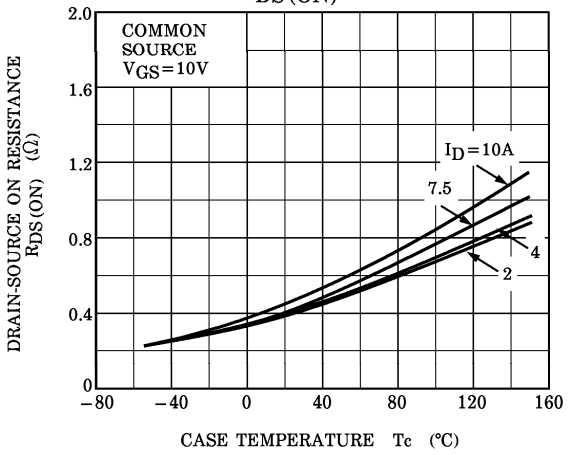
$|Y_{fs}| - I_D$



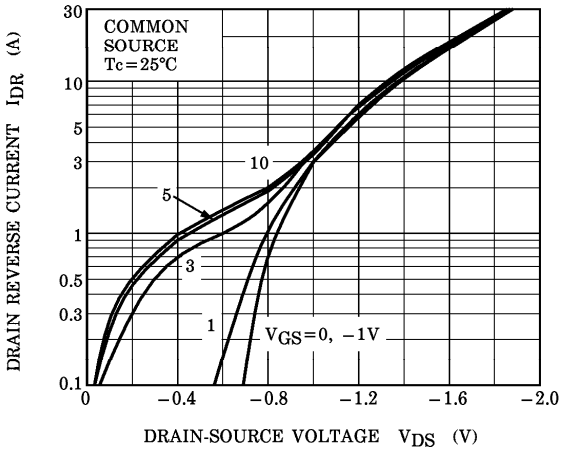
$R_{DS(ON)} - I_D$



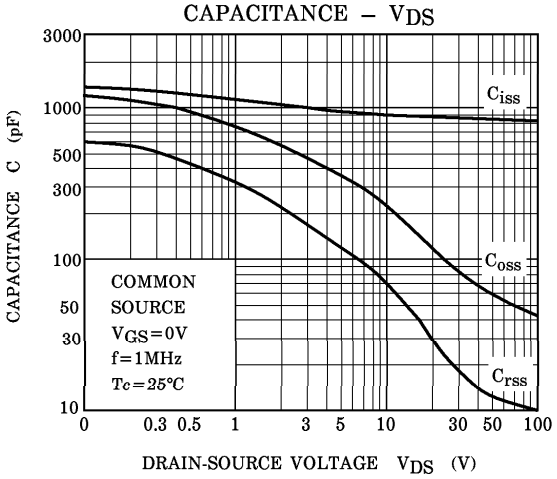
$R_{DS\,(ON)}$ – Tc



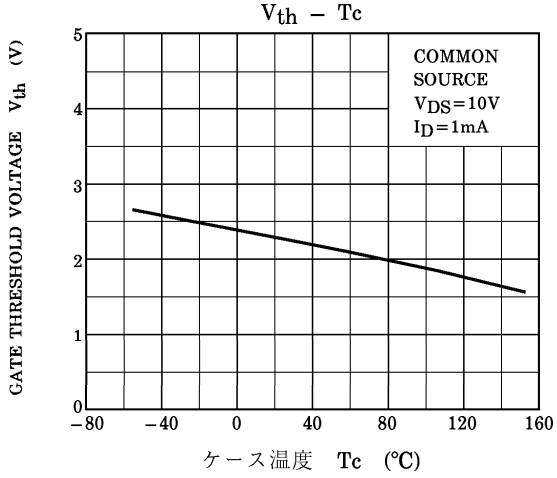
I_{DR} – V_{DS}



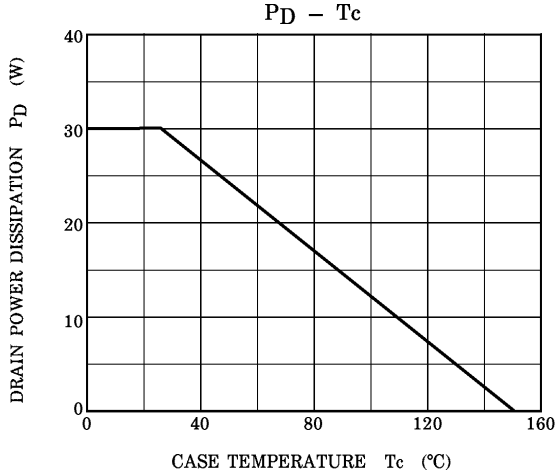
CAPACITANCE – V_{DS}



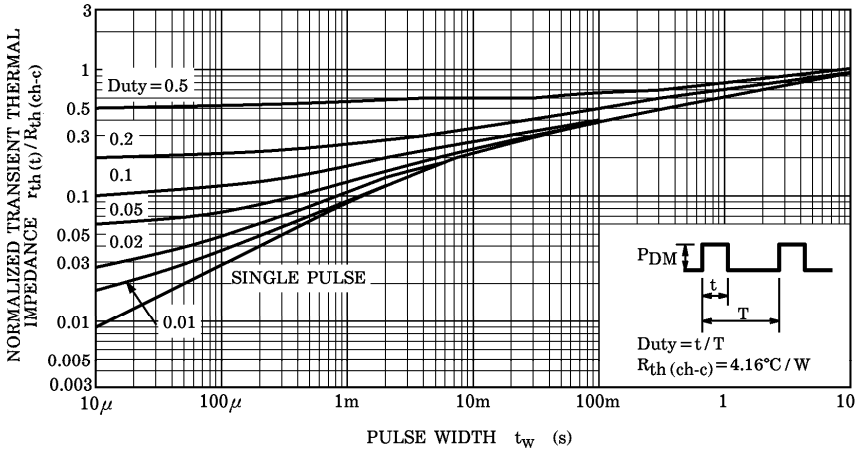
V_{th} – Tc



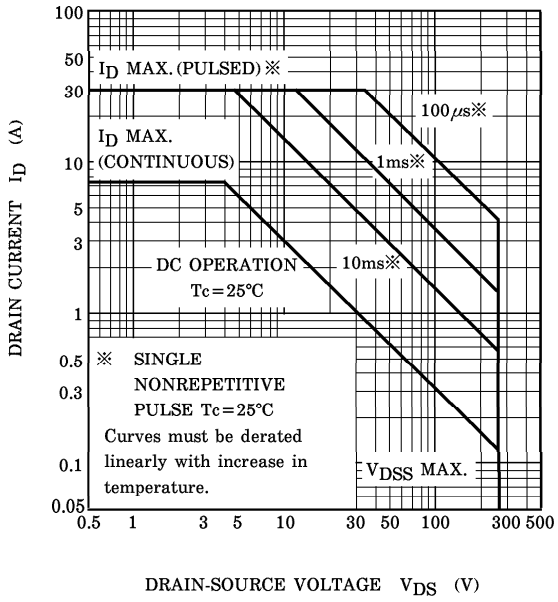
P_D – Tc



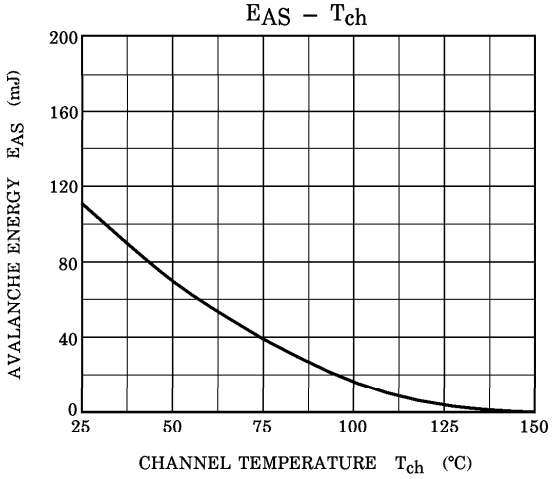
r_{th} – t_w

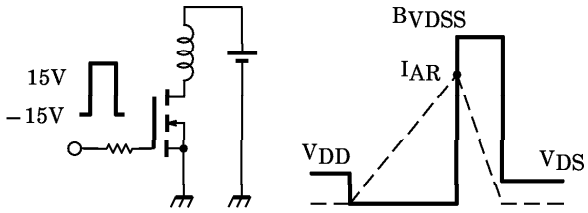


SAFE OPERATING AREA



E_{AS} – T_{ch}





TEST CIRCUIT WAVE FORM

Peak I_{AR} = 7.5A, R_G = 25Ω
V_{DD} = 50V, L = 3.3mH

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$