

RJK0452DPB

Silicon N Channel Power MOS FET Power Switching

R07DS0074EJ0102 (Previous: REJ03G1764-0101) Rev.1.02

Jul 30, 2010

Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting

Low on-resistance

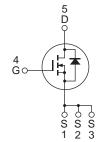
$$R_{DS(on)} = 2.8 \text{ m}\Omega \text{ typ. (at } V_{GS} = 10 \text{ V})$$

- Pb-free
- Halogen-free

Outline

RENESAS Package code: PTZZ0005DA-A (Package name: LFPAK)





1, 2, 3 Source 4 Gate 5 Drain

Application

• Switching Mode Power Supply

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	40	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	45	Α
Drain peak current	I _{D(pulse)} Note1	180	Α
Body-drain diode reverse drain current	I _{DR}	45	Α
Avalanche current	I _{AP} Note 2	22.5	Α
Avalanche energy	E _{AR} Note 2	40.5	mJ
Channel dissipation	Pch Note3	55	W
Channel to Case Thermal Resistance	θch-C	2.27	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at Tch = 25°C, Rg \geq 50 Ω

3. $Tc = 25^{\circ}C$

This product is for the low voltage drive ($\leq 10V$).

If the driving voltage is over 10 V under normal conditions, please use the product for high gate to source cutoff voltage $(V_{GS(off)})$ which characteristics has been improved.

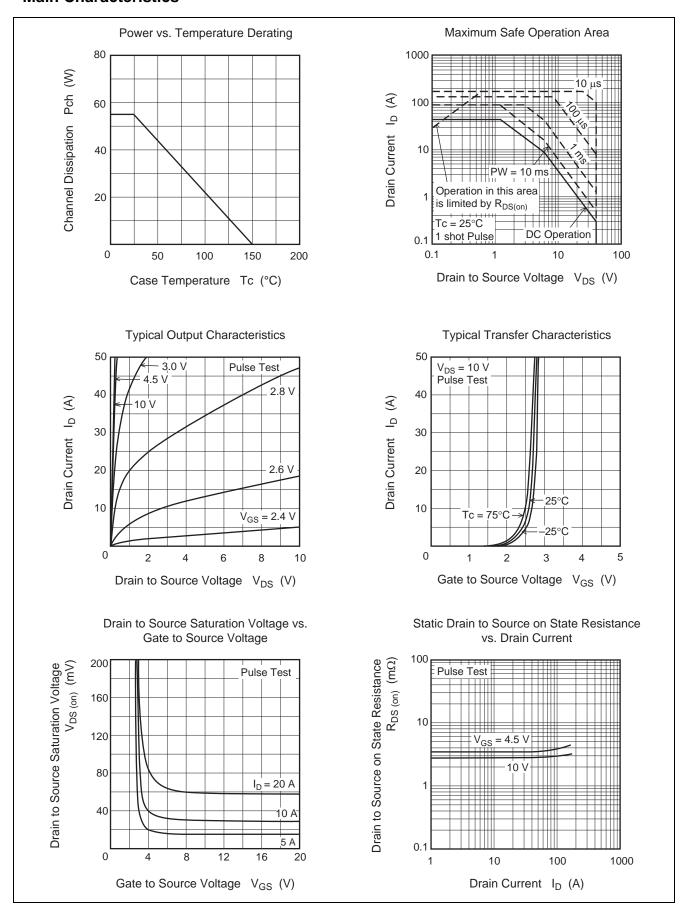
Electrical Characteristics

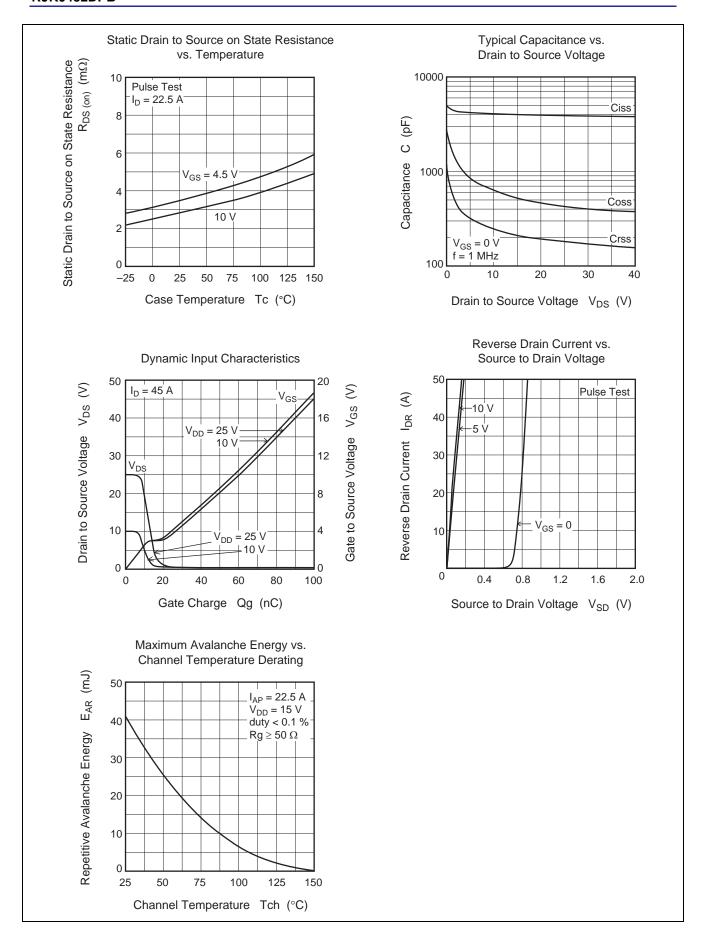
 $(Ta = 25^{\circ}C)$

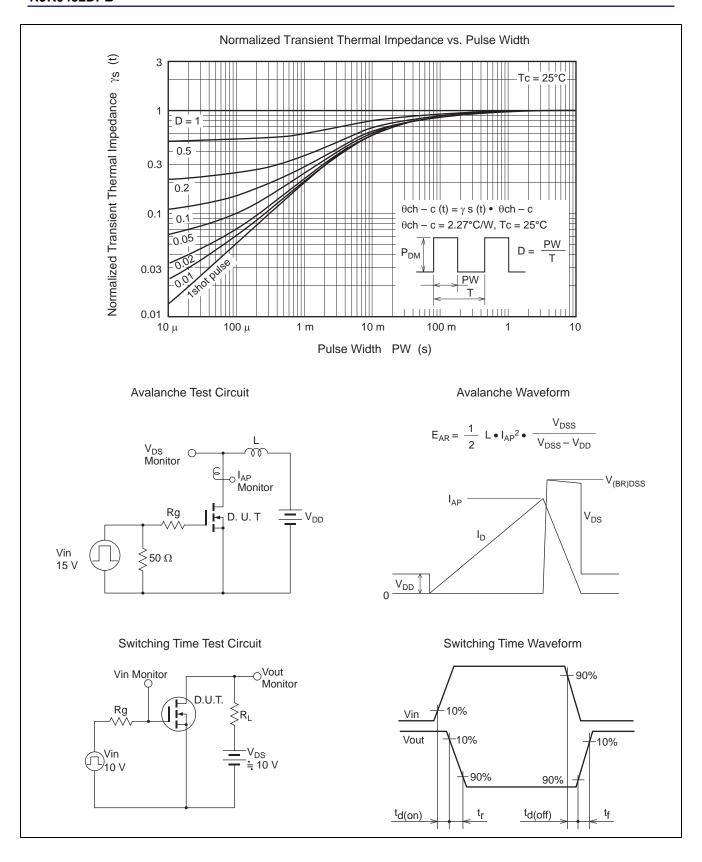
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	40	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$
Gate to source leak current	I_{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V}$
Gate to source cutoff voltage	$V_{GS(off)}$	1.2	_	2.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	2.8	3.5	mΩ	$I_D = 22.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R _{DS(on)}	_	3.5	4.7	mΩ	$I_D = 22.5 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}	_	108	_	S	$I_D = 22.5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	4030	_	pF	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz
Output capacitance	Coss	_	650	_	pF	
Reverse transfer capacitance	Crss	_	270	_	pF	
Gate Resistance	Rg	_	0.4	_	Ω	
Total gate charge	Qg	_	26	_	nC	$V_{DD} = 10 \text{ V}, V_{GS} = 4.5 \text{ V},$ $I_{D} = 45 \text{ A}$
Gate to source charge	Qgs	_	12	_	nC	
Gate to drain charge	Qgd	_	6.6	_	nC	
Turn-on delay time	t _{d(on)}	_	18	_	ns	$V_{GS} = 10 \text{ V}, I_D = 22.5 \text{ A},$
Rise time	t _r	_	6.0	_	ns	$\begin{aligned} V_{DD} &\cong 10 \text{ V}, \text{ R}_{L} = 0.44 \Omega, \\ \text{Rg} &= 4.7 \Omega \end{aligned}$
Turn-off delay time	t _{d(off)}	_	65	_	ns	
Fall time	t _f	_	8.5	_	ns	
Body-drain diode forward voltage	V_{DF}	_	0.83	1.1	V	$I_F = 45 \text{ A}, V_{GS} = 0 \text{ V}^{\text{Note4}}$
Body-drain diode reverse recovery	t _{rr}	_	35	_	ns	I _F = 45 A, V _{GS} = 0 V
time						di _F / dt = 100 A/ μs

Notes: 4. Pulse test

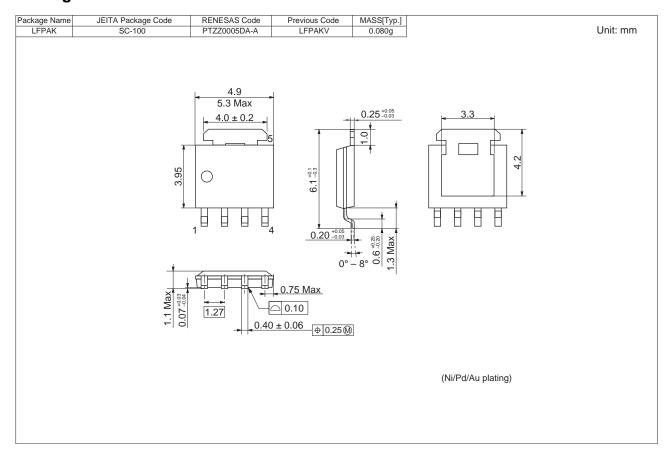
Main Characteristics







Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJK0452DPB-00-J5	2500 pcs	Taping

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