

Single N-channel MOSFET

ELM32428LA-S

General description

ELM32428LA-S uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance.

Features

- $V_{ds}=25V$
- $I_d=75A$
- $R_{ds(on)} < 7m\Omega$ ($V_{gs}=10V$)
- $R_{ds(on)} < 10m\Omega$ ($V_{gs}=4.5V$)

Maximum absolute ratings

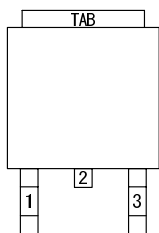
Parameter	Symbol	Limit	Unit	Note	
Gate-source voltage	V_{gs}	± 20	V		
Continuous drain current	I_d	$T_a=25^\circ C$	75	A	
		$T_a=100^\circ C$	50		
Pulsed drain current	I_{dm}	170	A	3	
Avalanche current	I_{ar}	60	A		
Avalanche energy	$L=0.1mH$	E _{as}	140	mJ	
Repetitive avalanche energy	$L=0.05mH$	E _{ar}	5.6	mJ	4
Power dissipation	P_d	$T_a=25^\circ C$	60.00	W	
		$T_a=100^\circ C$	32.75		
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	$^\circ C$		

Thermal characteristics

Parameter		Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-case	Steady-state	$R\theta_{jc}$		2.3	$^\circ C/W$	
Maximum junction-to-ambient	Steady-state	$R\theta_{ja}$		62.5	$^\circ C/W$	
Maximum case-to-heatsink		$R\theta_{cs}$	0.6		$^\circ C/W$	

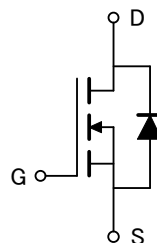
Pin configuration

TO-252-3 (TOP VIEW)



Pin No.	Pin name
1	GATE
2	DRAIN
3	SOURCE

Circuit



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Electrical characteristics

T_a=25°C

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BV _{dss}	I _d =250 μA, V _{gs} =0V	25			V	
Zero gate voltage drain current	I _{dss}	V _{ds} =20V, V _{gs} =0V			25	μA	
		V _{ds} =20V, V _{gs} =0V, T _j =125°C			250		
Gate-body leakage current	I _{gss}	V _{ds} =0V, V _{gs} =±20V			±250	nA	
Gate threshold voltage	V _{gs(th)}	V _{ds} =V _{gs} , I _d =250 μA	1.0	1.5	3.0	V	
On state drain current	I _{d(on)}	V _{gs} =10V, V _{ds} =10V	70			A	1
Static drain-source on-resistance	R _{ds(on)}	V _{gs} =10V, I _d =30A		5.0	7.0	mΩ	1
		V _{gs} =4.5V, I _d =24A		6.6	10.0	mΩ	
Forward transconductance	G _{fs}	V _{ds} =15V, I _d =30A		55		S	1
Diode forward voltage	V _{sd}	I _f =I _s , V _{gs} =0V			1.3	V	1
Max. body-diode continuous current	I _s				75	A	
Pulsed body-diode current	I _{sm}				170	A	3
DYNAMIC PARAMETERS							
Input capacitance	C _{iss}	V _{gs} =0V, V _{ds} =15V, f=1MHz		2700		pF	
Output capacitance	C _{oss}			500	1100	pF	
Reverse transfer capacitance	C _{rss}			200		pF	
SWITCHING PARAMETERS							
Total gate charge	Q _g	V _{gs} =4.5V, V _{ds} =15V, I _d =25A		19.0	25.0	nC	2
Gate-source charge	Q _{gs}			7.0	9.0	nC	2
Gate-drain charge	Q _{gd}			7.5	11.0	nC	2
Turn-on delay time	t _{d(on)}	V _{gs} =10V, V _{ds} =15V, I _d ≈ 30A R _{gen} =2.5 Ω		11.5	17.0	ns	2
Turn-on rise time	t _r			17.0	26.0	ns	2
Turn-off delay time	t _{d(off)}			32.0	48.0	ns	2
Turn-off fall time	t _f			7.5	11.0	ns	2
Body diode reverse recovery time	t _{rr}			37		ns	
Peak reverse recovery current	I _{rm(rec)}	I _f =I _s , dI/dt=100A/μs		200		A	
Body diode reverse recovery charge	Q _{rr}			0.043		μC	

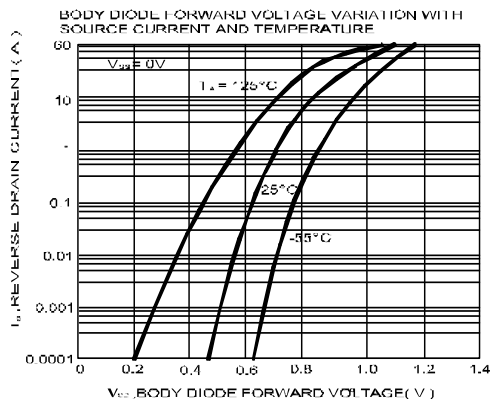
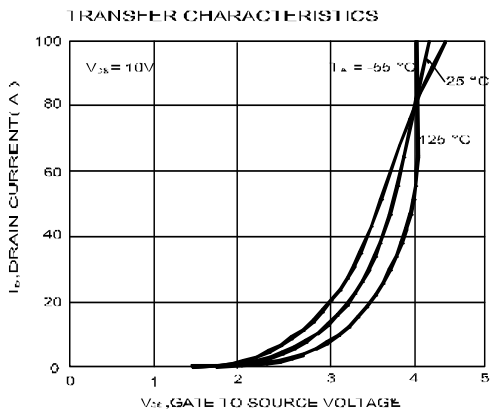
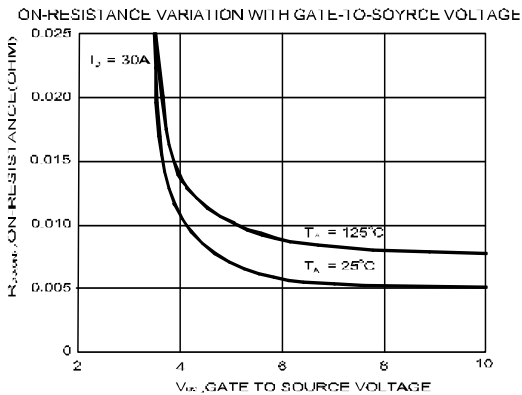
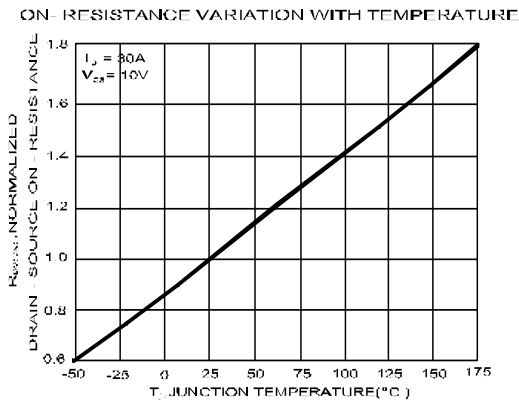
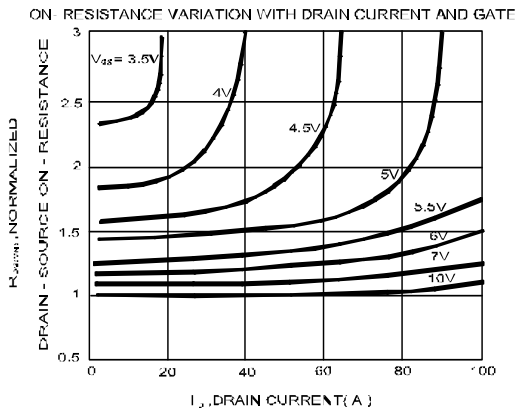
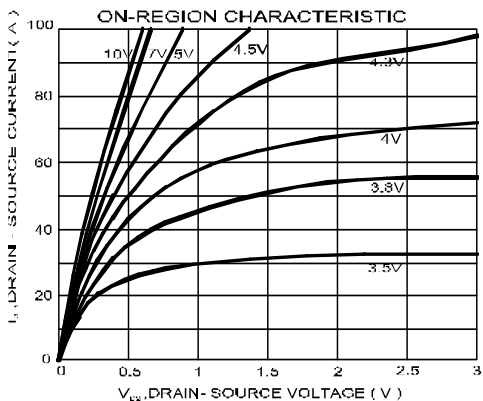
NOTE :

1. Pulse test : Pulsed width ≤ 300 μsec and Duty cycle ≤ 2%.
2. Independent of operating temperature.
3. Pulsed width limited by maximum junction temperature.
4. Duty cycle ≤ 1%.

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Typical electrical and thermal characteristics



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