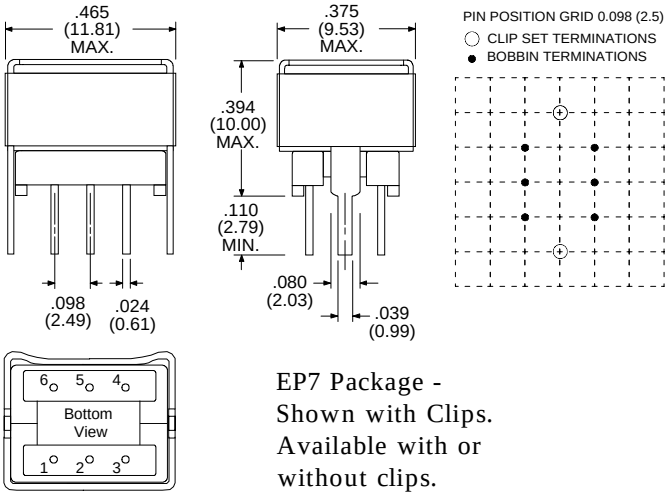
ISDN S-Interface Applications Transformers - EP Style

Designed to meet pulse waveform template of CCITT I.430 when using proper chip pair.

Electrical Specifications [1,2,3] at 25°C

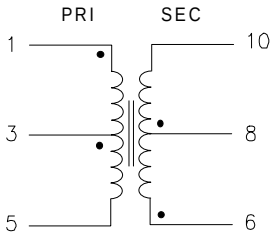
Part Number	Turns Ratio (± 5 %)	OCL min. (mH)	PRI-SEC $C_{W/W}$ max. (pF)	Leakage Ind. max. (µH)	Primary DCR max. (Ω)	Secondary DCR max. (Ω)	Style & Schem.	Primary Pins
T-13700	1CT:2CT	30	50	13	1.1	2.6	EP13 - A	1-5
T-13701	1CT:1CT	30	50	15	1.1	1.3	EP13 - A	1-5
T-13702	1CT:2.5CT	30	50	13	1.1	4.2	EP13 - A	1-5
T-13703	1CT:1.8CT	30	50	13	1.2	2.6	EP13 - A	1-5
T-13704	1CT:2CT	30	50	10	1.0	2.0	EP13 - A	1-5
T-13705	1CT:1CT	30	50	10	2.2	2.2	EP13 - A	1-5
T-13706	1CT:1CT	22	95	5	2.5	2.5	EP10 - B	1-3
T-13707 (1)	2:1CT	22	100	15	5.5	5.0	EP7 - C	1-3

1. Hi-pot (Pri. to Sec.) 2000 V_{RMS} except T-13707. Isolation is 1000 V_{RMS}
2. ET-Product of 10 V-µsec min.
3. OCL Measured at Primary @10KHz & 700mV

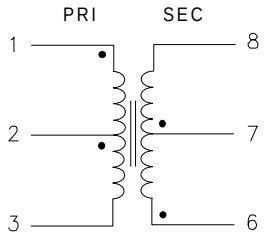


EP7 Package -
Shown with Clips.
Available with or
without clips.

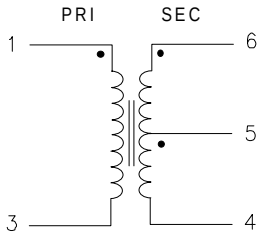
Schematic "A"

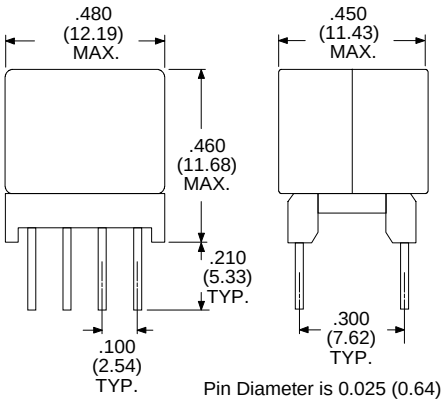


Schematic "B"



Schematic "C"





EP10 Package -
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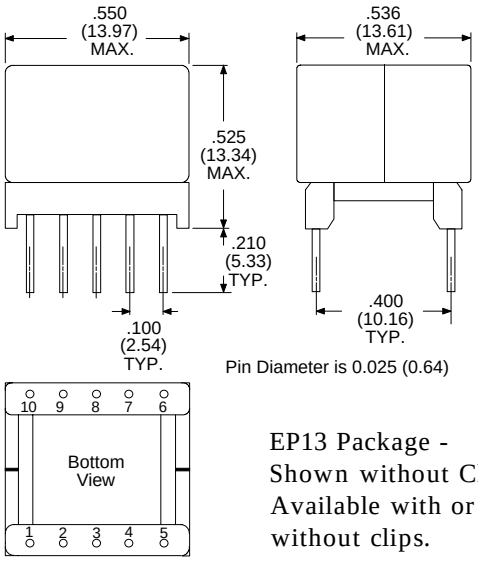
Available with or
without clips.



EP13 Package -
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Available with or
without clips.