

M62294FP

3.3 V, 2.0 V Fixed 2-Output Voltage DC/DC Converter

REJ03D0854-0200 Rev.2.00 Jun 14, 2006

Description

M62294FP is 3.3 V and 2.0 V fixed stable 2-output step-down DC/DC converter.

It is possible to simplify peripheral circuit and to design compact and low cost sets because this device includes peripheral devices in small size 8-pin package.

The IC also has Reset circuit with time delay that monitors power supply ($V_{CC} = 5 \text{ V}$) and one regulator output (Vout1 = 3.3 V; IN1 terminal), therefore an application system is protected system errors.

Especially this is most suitable for application system with microprocessor and ASIC.

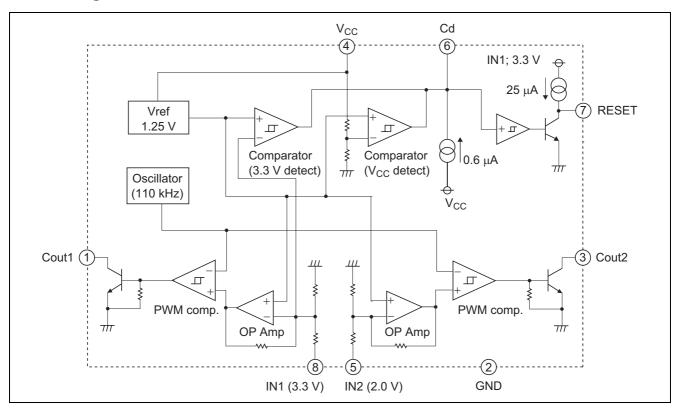
Features

- 3.3 V and 2.0 V step-down converter
- 4 to 15 V wide input supply voltage ($V_{CC} = 5 \text{ V typ.}$)
- · Reset circuit with time delay monitors
- Supply voltage ($V_{CC} = 5 \text{ V}$) and regulator output (3.3 V)
- 110 kHz fixed frequency oscillator without peripheral devices
- 8-pin SOP package

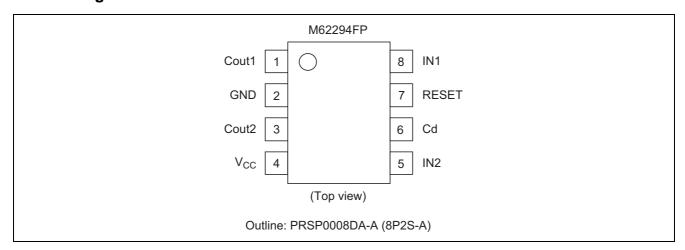
Application

Application system with microprocessor and ASIC

Block Diagram



Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C, unless otherwise noted)

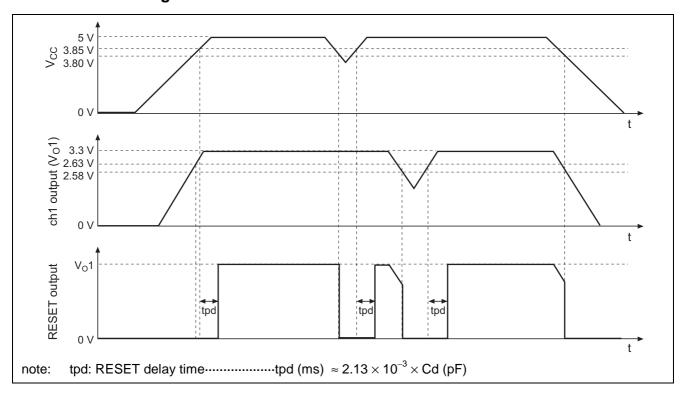
Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V_{CC}	16	V	
Output current (DC/DC converter block)	lo	30	mA	ch1, ch2
Output current (Reset block)	I _{ORESET}	6	mA	
Power dissipation	Pd	440	mW	Ta = 25°C
Thermal derating	Κθ	4.4	mW/°C	Ta > 25°C
Operating temperature	Topr	-20 to +85	°C	
Storage temperature	Tstg	-40 to +125	°C	

Electrical Characteristics

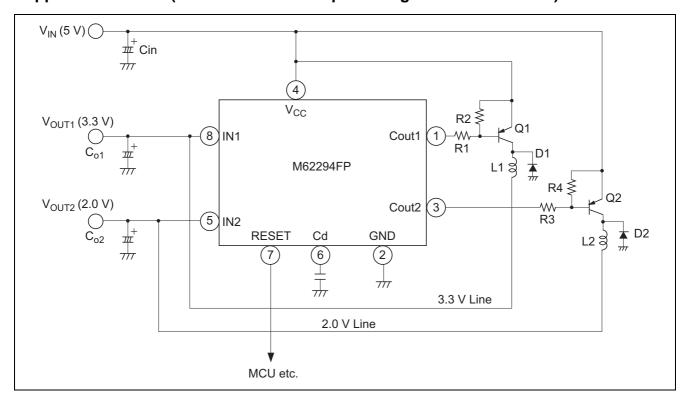
(Ta = 25°C, $V_{CC} = 5$ V, unless otherwise noted)

				Limits			
Block	Item	Symbol	Min	Тур	Max	Unit	Test Condition
All Blocks	Supply voltage	Vcc	4.0	5.0	15	V	
	Supply current	Icc	_	1.5	2.8	mA	Without load
DC/DC con	verter block						
Error Amp.	Output voltage	V _O 1	3.15	3.30	3.45	V	ch1 output
		V ₀ 2	1.90	2.00	2.10		ch2 output
	Line regulation	Vreg-L	_	5	15	mV	V _{CC} = 4 to 12 V
	Input current 1	lin	_	150	450	μΑ	ch1
	Input current 2	lin	_	100	300	μΑ	ch2
Oscillator	Oscillator frequency	fosc	65	110	160	kHz	
Output	Maximum on duty	T _{DUTY}	_	90	_	%	
	Output leakage current	I _{CL}	-1	_	1	V	V _{CC} = 12 V, V _C = 12 V
	Output saturation voltage	Vsat	_	1.2	2.0	V	I _O = 10 mA, Darlington connection
Reset circu	it block	•	•		•		-
Reset	Detecting voltage 1	Vs1	3.6	3.8	4.0	V	V _{CC} = 5 V detection
circuit	Hysteresis voltage 1	ΔVs1	30	50	80	mV	
	Detecting voltage 2	Vs2	2.46	2.58	2.70	V	ch1 output (3.3 V) detection
	Hysteresis voltage 2	ΔVs2	30	50	80	mV	
	Cd output current	I _{PD}	-1.1	-0.6	-0.3	μΑ	
	Delay time	tpd	5	10	20	ms	Cd = 4700 pF
	RESET output current	loc	-40	-25	-17	μΑ	$V_{CC} = 5 \text{ V}, V_O = 1/2 \times V_{CC}$
	RESET low voltage	V _{OL}	_	_	0.2 V ₀ 1	V	I _{ORESET} = 4 mA
	RESET high voltage	V _{OH}	0.8 V ₀ 1	_	_	V	

Reset Block Timing Chart



Application Circuit (3.3 V and 2.0 V 2-output Voltage DC/DC Converter)



The Expression of Circuit Constants

Constants	Expressions		
T _{ON} T _{OFF}	$\frac{V_O + V_F}{V_IN - V_CE (sat) - V_O}$		
(T _{ON} + T _{OFF}) _{MAX}	$\frac{1}{f_{OSC}} f_{OSC}: 110 \text{ kHz} (V_{CC} = 5 \text{ V})$		
T _{OFF (MIN)}	$(T_{ON} + T_{OFF}) / (1 + \frac{T_{ON}}{T_{OFF}})$		
T _{ON (MAX)}	$\frac{1}{f_{OSC}} - T_{OFF}$		
L (MIN)	$\frac{(V_{IN} - V_{CE (sat)} - V_O) \times Ton (MAX)}{\Delta I_O}$		
lpk	$I_{O} + \frac{1}{2} \Delta I_{O}$		

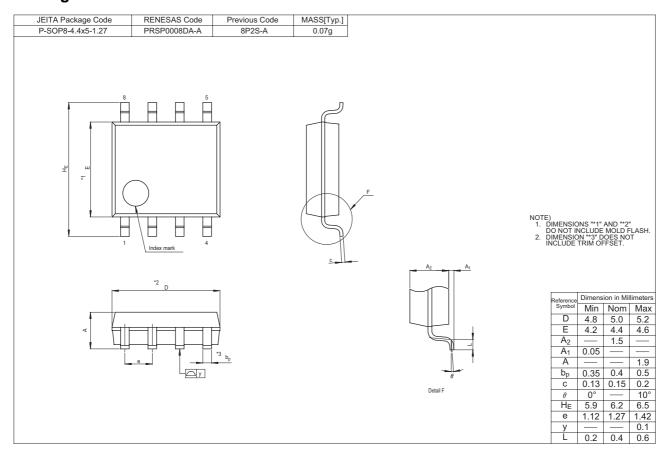
Note: V_F: Forward voltage drop of an external diode.

Vsat: Output saturation voltage of an external switching transistor.

 ΔI_{O} : Set to 1/3 to 1/5 of maximum output current.

Choose an external transistor, diode and inductor with peak current rating approximately greater than "lpk".

Package Dimensions



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