

Futures : Integrated in 3Phase Diode Bridge, Thyristor Switch, Inverter, Brake, and Snubber  
For 7.5kw 200V Inverter

Approximate Weight : 400g

**MAXMUM RATINGS (Tc=25°C)**

Item		Symbol	Rated Value	Unit	
3 Phase Rectification Diode	Repetitive Peak Reverse Voltage	$V_{RRM}$	800	V	
	Non-Repetitive Peak Reverse Voltage	$V_{RSM}$	900		
	Average Rectified Out -Put Current	$I_{O(AV)}$	75	A	
	Surge Forward Current	$I_{FSM}$	800		
	I Squared t	$I^2t$	3200	A <sup>2</sup> s	
	Critical Rate of Fall of Forward Current	-di/dt	160(@ :I <sub>FM</sub> =25A, V <sub>R</sub> =500V)	A/μs	
Switch Thyristor	Repetitive Peak Off-State Voltage	$V_{DRM}$	800	V	
	Non-Repetitive Peak Off-State Voltage	$V_{RSM}$	900		
	Average Rectified Out-Put Current	$I_{O(AV)}$	75	A	
	Surge Forward Current	$I_{TSM}$	800		
	I Squared t	$I^2t$	3200	A <sup>2</sup> s	
	Critical Rate Of Rise Of Turn-On Current	di/dt	100	A/μs	
	Peak Gate Power	$P_{GM}$	5	W	
	Average Gate Power	$P_{GM(AV)}$	1		
	Peak Gate Current	$I_{GM}$	2	A	
	Peak Gate Voltage	$V_{GM}$	10	V	
	Peak Gate Reverse Voltage	$V_{RGM}$	5		
	Inverter IGBT	Collector-Emitter Voltage	$V_{CES}$	600	V
Gate-Emitter Voltage		$V_{GES}$	+/- 20V		
Collector Current		DC	$I_C$	75	A
		1ms	$I_{CP}$	150	
Forward Current		DC	$I_F$	75	
		1ms	$I_{FM}$	150	
Collector Power Dissipation	$P_C$	300	W		
Brake IGBT	Collector-Emitter Voltage	$V_{CES}$	600	V	
	Gate Emitter Voltage	$V_{GES}$	+/- 20V		
	Collector Current	DC	$I_C$	30	A
		1ms	$I_{CP}$	60	
	Collector Power Dissipation	$P_C$	178	W	
Snubber Diode	Repetitive Peak Reverse Voltage	$V_{RRM}$	600	V	
	Forward Current, DC	$I_F$	15	A	
	Surge Forward Current	$I_{FSM}$	150		
Operating Junction Temperature Range	$T_{jw}$	-40 to +150°C (notes: +125 °C > Can not be biased.)		°C	
Storage Temperature Range	$T_{stg}$	-40 to +125°C			
Isolation Voltage(Terminal to Base)	Viso	2500(@AC, 1minute), 3000(@AC, 1second)		V	
Isolation Resistance(Terminal to Base, @DC=500V)	Riso	500		M.ohm	
Mounting Torque(Module Base to Heatsink)	Ftor	(M4), 1.4		N·m	

**ELECTRICAL CHARACTERISTICS (Tc=25°C Unless otherwise noted)**

Characteristic		Symbol	Test Condition	Min.	Typ.	Max.	Unit	
3 Phase Rectification Diode	Peak Reverse Current *1	$I_R$	$T_J=150^\circ\text{C}, V_{RM}=V_{RRM}$	-	-	10	mA	
	Peak Reverse Voltage *1	$V_F$	$I_F=80\text{A}$	-	-	1.40	μA	
Switch Thyristor	Peak OFF-State Current	$I_{DM}$	$T_J=125^\circ\text{C}, V_{DM}=V_{DRM}$	-	-	50	mA	
	Peak Reverse Current	$I_{RM}$	$T_J=125^\circ\text{C}, V_{RM}=V_{RRM}$	-	-	50		
	Peak On-State Voltage	$V_{TM}$	$I_T=80\text{A}$	-	-	1.40	V	
	Gate Current to Trigger	$I_{GT}$	$V_D=6\text{V}$ $I_T=1\text{A}$	$T_J=-40^\circ\text{C}$	-	-	200	mA
				$T_J=25^\circ\text{C}$	-	-	100	
				$T_J=125^\circ\text{C}$	-	-	50	
	Gate Voltage to Trigger	$V_{GT}$	$V_D=6\text{V}$ $I_T=1\text{A}$	$T_J=-40^\circ\text{C}$	-	-	40	V
				$T_J=25^\circ\text{C}$	-	-	25	
$T_J=125^\circ\text{C}$				-	-	20		
Gate Voltage to Non-Trigger	$V_{GD}$	$T_J=125^\circ\text{C}, V_D=2/3V_{DRM}$		0.25	-	-	V	
Critical Rate Of Rise Of Off-State Voltage	dv/dt			500	-	-	V/μs	

Switch Thyristor	Turn-Off Time	tq	T <sub>J</sub> =125°C, V <sub>D</sub> =2/3V <sub>DRM</sub> V <sub>RM</sub> =100V, dv/dt=20V/μs -di/dt=20A/μs	-	100	-	μs	
	Turn-On Time	tgt	T <sub>J</sub> =25°C, V <sub>D</sub> =2/3V <sub>DRM</sub>	-	6	-		
	Delay Time	td	I <sub>C</sub> =200mA	-	2	-		
	Rise Time	tr	-dic/dt=0.2A/μs	-	4	-		
	Latching Current	I <sub>L</sub>		-	100	-	mA	
	Holding Current	I <sub>H</sub>		-	80	-		
Inverter IGBT	Collector-Emitter Out-Off Current	I <sub>CEs</sub>	V <sub>CE</sub> =600V, V <sub>GE</sub> =0V	-	-	1.0	mA	
	Gate-Emitter Leakage Current	I <sub>GES</sub>	V <sub>GE</sub> =+/- 20V, V <sub>CE</sub> =0V	-	-	0.5	μA	
	Collector-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =75A, V <sub>GE</sub> =15V	-	2.1	2.6	V	
	Gate-Emitter Threshold Voltage	V <sub>GE(th)</sub>	V <sub>CE</sub> =5V, I <sub>C</sub> =75mA	4.0	-	8.0	V	
	Input Capacitance	C <sub>ies</sub>	V <sub>CE</sub> =10V, V <sub>GE</sub> =0V, f=1MHz	-	7500	-	pF	
	Switching Time	Rise Time	t <sub>r</sub>	V <sub>CC</sub> = 300V	-	0.15	0.30	μs
		Turn-On Time	t <sub>on</sub>	R <sub>L</sub> = 2 ohm	-	0.25	0.40	
		Fall Time	t <sub>f</sub>	R <sub>G</sub> = 10 ohm	-	0.20	0.35	
		Turn-Off Time	t <sub>off</sub>	V <sub>GE</sub> = +/- 15V	-	0.45	0.7	
	Peak Forward Voltage	V <sub>F</sub>	I <sub>F</sub> =75A	-	1.9	2.4	V	
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =75A, V <sub>CE</sub> =-10V, di/dt=75A/μs	-	0.15	0.25	μs		
Brake IGBT	Collector-Emitter Cut-Off Current	I <sub>CEs</sub>	V <sub>CE</sub> =600V, V <sub>GE</sub> =0V	-	-	1.0	mA	
	Gate-Emitter Leakage Current	I <sub>GES</sub>	V <sub>GE</sub> =+/- 20V, V <sub>CE</sub> =0V	-	-	0.5	μA	
	Collector-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =30A, V <sub>GE</sub> =15V	-	2.0	2.5	V	
	Gate-Emitter Threshold Voltage	V <sub>GE(th)</sub>	V <sub>CE</sub> =5V, I <sub>C</sub> =30mA	4.0	-	8.0	V	
	Input Capacitance	C <sub>ies</sub>	V <sub>CE</sub> =10V, V <sub>GE</sub> =0V, f=1MHz	-	4000	-	pF	
	Switching Time	Rise Time	t <sub>r</sub>	V <sub>CC</sub> = 300V	-	0.15	0.3	μs
		Turn-on Time	t <sub>on</sub>	R <sub>L</sub> = 10 ohm	-	0.25	0.4	
Fall Time		t <sub>f</sub>	R <sub>G</sub> = 15 ohm	-	0.20	0.35		
Turn-off Time		t <sub>off</sub>	V <sub>GE</sub> = +/- 15V	-	0.45	0.7		
Snubber Diode	Peak Forward Voltage	V <sub>F</sub>	I <sub>F</sub> =15A	-	-	2.5	V	
	Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =15A, di/dt=50A/μs	-	-	0.3	μs	

\*1: per 1arm

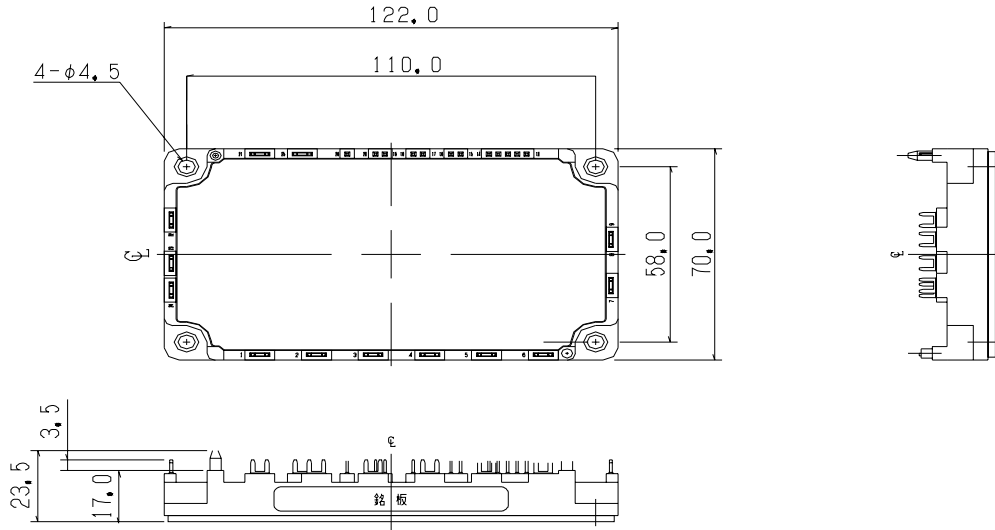
**ELECTRICAL CHARACTERISTICS** (T<sub>c</sub>=25°C Unless otherwise noted)

Thermister	Resistance	25°C	-	5.00	-	k. ohm
		75°C	-	0.97	-	
		125°C	-	0.27	-	
	B-Value	25°C/50°C	-	3375	-	K
		25°C/85°C	-	3420	-	
Thermal Time Constant		-	10	-	s	

**THERMAL CHARACTERISTICS**

Characteristic		Test Condition	Min.	Typ.	Max.	Unit
Thermal Impedance	R <sub>th(j-c)</sub> Junction to Case	3 Phase Rectification Diode	-	-	0.75	°C/W
		Switch Thyristor	-	-	0.55	
		Inverter IGBT	-	-	0.42	
		Inverter Free Wheeling Diode	-	-	0.90	
		Brake IGBT	-	-	0.70	

PVD75-6 OUTLINE DRAWING  
(Dimensions in mm)



CIRCUIT

