


**Pin Definition:**

1. Source
2. Source
3. Source
4. Gate
- 5, 6, 7, 8. Drain

**PRODUCT SUMMARY**

$V_{DS}$ (V)	$R_{DS(on)}$ (m $\Omega$ )	$I_D$ (A)
30	10 @ $V_{GS} = 10V$	13
	13.5 @ $V_{GS} = 4.5V$	11

**Features**

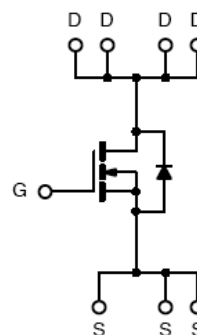
- Advance Trench Process Technology
- High Density Cell Design for Ultra Low On-resistance

**Application**

- DC-DC Conversion
- Battery Switch

**Ordering Information**

Part No.	Package	Packing
TSM4886CS RL	SOP-8	2.5Kpcs / 13" Reel

**Block Diagram**


N-Channel MOSFET

**Absolute Maximum Rating** ( $T_a = 25^\circ\text{C}$  unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	20	V
Continuous Drain Current	$I_D$	13	A
Pulsed Drain Current	$I_{DM}$	50	A
Continuous Source Current (Diode Conduction) <sup>a,b</sup>	$I_S$	2.6	A
Maximum Power Dissipation	$P_D$	$T_a = 25^\circ\text{C}$	2.95
		$T_a = 75^\circ\text{C}$	1.9
Operating Junction Temperature	$T_J$	+150	$^\circ\text{C}$
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	- 55 to +150	$^\circ\text{C}$

**Thermal Performance**

Parameter	Symbol	Limit	Unit
Junction to Case Thermal Resistance	$R_{\theta JC}$	25	$^\circ\text{C/W}$
Junction to Ambient Thermal Resistance (PCB mounted)	$R_{\theta JA}$	50	$^\circ\text{C/W}$

**Notes:**

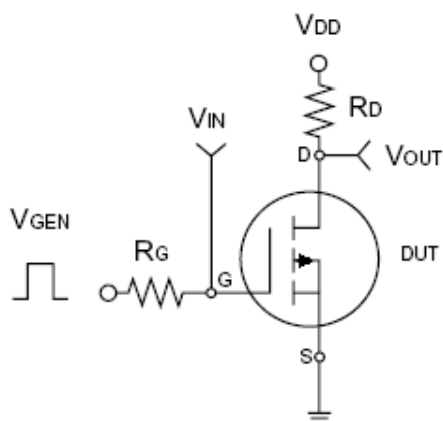
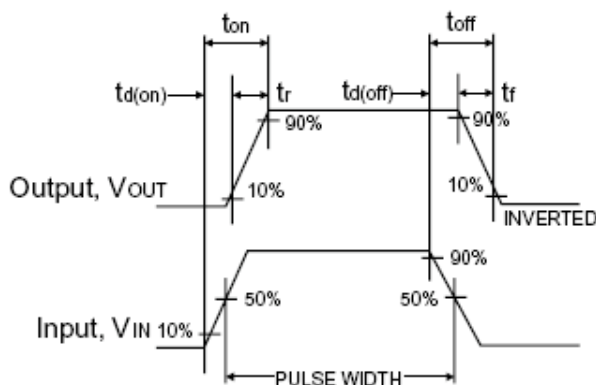
- a. Pulse width limited by the Maximum junction temperature
- b. Surface Mounted on FR4 Board,  $t \leq 10$  sec.

**Electrical Specifications** (Ta = 25°C unless otherwise noted)

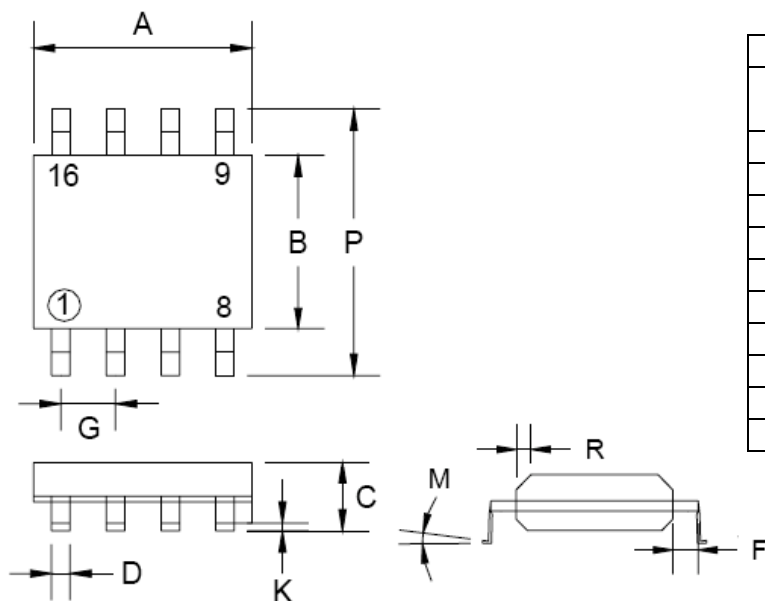
Parameter	Conditions	Symbol	Min	Typ	Max	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	$BV_{DSS}$	30	--	--	V
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	$V_{GS(TH)}$	1	1.8	3	V
Gate Body Leakage	$V_{GS} = \pm 20V, V_{DS} = 0V$	$I_{GSS}$	--	--	$\pm 100$	nA
Zero Gate Voltage Drain Current	$V_{DS} = 24V, V_{GS} = 0V$	$I_{DSS}$	--	--	1.0	$\mu A$
On-State Drain Current <sup>a</sup>	$V_{DS} \geq 5V, V_{GS} = 10V$	$I_{D(ON)}$	40	--	--	A
Drain-Source On-State Resistance <sup>a</sup>	$V_{GS} = 10V, I_D = 13A$	$R_{DS(ON)}$	--	7.8	10	m $\Omega$
	$V_{GS} = 4.5V, I_D = 11A$		--	10.5	13.5	
Forward Transconductance <sup>a</sup>	$V_{DS} = 15V, I_D = 13A$	$g_{fs}$	--	38	--	S
Diode Forward Voltage	$I_S = 2.6A, V_{GS} = 0V$	$V_{SD}$	--	0.85	1.3	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	$V_{DS} = 15V, I_D = 13A, V_{GS} = 5V$	$Q_g$	--	26	--	nC
Gate-Source Charge		$Q_{gs}$	--	6	--	
Gate-Drain Charge		$Q_{gd}$	--	5	--	
Input Capacitance	$V_{DS} = 15V, V_{GS} = 0V, f = 1.0MHz$	$C_{iss}$	--	2134	--	pF
Output Capacitance		$C_{oss}$	--	343	--	
Reverse Transfer Capacitance		$C_{rss}$	--	134	--	
<b>Switching<sup>c</sup></b>						
Turn-On Delay Time	$V_{DD} = 15V, R_L = 1.0\Omega, I_D = 1A, V_{GEN} = 10V, R_G = 3\Omega$	$t_{d(on)}$	--	17	--	nS
Turn-On Rise Time		$t_r$	--	3.5	--	
Turn-Off Delay Time		$t_{d(off)}$	--	40	--	
Turn-Off Fall Time		$t_f$	--	6	--	

**Notes:**

- pulse test: PW  $\leq 300\mu s$ , duty cycle  $\leq 2\%$
- For DESIGN AID ONLY, not subject to production testing.
- Switching time is essentially independent of operating temperature.


**Switching Test Circuit**

**Switchin Waveforms**

**SOP-8 Mechanical Drawing**



SOP-8 DIMENSION				
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX.
A	4.80	5.00	0.189	0.196
B	3.80	4.00	0.150	0.157
C	1.35	1.75	0.054	0.068
D	0.35	0.49	0.014	0.019
F	0.40	1.25	0.016	0.049
G	1.27BSC		0.05BSC	
K	0.10	0.25	0.004	0.009
M	0°	7°	0°	7°
P	5.80	6.20	0.229	0.244
R	0.25	0.50	0.010	0.019

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