

Preliminary W541E260



4-BIT FLASH EPROM MICROCONTROLLER

GENERAL DESCRIPTION

The W541E260 is fully compatible with W741E260 in terms of pin assignment and IC function. It is a high-performance 4-bit microcontroller (μC) that provides an LCD driver and the flash EEPROM for the program memory. The device contains a 4-bit ALU, two 8-bit timers, two dividers (for two oscillators) in dual-clock operation, a 32×4 LCD driver, and five 4-bit I/O ports (including 1 output port for LED driving). There are also five interrupt sources and a 8-level subroutine nesting for interrupt applications. The W541E260 operates on very low current and has three power reduction modes, hold mode and stop mode in single-clock operation and the dual-clock slow operation, which help to minimize power dissipation.

This chip is available for W541C250 and W541C260 bodies, which can be selected by option code. The W541E260 is suitable for end product manufacturer engineering testing and earlier samples before mass production.

FEATURES

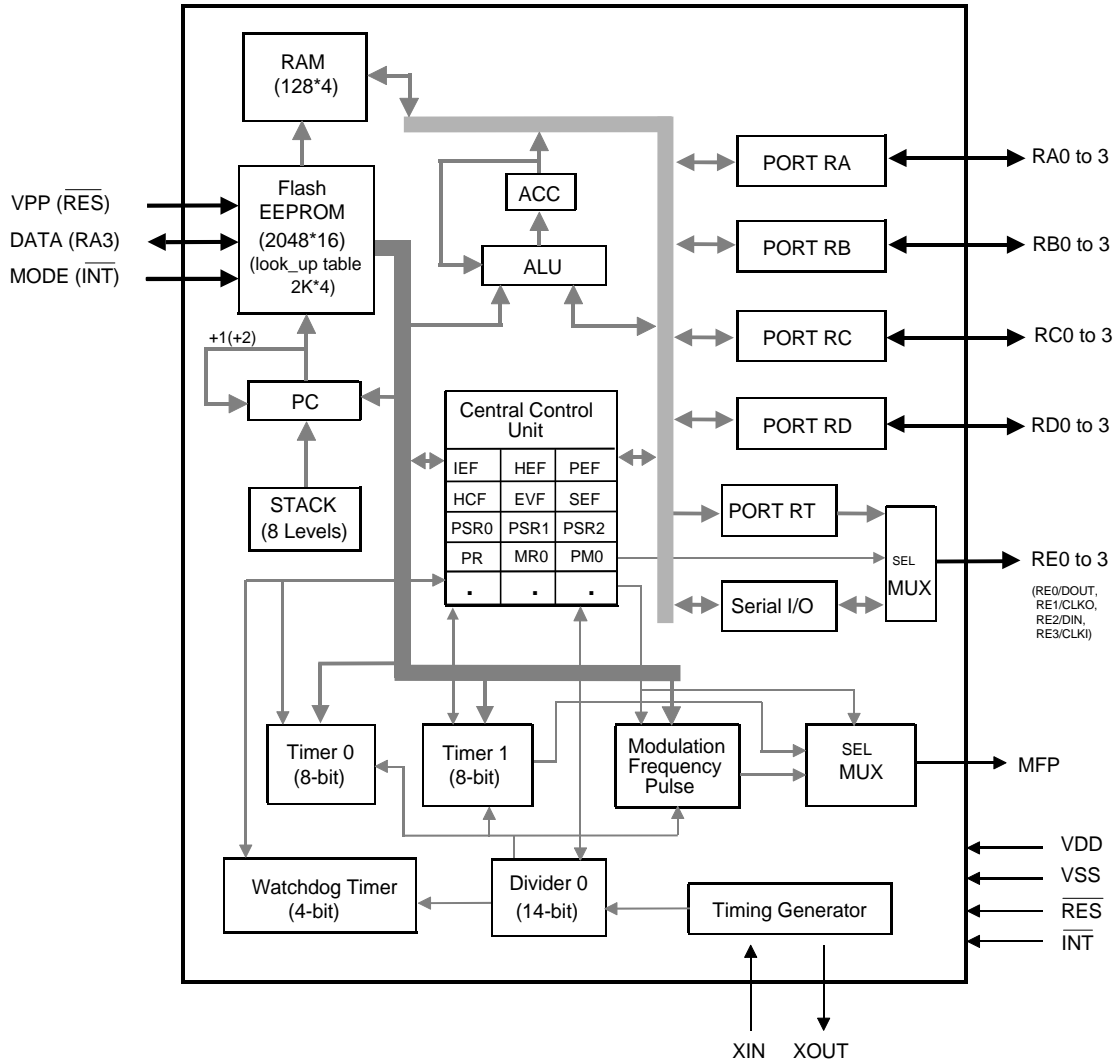
- Operating voltage: 2.4V to 5.5V (LCD drive voltage: 3.0V, or 4.5V)
- Crystal/Ceramic oscillator: up to 4 MHz
- RC oscillator: up to 4 MHz
- Dual-clock operation is selected by code option
- Main oscillator
 - Crystal or RC oscillation circuit can be selected by code option
 - In crystal mode, high-frequency (400 KHz to 4 MHz) or low-frequency (32.768 KHz) oscillation should be selected by code option
 - In RC mode, attention must be paid to the high/low frequency oscillation option, because the LCD driver frequency and the ROM code emulation time are related to this option.
- Sub-oscillator
 - Connect to 32768 Hz crystal only
 - Used in dual-clock operation
- Memory
 - 2048 x 16-bit program flash EEPROM (including 2K x 4-bit look-up table)
 - 128 x 4-bit data RAM (including 16 working registers)
 - 32 x 4 LCD data RAM
- 21 input/output pins
 - Ports for input only: 2 ports/8 pins
 - Input/output ports: 2 ports/8 pins
 - High sink current for LED driving: 1 port/4 pins
 - MFP output pin: 1 pin (MFP)
 - **Do not be floating when it is as input or output open-drain (NMOS type).**



- Power-down mode
 - Hold function: no operation (excluding main oscillator and sub-oscillator)
 - Stop function: no operation (excluding sub-oscillator)
 - Dual-clock slow operation mode: system is operated by the sub-oscillator (Fosc = Fs and Fm is stopped)
- Five types of interrupts
 - Four internal interrupts (Divider0, Divider1, Timer0, Timer1) for W541C260 body; three internal interrupts (Divider0, Timer0, Timer1) for W541C250 body.
 - One external interrupt (RC Port) for W541C260 body; two external interrupts (RC port and \overline{INT} pin) for W541C250 body.
- LCD driver output
 - 32 segment x 4 common
 - Static, 1/2 duty (1/2 bias), 1/3 duty (1/2 or 1/3 bias), 1/4 duty (1/3 bias) driving mode can be selected
 - LCD driver output pins can be used as DC output port by code option
 - Clock source can be main oscillator clock in the single-clock operation mode, or sub-oscillator clock in the dual-clock operation mode; operation mode is selected by code option
- MFP output pin
 - Output is software selectable as modulating or non-modulating frequency
 - Works as frequency output specified by Timer 1
- Two built-in 14-bit frequency dividers
 - Divider0: the clock source is the output of the main oscillator
 - Divider1: the clock source is the output of the sub-oscillator
- Two built-in 8-bit programmable countdown timers
 - Timer 0: one of two internal clock frequencies (Fosc/4 or Fosc/1024) can be selected
 - Timer 1: includes an auto-reload function and one of two internal clock frequencies (Fosc or Fosc/64) can be selected, or falling edge of pin RC.0 can be selected (output through MFP pin)
- Built-in 18/14-bit watchdog timer selectable for system reset
 - Enable/Disable the watchdog timer can be controlled by command or by option code; the control source (command or option code) can be determined by another option code
- Powerful instruction set: 115 instructions for W541C260 body
119 instructions for W541C250 body
- 8-level subroutine (include interrupt) nesting
- Up to 1 μ S instruction cycle (with 4 MHz operating frequency)
- Packaged in 80-pin QFP

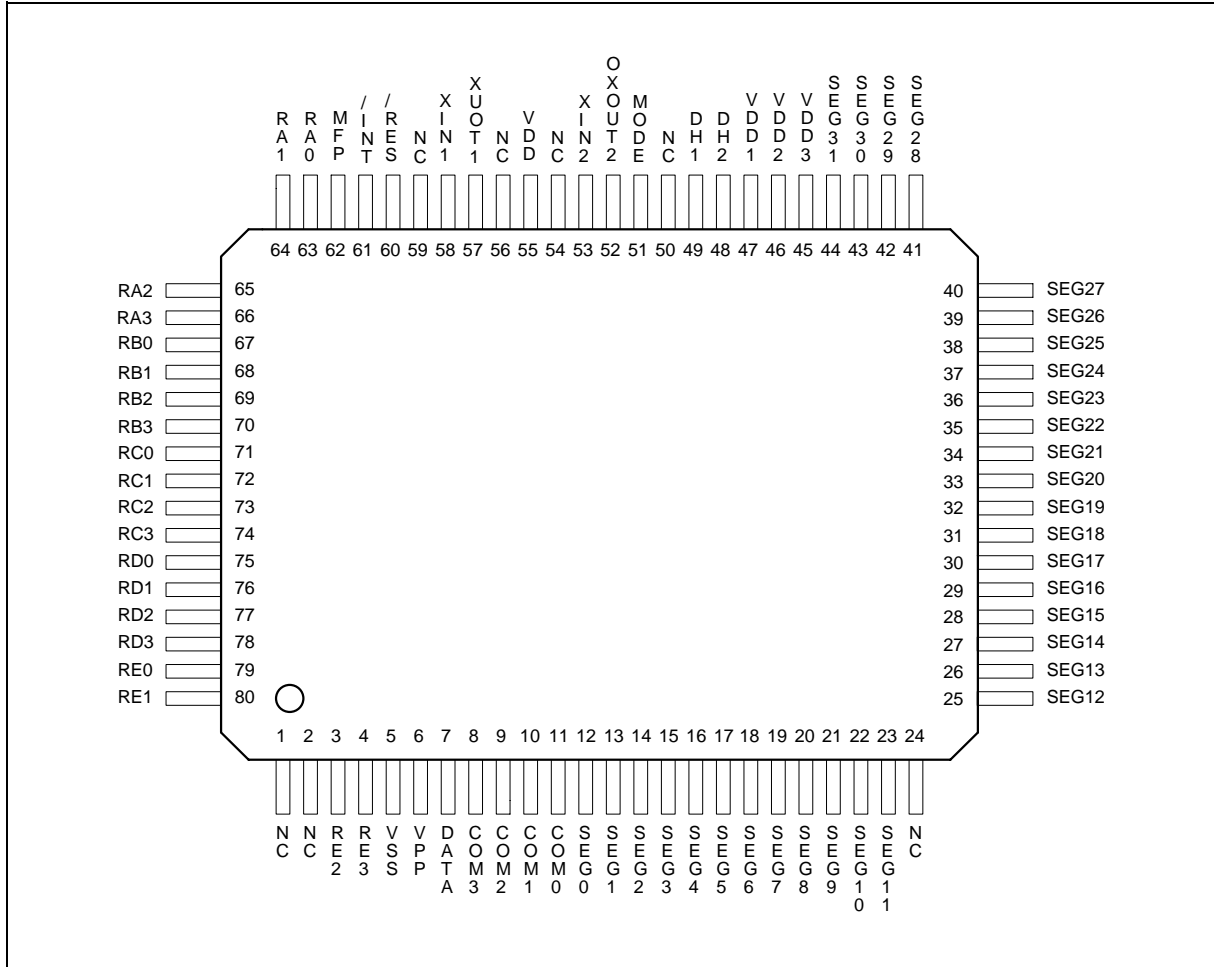


BLOCK DIAGRAM





PIN CONFIGURATION





PAD DESCRIPTION

SYMBOL	I/O	FUNCTION																									
XIN1	I	Input pin for oscillator. Connected to crystal or resistor to generate system clock by code option. External 10~20pF capacitor uses to get accurate freq in crystal mode.																									
XOUT1	O	Output pin for oscillator. Connected to crystal or resistor to generate system clock by code option. 20pF capacitor is built in internal for crystal mode.																									
XIN2	I	Input pin for sub-oscillator. Connected to 32.768 KHz crystal. External 10~20pF capacitor uses to get accurate freq in crystal mode.																									
XOUT2	O	Output pin for sub-oscillator with internal oscillation capacitor. Built-in 20pF capacitor. Connected to 32.768 KHz crystal.																									
RA0 to RA3	I/O	Input/Output port. Input/output mode specified by port mode 1 register (PM1).																									
RB0 to RB3	I/O	Input/Output port. Input/output mode specified by port mode 2 register (PM2).																									
RC0 to RC3	I	4-bit port for input only. Each pin has an independent interrupt capability.																									
RD0 to RD3	I	4-bit port for input only.																									
RE0 to RE3	O	Output port only. This port provides high sink current to drive LEDs.																									
MFP	O	Output pin only. This pin can output modulating or nonmodulating frequency, or Timer 1 clock output specified by mode register 1 (MR1).																									
$\overline{\text{RES}}$	I	System reset pin with pull-high resistor.																									
$\overline{\text{INT}}$	I	External interrupt pin with pull-high resistor. This pin is bonding option for the W541C250 body.																									
SEG0 to SEG31	O	LCD segment output pins. Also can be used as DC output ports specified by option codes.																									
COM0 to COM3	O	LCD common signal output pins. <table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th></th> <th>Static</th> <th>1/2 Duty</th> <th>1/3 Duty</th> <th>1/4 Duty</th> </tr> </thead> <tbody> <tr> <td>COM0</td> <td>Used</td> <td>Used</td> <td>Used</td> <td>Used</td> </tr> <tr> <td>COM1</td> <td>Not Used</td> <td>Used</td> <td>Used</td> <td>Used</td> </tr> <tr> <td>COM2</td> <td>Not Used</td> <td>Not Used</td> <td>Used</td> <td>Used</td> </tr> <tr> <td>COM3</td> <td>Not Used</td> <td>Not Used</td> <td>Not Used</td> <td>Used</td> </tr> </tbody> </table> <p>The LCD alternating frequency can be selected by code option.</p>		Static	1/2 Duty	1/3 Duty	1/4 Duty	COM0	Used	Used	Used	Used	COM1	Not Used	Used	Used	Used	COM2	Not Used	Not Used	Used	Used	COM3	Not Used	Not Used	Not Used	Used
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COM3	Not Used	Not Used	Not Used	Used																							
DH1, DH2	I	Connection terminals for voltage doubler (halver) capacitor.																									
VDD1, VDD2, VDD3	I	Positive (+) supply voltage terminal. Refer to Functional Description.																									



Pin description, continued

SYMBOL	I/O	FUNCTION
VDD	I	Positive power supply (+).
VSS	I	Negative power supply (-).
VPP	I	Voltage control pin for the flash EEPROM programming, erasing and verifying. This pin has a built-in pull-low resistor.
MODE	I	Mode selection pin for the flash EEPROM programming, erasing and verifying. This pin has a built-in pull-low resistor.
DATA	I/O	Data I/O pin for the flash EEPROM programming and verifying. This pin has a built-in pull-low resistor.

ABSOLUTE MAXIMUM RATINGS

PARAMETER	RATING	UNIT
Supply Voltage to Ground Potential	-0.3 to +7.0	V
Applied Input/Output Voltage	-0.3 to +7.0	V
Power Dissipation	120	mW
Ambient Operating Temperature	0 to +70	°C
Storage Temperature	-55 to +150	°C

Note: Exposure to conditions beyond those listed under Absolute Maximum Ratings may adversely affect the life and reliability of the device.

DC CHARACTERISTICS

(VDD-VSS = 3.0V, Fm = 4. MHz, Fs = 32.768 KHz, Ta = 25° C, LCD on; unless otherwise specified)

PARAMETER	SYM.	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Op. Voltage	VDD	-	2.4	-	5.5	V
Op. Current (Crystal type)	IOP1	No load (Ext-V) In dual-clock normal operation	-	0.6	2.5	mA
Op. Current (RC type)	IOP2	No load (Ext-V) In dual-clock normal operation	-	1	4	mA
Op. Current (Crystal type)	IOP3	No load (Ext-V) In dual-clock slow operation and Fm is stopped	-	8.5	20	μA
Hold Current (Crystal type)	IHM1	Hold mode No load (Ext-V) In dual-clock normal operation	-	280	450	μA

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Hold Current (RC type)	IHM2	Hold mode No load (Ext-V) In dual-clock normal operation	-	500	600	μA
Hold Current (Crystal type)	IHM3	Hold mode No load (Ext-V) In dual-clock slow operation and Fm is stopped	-	4.0	6	μA
Stop Current (Crystal type)	ISM1	Stop mode No load (Ext-V) In dual-clock normal operation	-	4.0	6	μA
Stop Current (Crystal type)	ISM2	Stop mode No load (Ext-V) In single-clock operation	-	0.1	2	μA
Input Low Voltage	V _{IL}	-	V _{SS}	-	0.3 V _{DD}	V



DC Characteristics, continued

PARAMETER	SYM.	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Input High Voltage	V _{IH}	-	0.7 V _{DD}	-	V _{DD}	V
MFP Output Low Voltage	V _{ML}	I _{OL} = 3.5 mA	-	-	0.4	V
MFP Output High Voltage	V _{MH}	I _{OH} = 3.5 mA	2.4	-	-	V
Port RA, RB Output Low Voltage	V _{ABL}	I _{OL} = 2.0 mA	-	-	0.4	V
Port RA, RB Output high Voltage	V _{ABH}	I _{OH} = 2.0 mA	2.4	-	-	V
LCD Supply Current	I _{LCD}	All Seg. ON	-	-	6	μA
SEG0-SEG31 Sink Current (Used as LCD output)	I _{OL1}	V _{OL} = 0.4V V _{LCD} = 0.0V	0.4	-	-	μA
SEG0-SEG31 Drive Current (Used as LCD output)	I _{OH1}	V _{OH} = 2.4V V _{LCD} = 3.0V	0.3	-	-	μA
Segment output low voltage (Used as DC output)	V _{SL}	I _{OL} = 0.6 mA	-	-	0.4	V
Segment output high voltage (Used as DC output)	V _{SH}	I _{OH} = 3 μA	2.4	-	-	V
Port RE Sink Current	I _{EL}	V _{OL} = 0.9V	9	-	-	mA
Port RE Source Current	I _{EH}	V _{OH} = 2.4V	0.4	1.2	-	mA
Input Port Pull-up Resistor	R _{CD}	Port RC, RD	100	300	500	KΩ
$\overline{\text{INT}}$ Pull-up Resistor	R _{INT}	-	50	150	500	KΩ
$\overline{\text{RES}}$ Pull-up Resistor	R _{RES}	-	50	150	500	KΩ
V _{PP} Pull-down Resistor	R _{VPP}	V _{DD} = 5V	500	1000	1500	KΩ
MODE Pull-down Resistor	R _{MODE}	V _{DD} = 5V	500	1000	1500	KΩ
DATA Pull-down Resistor	R _{DATA}	V _{DD} = 5V	20	65	150	KΩ



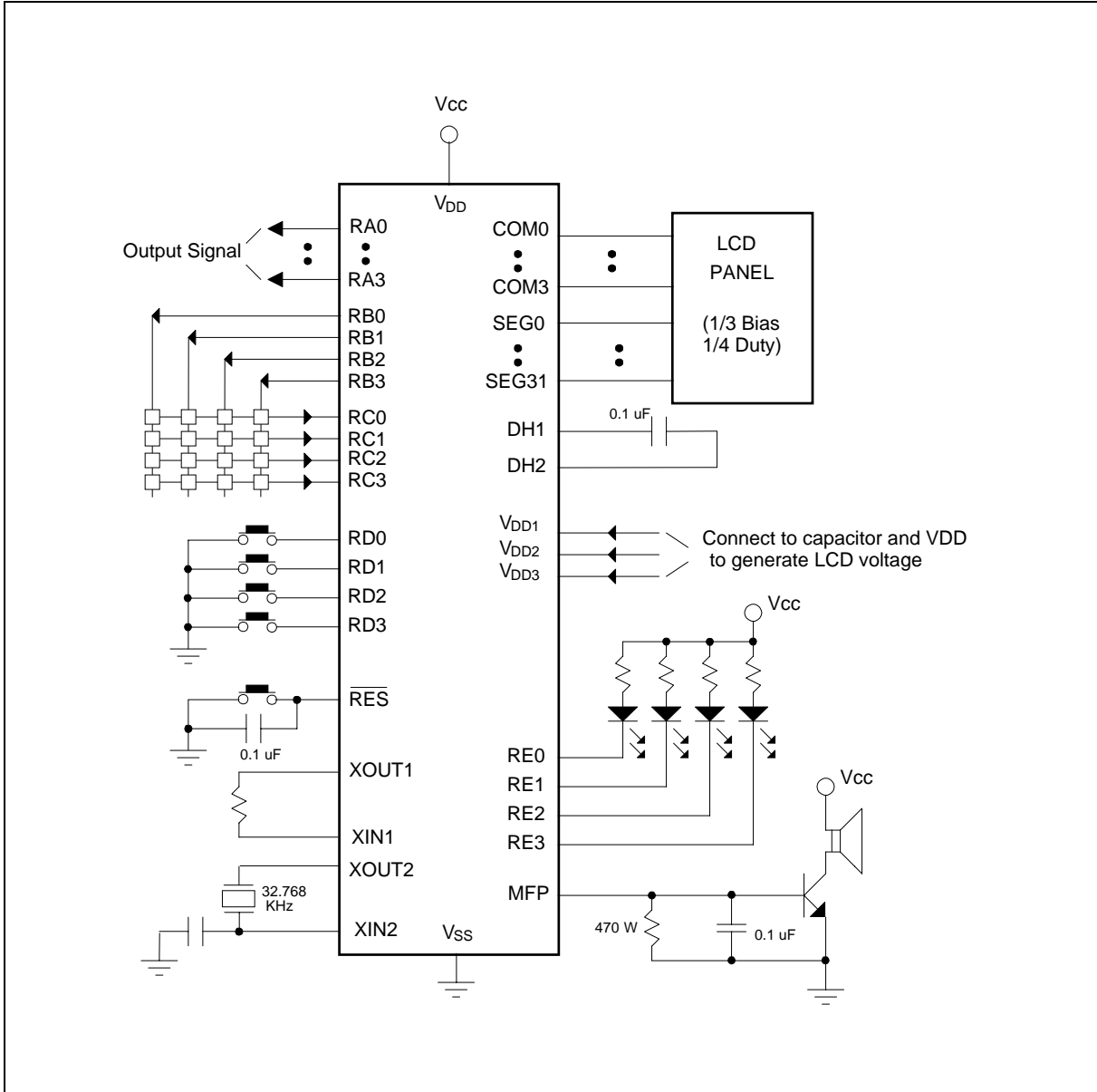
AC CHARACTERISTICS

(V_{DD}-V_{SS} = 3.0V, T_a = 25° C; unless otherwise specified)

PARAMETER	SYM.	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Op. Frequency	F _{OSC}	RC type	-	-	4000	KHz
		Crystal type 1 (Option low speed type)	-	32.768	-	
		Crystal type 2 (Option high speed type)	400	-	3580	
Frequency Deviation by Voltage Drop for RC Oscillator (low freq.)	$\frac{\Delta f}{f}$	$\frac{f(3V) - f(2.4V)}{f(3V)}$	-	-	10	%
Instruction Cycle Time	T _I	One machine cycle	-	4/F _{OSC}	-	mS
Reset Active Width	T _{RAW}	F _{OSC} = 32.768 KHz	1	-	-	μS
Interrupt Active Width	T _{IAW}	F _{OSC} = 32.768 KHz	1	-	-	μS



TYPICAL APPLICATION CIRCUIT





PACKAGE DIMENSIONS

80-Lead QFP

