ELECTRONICS, INC.
44 FARRAND STREET
BLOOMFIELD, NJ 07003
(973) 748–5089

NTE2373
MOSFET
P–Ch, Enhancement Mode
High Speed Switch

Features:

- Dynamic dv/dt Rating
- Repetitive Avalanche Rated
- Fast Switching
- Ease of Paralleling
- Simple Drive Requirements

Absolute Maximum Ratings:

Continuous Drain Current (V_{GS} = 10V), I_D
- T_C = +25°C 11A
- T_C = +100°C 6.8A

Pulsed Drain Current (Note 1), I_{DM} 44A

Power Dissipation (T_C = +25°C), P_D 125W
- Derate Linearly Above 25°C . . . . . 1.0W/°C

Gate–to–Source Voltage, V_{GS} . . . . . ±20

Single Pulse Avalanche Energy (Note 2), E_{AS} 700mJ

Avalanche Current (Note 1), I_{AR} 11A

Repetitive Avalanche Energy (Note 1), E_{AR} 13mJ

Peak Diode Recovery dv/dt (Note 3), dv/dt 5.0V/ns

Operating Junction Temperature Range, T_J . . . . . –55° to +150°C

Storage Temperature Range, T_{stg} . . . . . –55° to +150°C

Lead Temperature (During Soldering, 1.6mm from case for 10sec), T_L . . . . . +300°C

Mounting Torque (6–32 or M3 Screw) . . . . . 10 lbf•in (1.1N•m)

Thermal Resistance, Junction–to–Case, R_{thJC} . . . . . 1.0°C/W

Thermal Resistance, Junction–to–Ambient, R_{thJA} . . . . . 62°C/W

Typical Thermal Resistance, Case–to–Sink (Flat, Greased Surface), R_{thCS} . . . . . 0.5°C/W

Note 1. Repetitive rating; pulse width limited by maximum junction temperature.

Note 2. V_{DD} = 50V, starting T_J = +25°C, L = 8.7mH, R_G = 25Ω, I_{AS} = 11A

Note 3. $I_{SD} \le 11A$, di/dt ≤ 150A/μs, $V_{DD} \le V_{(BR)DSS}$, $T_J \le +150°C$

Note 4. Pules Width ≤ 300μs, Duty Cycle ≤ 2%.

Electrical Characteristics: (T_J = +25°C unless otherwise specified)

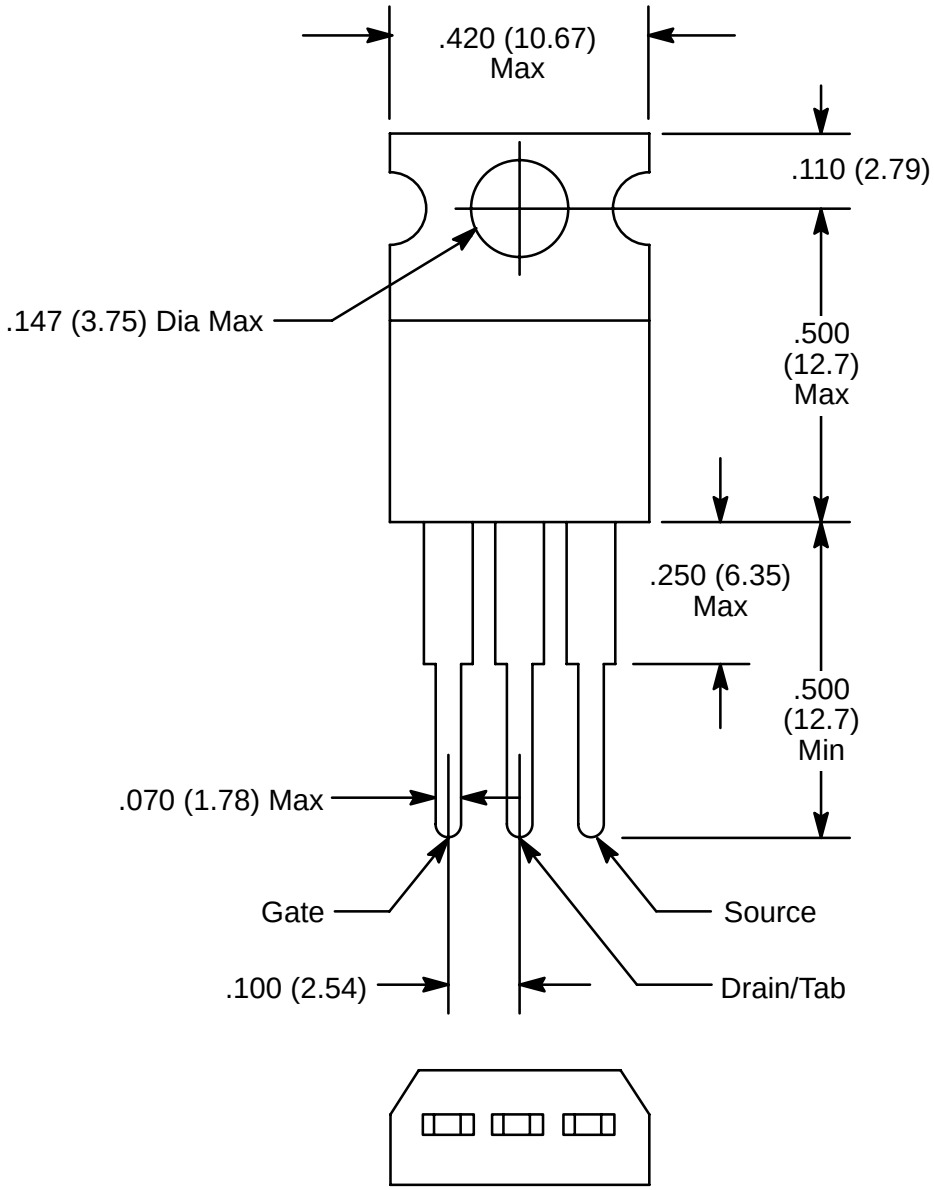
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain–to–Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	200	–	–	V
Breakdown Voltage Temp. Coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_J}$	Reference to +25°C, I_D = 1mA	–	0.20	–	V/°C
Static Drain–to–Source On–Resistance	$R_{DS(on)}$	V_{GS} = 10V, I_D = 6.6A, Note 4	–	–	0.50	Ω
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	2.0	–	4.0	V
Forward Transconductance	g_{fs}	V_{DS} = 50V, I_D = 6.6A, Note4	4.1	–	–	mhos
Drain–to–Source Leakage Current	I_{DSS}	V_{DS} = 200V, V_{GS} = 0V	–	–	100	μA
		V_{DS} = 160V, V_{GS} = 0V, T_J = +125°C	–	–	500	μA
Gate–to–Source Forward Leakage	I_{GSS}	V_{GS} = –20V	–	–	–100	nA
Gate–to–Source Reverse Leakage	I_{GSS}	V_{GS} = 20V	–	–	100	nA
Total Gate Charge	Q_g	I_D = 11A, V_{DS} = 160V, V_{GS} = 10V, Note 4	–	–	44	nC
Gate–to–Source Charge	Q_{gs}		–	–	7.1	nC
Gate–to–Drain (“Miller”) Charge	Q_{gd}		–	–	27	nC
Turn–On Delay Time	$t_{d(on)}$	V_{DD} = 100V, I_D = 11A, R_G = 9.1Ω, R_D = 8.6Ω, Note 4	–	14	–	ns
Rise Time	t_r		–	43	–	ns
Turn–Off Delay Time	$t_{d(off)}$		–	39	–	ns
Fall Time	t_f		–	38	–	ns
Internal Drain Inductance	L_D	Between lead, .250in. (6.0) mm from package and center of die contact	–	4.5	–	nH
Internal Source Inductance	L_S		–	7.5	–	nH
Input Capacitance	C_{iss}	V_{GS} = 0V, V_{DS} = 25V, f = 1MHz	–	1200	–	pF
Output Capacitance	C_{oss}		–	370	–	pF
Reverse Transfer Capaticance	C_{rss}		–	81	–	pF

Source–Drain Ratings and Characteristics:

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Continuous Source Current (Body Diode)	I_S		–	–	11	A
Pulsed Source Current (Body Diode)	I_{SM}	Note 1	–	–	44	A
Diode Forward Voltage	V_{SD}	T_J = +25°C, I_S = 11A, V_{GS} = 0V, Note 4	–	–	5.0	V
Reverse Recovery Time	t_{rr}	T_J = +25°C, I_F = 11A, di/dt = 100A/μs, Note 4	–	250	300	ns
Reverse Recovery Charge	Q_{rr}		–	2.9	3.6	μC
Forward Turn–On Time	t_{on}	Intrinsic turn–on time is neglegible (turn–on is dominated by L_S+L_D)				

Note 1. Repetitive rating; pulse width limited by maximum junction temperature.

Note 4. Pulse width ≤ 300μs; duty cycle ≤ 2%.


.420 (10.67)
Max
.110 (2.79)
.147 (3.75) Dia Max
.500
(12.7)
Max
.250 (6.35)
Max
.500
(12.7)
Min
.070 (1.78) Max
Gate
Source
.100 (2.54)
Drain/Tab