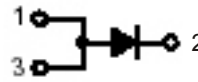


CDBD835L

Reverse Voltage: 35 Volts
Forward Current: 8.0 Amp

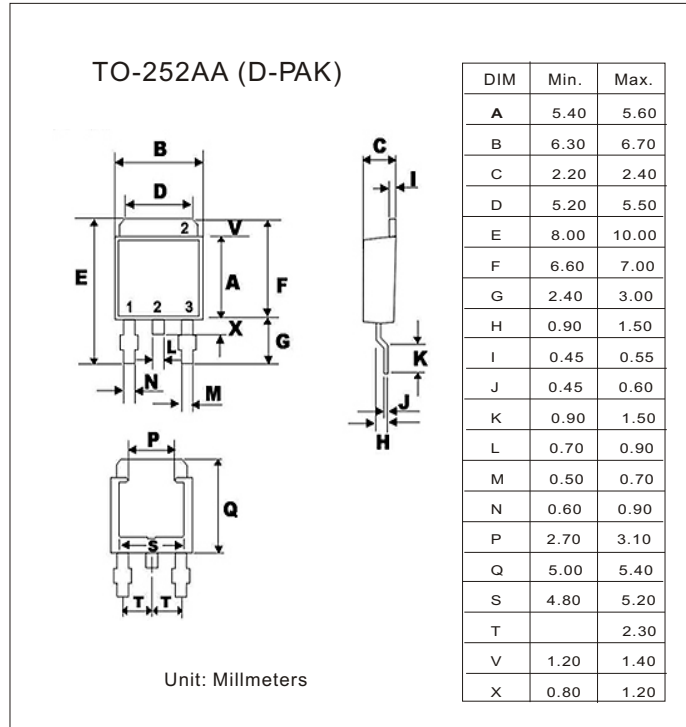


Features

- Lead formed for surface mount
- Easy pick and place
- Plastic package has Underwriters Lab. flammability classification 94V-0
- Low Switching Noise
- Low forward voltage drop

Mechanical data

- Case: TO-252AA molded plastic
- Terminals: solderable per MIL-STD-750, method 2026
- Mounting position: Any
- Approx. Weight: 0.295 gram



Maximum Ratings and Electrical Characteristics

Parameter	Symbol	CDBD835	Unit
Max.Repetitive Peak Reverse Voltage	V _{RRM}	35	V
Max. DC Blocking Voltage	V _{DC}	35	V
Max. RMS Voltage	V _{RMS}	24.5	V
Peak Surge Forward Current 8.3ms single halfsine-wave superimposed on rateload (JEDEC method)	I _{FSM}	75	A
Average Rectifier Forward Current (Note 1)	I _{F(AV)}	8.0	A
Peak Repetitive Foward Current (at Rated V _R , Square Wave, 20KHz, T _c =80°C)	I _{FRM}	16	A
Max. Instantaneous Forward Current at 8.0A (Note 2)	V _F	0.5	V
Max. DC Reverse Current at Rated DC Blocking Voltage T _a =25°C T _a =100°C	I _R	1.4 35	mA
Max. Thermal Resistance (Note 3)	R _{θJA} R _{θJC}	80 6.0	°C/W
Operating Junction temperature	T _j	-65 to +125	°C
Storage Temperature	T _{STG}	-65 to +150	°C

Note 1. Total device Rated V_p at T_c=100°C

2. Pulse width= 300uS, duty cycle less than 2%.

3. Thermal resistance from junction to ambient and junction to case mounted on minimum pad size recommended

Rating and Characteristic Curves (CDBD835L)

Fig.1 - Reverse Characteristics

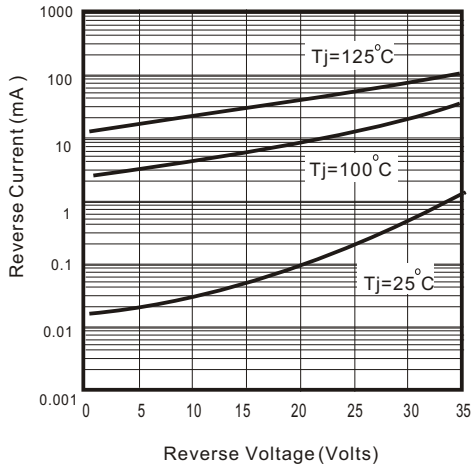


Fig. 2 - Forward Characteristics

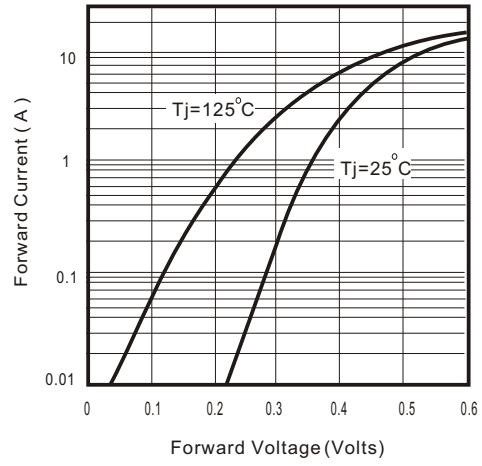


Fig. 3 - Junction Capacitance

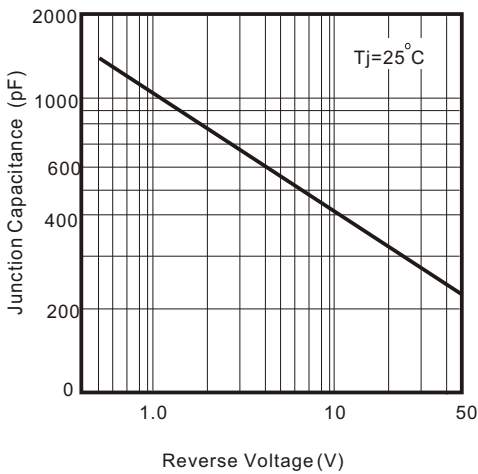


Fig. 4 - Current Derating Curve

