



ELECTRICAL SPECIFICATIONS:

- |  |   |
|--|---|
| 1.0 TURNS RATIO (P1-P3-P2) : (J3-J6)                   | : 1CT : 1CT ± 3%                          |
| (P7-P6-P8) : (J1-J2)                                   | : 1CT : 1CT ± 3%                          |
| 2.0 INDUCTANCE (P7-P8)                                 | : 350uH MIN. @ 0.1V , 100KHz, 8mA DC Bias |
| (P1-P2)  | : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias  |
| 3.0 LEAKAGE INDUCTANCE P1-P3-P2 (WITH J6 AND J3 SHORT) | : 0.3uH MAX. @ 1MHz                       |
| P7-P6-P8 (WITH J2 AND J1 SHORT)                        | : 0.3uH MAX. @ 1MHz                       |
| 4.0 INTERWINDING CAPACITANCE (P1,P3,P2) TO (J6,J3)     | : 30pf TYP @ 1MHZ                         |
| (P8,P6,P7) TO (J2,J1)                                  | : 30pf TYP @ 1MHZ                         |
| 5.0 DC RESISTANCE (J6-J3)=(J2-J1)                      | : 1.2 ohms Max.                           |

NOTES

- 1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.
- 2.0 ALL RESISTORS ARE ±5% TOLERANCE.

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6.0 RETURN LOSS:  $\langle P7-P8 \rangle = 100 \text{ } \Omega \text{HMS}$  AND  $\langle P1-P2 \rangle = 100 \text{ } \Omega \text{HM REF.}$   
 1MHz TO 30MHz : -18dB MIN.  
 30MHz TO 60MHz :  $-(19-20 \text{ LOG } (f/30\text{MHz}))$   
 60MHz TO 80MHz : -12dB MIN.

NOTE: 100  $\Omega \text{HMS}$  CONNECTED TO  $\langle J2-J1 \rangle$  OR  $\langle J6-J3 \rangle$ .



7.0 VOLTAGE WITHSTAND:  
 $\langle J1, J2 \rangle$  TO  $\langle P7, P8 \rangle$  : 1500 VAC  
 $\langle J3, J6 \rangle$  TO  $\langle P1, P2 \rangle$  : 1500 VAC

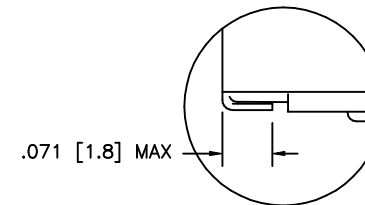
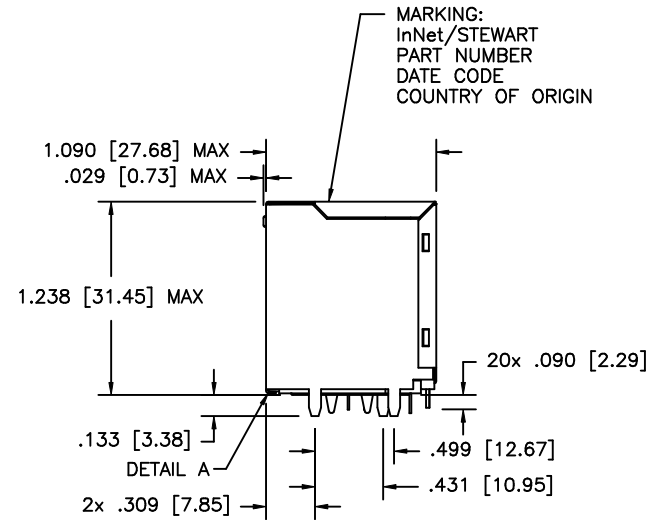
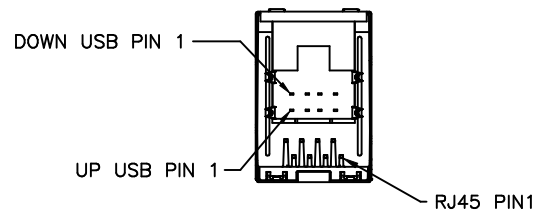
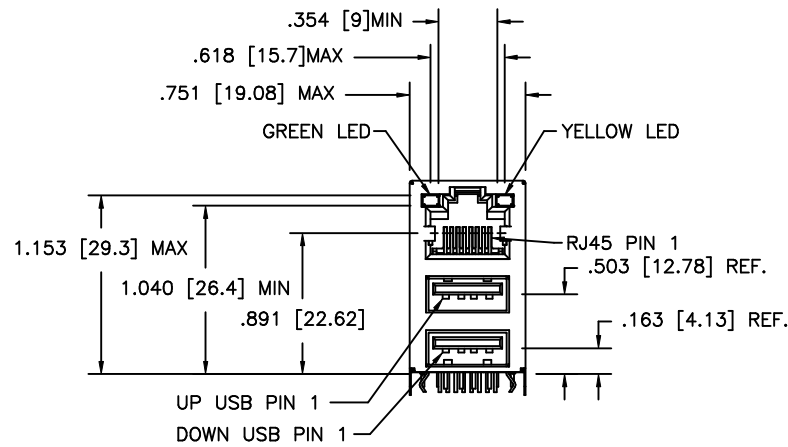
8.0 INSERTION LOSS:  $R_S = R_L = 100 \text{ ohms}$   
 1-65MHz -1 dB MAX

9.0 RISE TIME:  $R_S = 100 \text{ } \Omega \text{HMS}$  AND  $R_L = 100 \text{ } \Omega \text{HMS}$   
 OUTPUT VOLTAGE = 1 V peak 3.0 nS MAX  
 PULSE WIDTH= 112nS 3.0 nS MAX

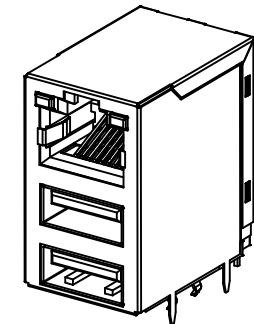
10.0 CROSS TALK:  
 1-65MHz -35 dB MIN

11.0 COMMON TO COMMON MODE ATTENUATION:  
 30MHz TO 100MHz -30dB MAX  
 100MHz TO 130MHz -20dB MAX

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DETAIL A  
SCALE: 5=1

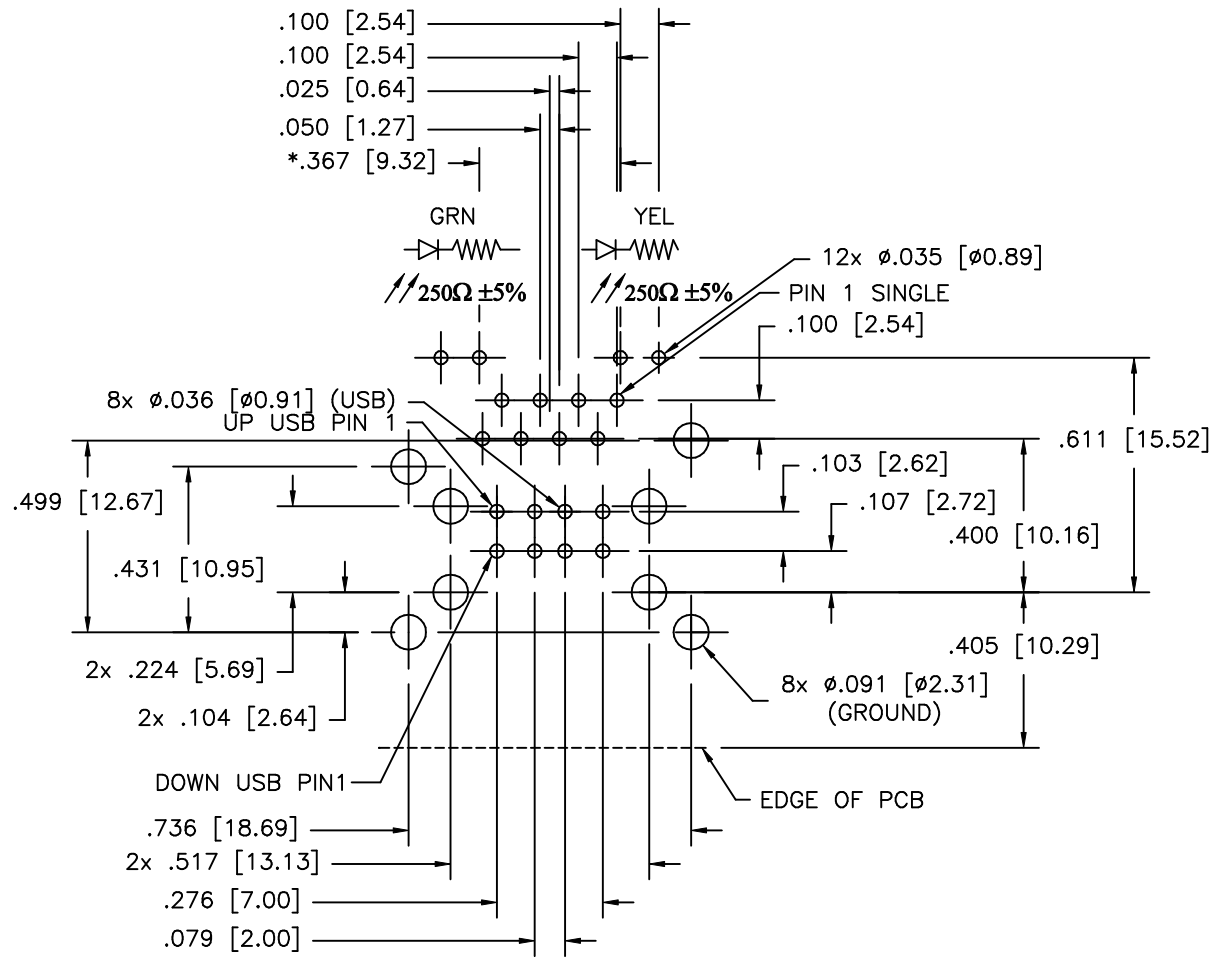


PRELIMINARY  
DRAWING

NOTES:



- 1.0 TOLERANCE COMPLY WITH FCC DIMENSION REQUIREMENTS.
- 2.0 PIN NOT ELECTRICALLY CONNECTED MAY BE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- 3.0 MATERIAL:
  - PLASTIC: UL94V-Ø
  - TERMINAL USB: PdNi AT 30 MICROINCHES
  - TERMINAL RJ45: 50 MICROINCHES Au OVER 50 MICROINCHES Ni OVER PHOSBRONZE SHIELD, BOTH USB AND RJ45: BRASS PLATED W/EITHER Ni OR TIN-LEAD
- 4.0 GENERAL TOLERANCE: ±.005 [.127]

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PC BOARD LAYOUT COMPONENT SIDE SHOWN  
 TOLERANCE: ±.003 [.08] UNLESS OTHERWISE SPECIFIED

PRELIMINARY  
 DRAWING

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