



SANYO Semiconductors

DATA SHEET

2SC5979

 — NPN Epitaxial Planar Silicon Transistor
High-Current Switching Applications

Applications

- DC / DC converter, relay drivers, lamp drivers, motor drivers, flash.

Features

- Adoption of FBET, MBIT process.
- High current capacitance.
- Low collector-to-emitter saturation voltage.
- High-speed switching.
- Narrow hFE width.
- High allowable power dissipation.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CB0}		100	V
Collector-to-Emitter Voltage	V _{CES}		100	V
Collector-to-Emitter Voltage	V _{CEO}		50	V
Emitter-to-Base Voltage	V _{EBO}		6	V
Collector Current	I _C		5	A
Collector Current (Pulse)	I _{CP}		7.5	A
Base Current	I _B		1.2	A
Collector Dissipation	P _C		0.8	W
		T _c =25°C	15	W
Junction Temperature	T _J		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	I _{CB0}	V _{CB} =40V, I _E =0			0.1	μA
Emitter Cutoff Current	I _{EBO}	V _{EB} =4V, I _C =0			0.1	μA
DC Current Gain	h _{FE}	V _{CE} =2V, I _C =500mA	250		400	

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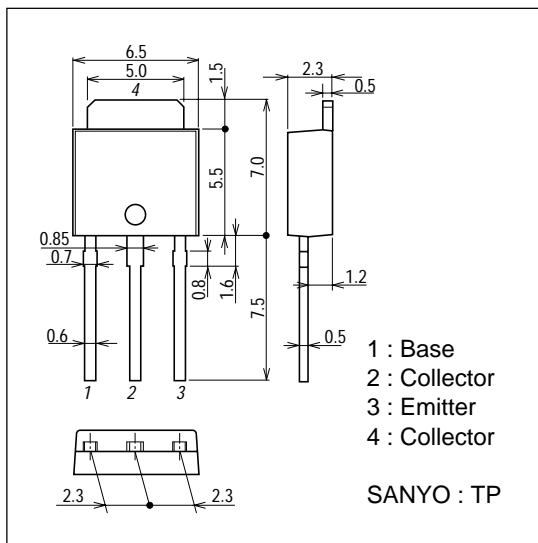
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Gain-Bandwidth Product	f_T	$V_{CE}=10V, I_C=500mA$		400		MHz
Output Capacitance	C_{ob}	$V_{CB}=10V, f=1MHz$		15		pF
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C=1A, I_B=50mA$		70	105	mV
		$I_C=2A, I_B=100mA$		120	180	mV
Base-to-Emitter Saturation Voltage	$V_{BE(sat)}$	$I_C=2A, I_B=100mA$		0.88	1.2	V
Collector-to-Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C=10\mu A, I_E=0$	100			V
Collector-to-Emitter Breakdown Voltage	$V_{(BR)CES}$	$I_C=100\mu A, R_{BE}=0$	100			V
Collector-to-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C=1mA, R_{BE}=\infty$	50			V
Emitter-to-Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E=10\mu A, I_C=0$	6			V
Turn-ON Time	t_{on}	See specified Test Circuit.		35		ns
Storage Time	t_{stg}	See specified Test Circuit.		300		ns
Fall Time	t_f	See specified Test Circuit.		20		ns

Package Dimensions

unit : mm

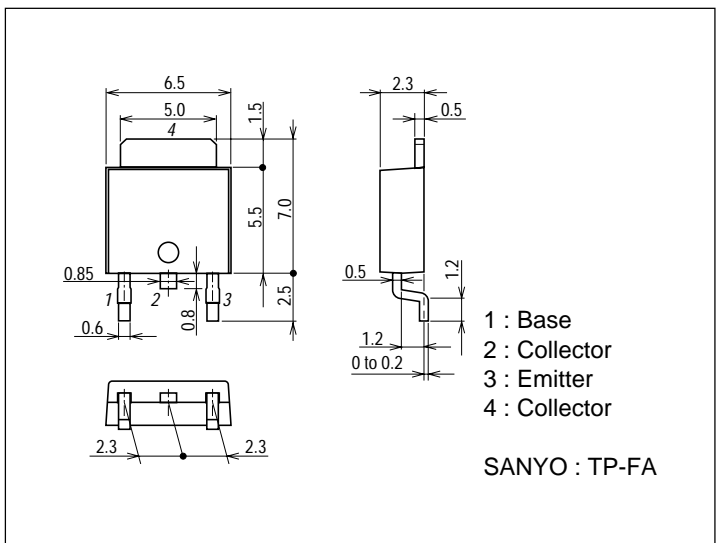
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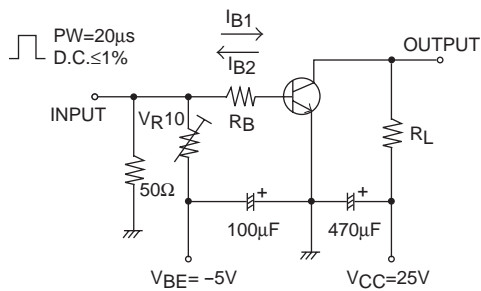
Package Dimensions

unit : mm

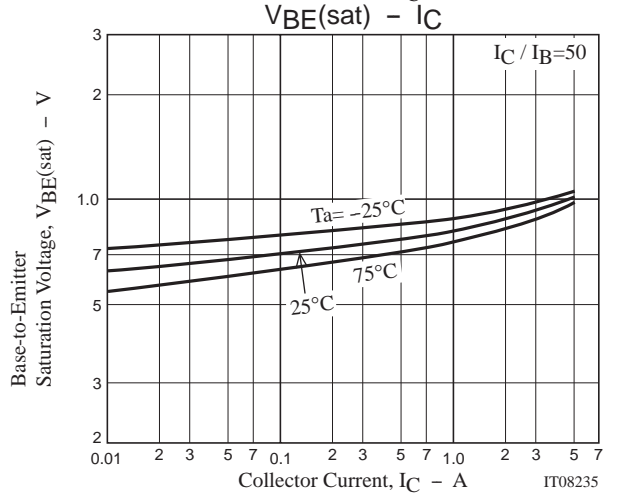
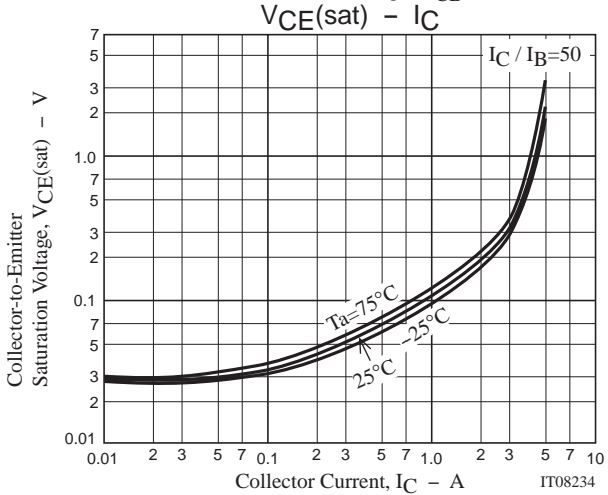
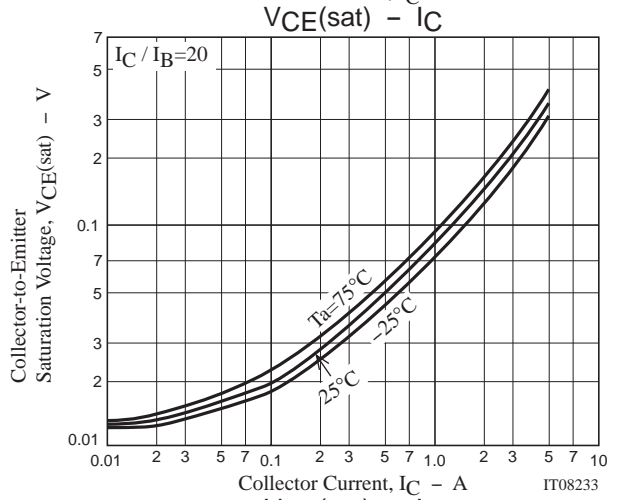
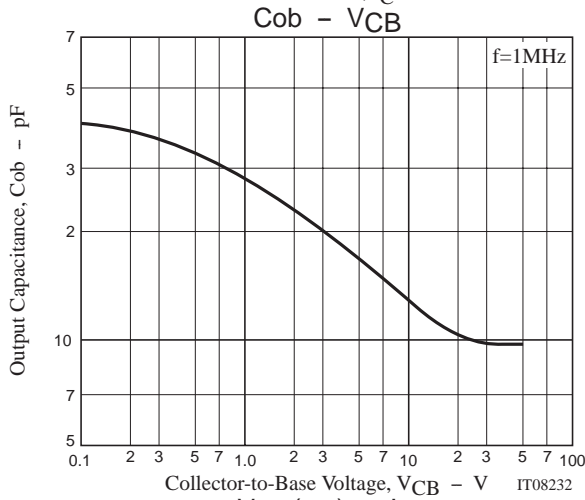
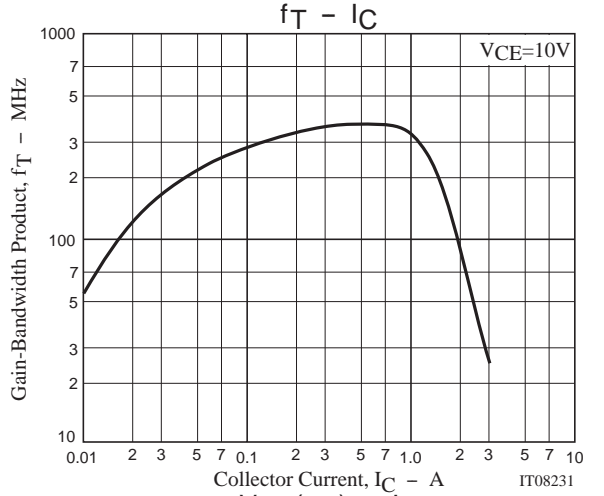
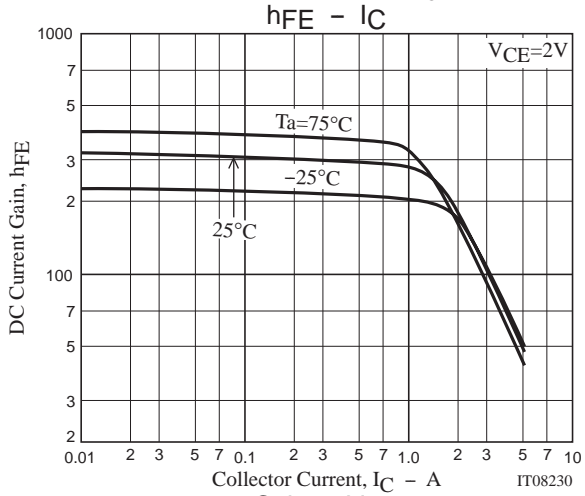
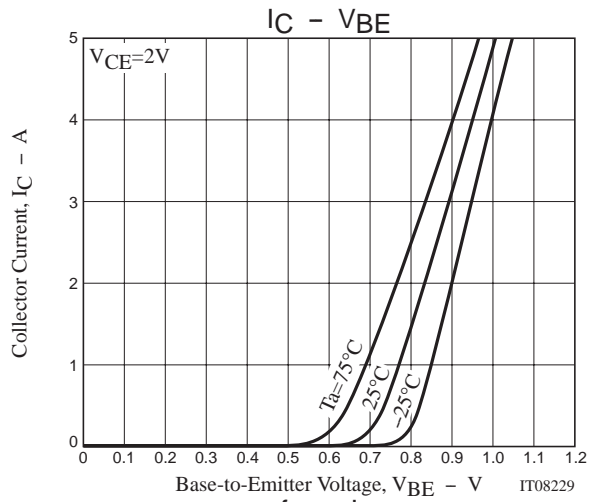
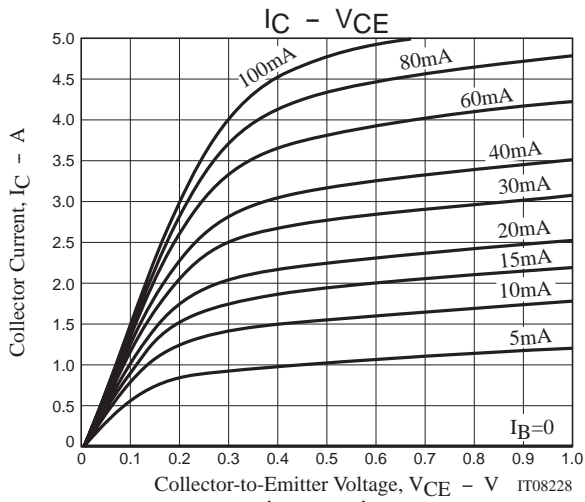
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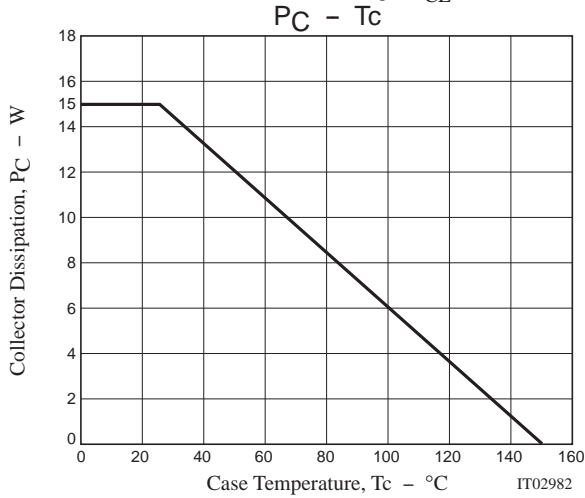
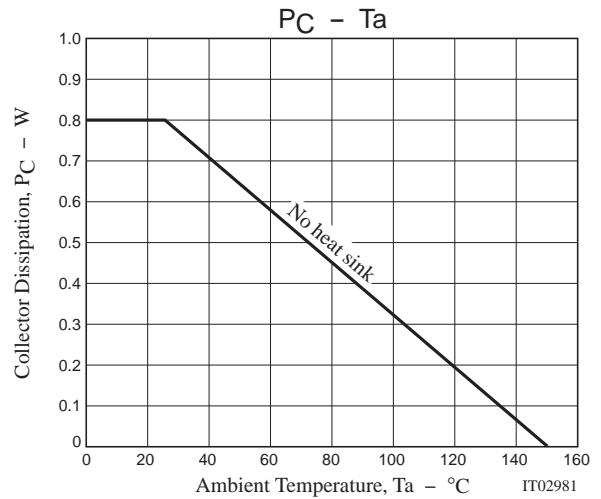
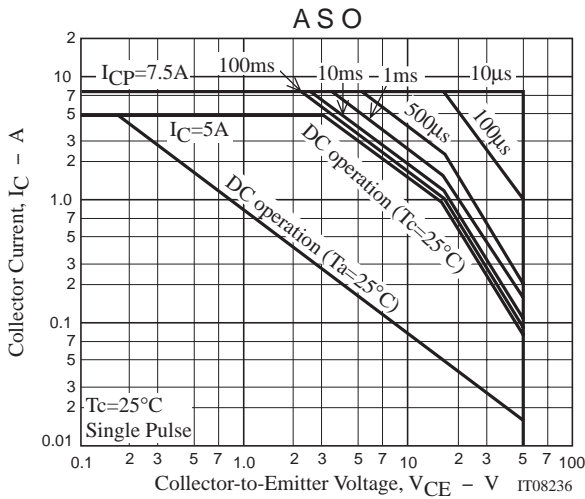


Switching Time Test Circuit



$$I_C = 10I_{B1} = -10I_{B2} = 1A$$





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