XN0111H (XN111H)

Silicon PNP epitaxial planer transistor

For switching/digital circuits

Features

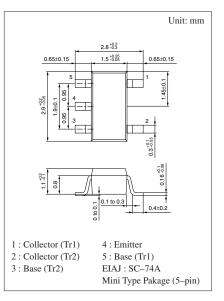
- Two elements incorporated into one package. (Emitter-coupled transistors with built-in resistor)
- Reduction of the mounting area and assembly cost by one half.

Basic Part Number of Element

• UNR111H(UN111H) × 2 elements

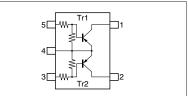
Parameter		Symbol	Ratings	Unit
Rating of element	Collector to base voltage	V _{CBO}	-50	V
	Collector to emitter voltage	V _{CEO}	-50	V
	Collector current	I _C	-100	mA
Overall	Total power dissipation	P _T	300	mW
	Junction temperature	Tj	150	°C
	Storage temperature	T _{stg}	-55 to +150	°C

Absolute Maximum Ratings (Ta=25°C)



Marking Symbol: 9X

Internal Connection



Parameter Symbol Conditions min max typ Collector to base voltage V_{CBO} $I_{C} = -10\mu A, I_{E} = 0$ -50 Collector to emitter voltage V_{CEO} $I_{C} = -2mA, I_{B} = 0$ -50 $V_{CB} = -50V, I_E = 0$ I_{CBO} -0.1Collector cutoff current $V_{CE} = -50V, I_B = 0$ -0.5 I_{CEO} Emitter cutoff current $V_{EB} = -6V, I_C = 0$ -0.5 I_{EBO} Forward current transfer ratio $V_{CE} = -10V, I_C = -5mA$ 30 h_{FE} $V_{CE} = -10V, I_{C} = -5mA$ hFE (small/large)*1 0.5 0.99 Forward current transfer h_{FE} ratio $I_{C} = -10mA$, $I_{B} = -0.3mA$ -0.25 Collector to emitter saturation voltage V_{CE(sat)} $V_{CC} = -5V, V_B = -0.5V, R_L = 1k\Omega$ Output voltage high level VOH -4.9 $V_{CC} = -5V, V_B = -2.5V, R_L = 1k\Omega$ Output voltage low level VOL -0.2 $V_{CB} = -10V, I_E = 1mA, f = 200MHz$ Transition frequency 80 f_T Input resistance R_1 -30% 2.2 +30% Resistance ratio R_{1}/R_{2} 0.17 0.22 0.27

Electrical Characteristics (Ta=25°C)

*1 Ratio between 2 elements

Note.) The Part number in the Parenthesis shows conventional part number.

Unit

V

V

μΑ

μΑ

mA

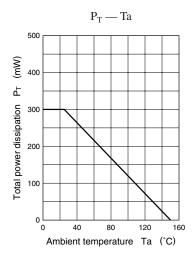
v

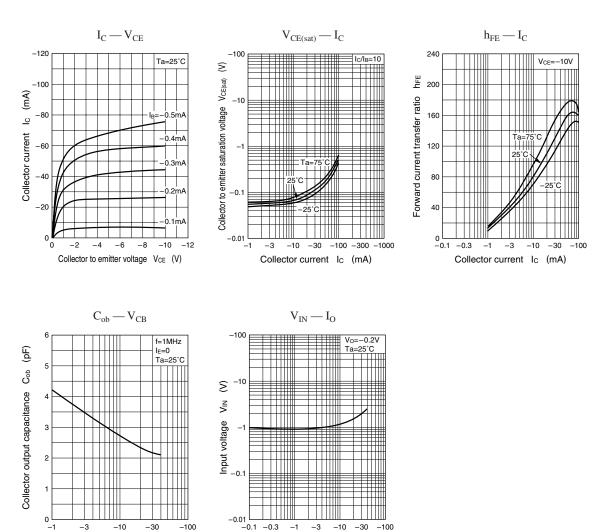
V

V

MHz

kΩ





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Output current Io (mA)

-1

Collector to base voltage V_{CB} (V)

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