

2SK1620L, 2SK1620S

Silicon N-Channel MOS FET

Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator, DC-DC converter and motor driver

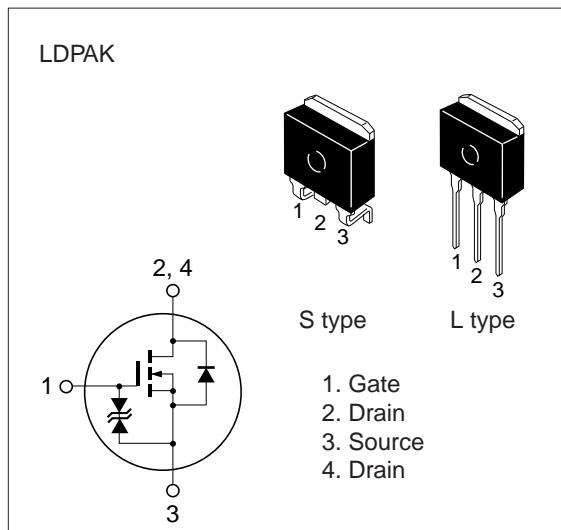


Table 1 Absolute Maximum Ratings (Ta = 25°C)

| Item | Symbol | Ratings | Unit |
|---|-------------------------|-------------|------|
| Drain to source voltage | V _{DSS} | 150 | V |
| Gate to source voltage | V _{GSS} | ±20 | V |
| Drain current | I _D | 10 | A |
| Drain peak current | I _{D(pulse)} * | 40 | A |
| Body to drain diode reverse drain current | I _{DR} | 10 | A |
| Channel dissipation | P _{ch} ** | 50 | W |
| Channel temperature | T _{ch} | 150 | °C |
| Storage temperature | T _{stg} | -55 to +150 | °C |

* PW ≤ 10 µs, duty cycle ≤ 1 %

** Value at T_C = 25 °C

Table 2 Electrical Characteristics ($T_a = 25^\circ\text{C}$)

| Item | Symbol | Min | Typ | Max | Unit | Test Conditions |
|--|-----------------------------|----------|------|----------|---------------|---|
| Drain to source breakdown voltage | $V_{(\text{BR})\text{DSS}}$ | 150 | — | — | V | $I_D = 10 \text{ mA}, V_{GS} = 0$ |
| Gate to source breakdown voltage | $V_{(\text{BR})\text{GSS}}$ | ± 20 | — | — | V | $I_G = \pm 100 \mu\text{A}, V_{DS} = 0$ |
| Gate to source leak current | I_{GSS} | — | — | ± 10 | μA | $V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$ |
| Zero gate voltage drain current | I_{DSS} | — | — | 250 | μA | $V_{DS} = 120 \text{ V}, V_{GS} = 0$ |
| Gate to source cutoff voltage | $V_{GS(\text{off})}$ | 2.0 | — | 4.0 | V | $I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$ |
| Static Drain to source on state resistance | $R_{DS(\text{on})}$ | — | 0.12 | 0.15 | Ω | $I_D = 5 \text{ A}, V_{GS} = 10 \text{ V}^*$ |
| Forward transfer admittance | $ y_{fs} $ | 4.0 | 7.0 | — | S | $I_D = 5 \text{ A}, V_{DS} = 10 \text{ V}^*$ |
| Input capacitance | C_{iss} | — | 1200 | — | pF | $V_{DS} = 10 \text{ V}, V_{GS} = 0,$ |
| Output capacitance | C_{oss} | — | 550 | — | pF | $f = 1 \text{ MHz}$ |
| Reverse transfer capacitance | C_{rss} | — | 85 | — | pF | |
| Turn-on delay time | $t_{d(\text{on})}$ | — | 20 | — | ns | $I_D = 5 \text{ A}, V_{GS} = 10 \text{ V},$ |
| Rise time | t_r | — | 50 | — | ns | $R_L = 6 \Omega$ |
| Turn-off delay time | $t_{d(\text{off})}$ | — | 70 | — | ns | |
| Fall time | t_f | — | 40 | — | ns | |
| Body to drain diode forward voltage | V_{DF} | — | 1.2 | — | V | $I_F = 10 \text{ A}, V_{GS} = 0$ |
| Body to drain diode reverse recovery time | t_{rr} | — | 220 | — | ns | $I_F = 10 \text{ A}, V_{GS} = 0,$ $dI_F/dt = 50 \text{ A}/\mu\text{s}$ |

* Pulse Test

See characteristic curves of 2SK740.

